

Description

The AP80P10P/T uses advanced APM-SGT V technology

to provide excellent $R_{\text{DS}(\text{ON})}$, low gate charge and

operation with gate voltages as low as 4.5V.

This device is suitable for use as a Battery protection

or in other Switching application.

General Features

 $V_{DS} = -100V I_{D} = -80A$

 $R_{DS(ON)}$ <25m Ω @ V_{GS} =10V (Type: 19m Ω)

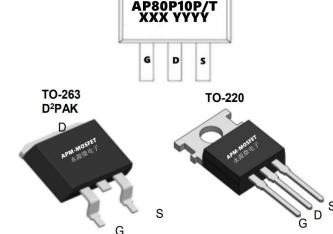
Ciss≈4230 PF

Application

Brushless motor

Load switch

Uninterruptible power supply



Package Marking and Ordering Information

Product ID	Pack	Marking	Qty(PCS)
AP80P10T	TO-263-3L	AP80P10T XXX YYYY	800
AP80P10P	TO-220-3L	AP80P10P XXX YYYY	1000

Absolute Maximum Ratings (Tc=25°Cunless otherwise noted)

Symbol	Parameter	Rating	Units
V _D s	Drain-Source Voltage	-100	V
Vgs	Gate-Source Voltage	±20	V
I _D @T _C =25°C	Continuous Drain Current, V _{GS} @ -10V ¹	-80	А
I _D @T _C =100°C	Continuous Drain Current, V _{GS} @ -10V ¹	-56	А
Ірм	Pulsed Drain Current ²	-300	А
EAS	Single Pulse Avalanche Energy ³	174	mJ
las	Avalanche Current	-50	А
P _D @T _C =25°C	Total Power Dissipation ⁴	280	W
Тѕтс	Storage Temperature Range	-55 to 150	°C
TJ	Operating Junction Temperature Range	-55 to 150	°C
Reja	Thermal Resistance Junction-Ambient ¹	62	°C/W
Rejc	Thermal Resistance Junction-Case ¹	0.65	°C/W





P-Channel Electrical Characteristics (TJ =25 ℃, unless otherwise noted)

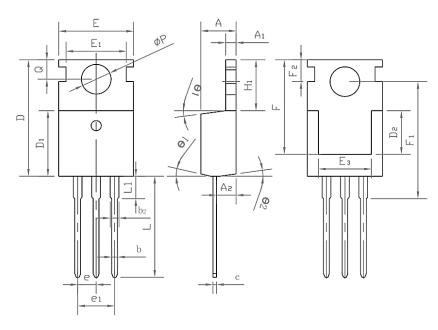
Symbol	Parameter	Test Condition	Min.	Тур.	Max.	Units
V(BR)DSS	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D =-250µA	-100	-	-	V
IDSS	Zero Gate Voltage Drain Current	V _{DS} =-100V, V _{GS} =0V,	-	ı	-1.0	μΑ
IGSS	Gate to Body Leakage Current	V_{DS} =0 V , V_{GS} = $\pm 20V$	-	ı	±100	nA
VGS(th)	Gate Threshold Voltage	$V_{DS}=V_{GS}$, $I_{D}=-250\mu A$	-1.0	-1.6	-2.5	V
RDS(on)	Static Drain-Source on-Resistance	V _{GS} =-10V, I _D =-20A	-	19	25	mΩ
RDS(0II)	Static Drain-Source on-Resistance	V _{GS} =-4.5V, I _D =-10A	-	25	30	11122
Ciss	Input Capacitance		-	4230	-	pF
Coss	Output Capacitance	V_{DS} =-50V, V_{GS} =0V, f=1.0MHz	-	388	-	pF
Crss	Reverse Transfer Capacitance	1 1.011112	-	26	-	рF
Qg	Total Gate Charge		-	80	-	nC
Qgs	Gate-Source Charge	V _{DS} =-50V, I _D =-5A, V _{GS} =-10V	-	15.6	-	nC
Qgd	Gate-Drain("Miller") Charge	V 03 10 V	-	17.2	-	nC
td(on)	Turn-on Delay Time		-	26	-	ns
tr	Turn-on Rise Time	V _{DD} =-50V, I _D =-5A,	-	78	-	ns
td(off)	Turn-off Delay Time	$R_G=6\Omega$, $V_{GS}=-10V$	-	200	-	ns
t _f	Turn-off Fall Time		-	210	-	ns
IS	Maximum Continuous Drain to Source Diode Forward Current		-	ı	-80	Α
ISM	Maximum Pulsed Drain to Source Diode Forward Current		-	i	-280	Α
VSD	Drain to Source Diode Forward Voltage	V _{GS} =0V, I _S =-30A	-	i	-1.2	V
trr	Body Diode Reverse Recovery Time	Tյ=25°C,	-	208	-	ns
Q _{rr}	Body Diode Reverse Recovery Charge	I _F =-5A,dI/dt=100A/μs	-	560	-	nC

Note:

- 1. The data tested by surface mounted on a 1 inch 2 FR-4 board with 2OZ copper.
- 2_{\times} The data tested by pulsed , pulse width $\leqq 300 us$, duty cycle $\leqq 2\%$
- 3. The EAS data shows Max. rating . The test condition is VDD =-72V,VGS =-10V,L=0.1mH,IAS =-50A
- 4. The power dissipation is limited by 150 ℃ junction temperature
- 5. The data is theoretically the same as I D and I DM, in real applications, should be limited by total power dissipation.



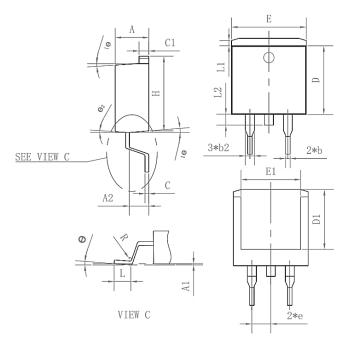
Package Mechanical Data-TO-220-3L-SLK



Common			
Symbol		mm	
	Mim	Nom	Max
A	4.27	4.57	4.87
A1	1.15	1.30	1.45
A2	2.10	2.40	2.70
b	0.70	0.80	1.00
b2	1.17	1.27	1.50
D	0.40	0.50	0.65
D1	8.80	9.10	9.40
D2	5.70	6.70	7.00
E	9.70	10.00	10.30
E1	-	8.70	-
E2	9.63	10.00	10.35
E3	7.00	8.00	8.40
е		0.37	
e1		0.10	
H1	6.00	6.50	6.85
L	12.75	13.50	13.90
L1	-	3.10	3.40
Фр	3.45	3.60	3.75
Q	2.60	2.80	3.00
θ1	4°	7°	10°
θ2	0°	3°	6°
F	13.30	13.50	13.70
F1	15.50	15.90	16.30
F2	2.80	3.00	3.20



Package Mechanical Data-TO-263-3L-SLK



	Common		
Symbol mm			
	Mim	Nom	Max
Α	4.35	4.47	4.60
A1	0.09	0.10	0.11
A2	2.30	2.40	2.70
b	0.70	0.80	1.00
b2	1.25	1.36	1.50
С	0.45	0.50	0.65
C1	1.29	1.30	9.40
D	9.10	9.20	9.30
D1	7.90	8.00	8.10
E	9.85	10.00	10.20
E1	7.90	8.00	8.10
Н	15.30	15.50	15.70
е	-	2.54	-
L	2.34	2.54	2.74
L1	1.00	1.10	1.20
L2	1.30	1.40	1.50
R	0.24	0.25	0.26
θ	0°	4°	8°
Θ1	4°	7°	10°
Θ2	0°	3°	6°





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AP80P10P/T RVE1.0 永源微電子科技有限公司



AP80P10P/T

-100V P-Channel Enhancement Mode MOSFET

Edition	Date	Change
Rve1.0	2021/4/13	Initial release

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