TOSHIBA Photocoupler IRED & Photo-Transistor

TLP290(SE

Programmable Controllers AC/DC-Input Module Hybrid ICs

TLP290(SE consist of photo transistor, optically coupled to two infrared emitting diode connected inverse parallel, and can operate directly by AC input current.

The TLP290(SE is housed in the very small and thin SO4 package. Since TLP290(SE are guaranteed wide operating temperature (Ta=-55 to 110 °C) and high isolation voltage (3750Vrms), it's suitable for high-density surface mounting applications such as programmable controllers and hybrid ICs.

Collector-Emitter voltage : 80 V (min)
 Current transfer ratio : 50% (min)
 Rank GB : 100% (min)
 Isolation voltage : 3750 Vrms (min)
 Guaranteed performance over : -55 to 110 °C

UL-recognized : UL 1577, File No.E67349

• cUL-recognized : CSA Component Acceptance Service No.5A,

File No.E67349

• VDE-approved : EN 60747-5-5, EN 62368-1 (Note 1)

CQC-approved : GB4943.1, GB8898 Japan and Thailand Factory



仅适用干海拔 2000m 以下地区安全使用

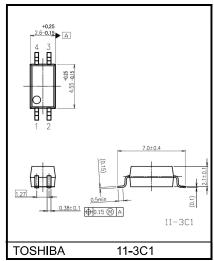
Note 1: When a VDE approved type is needed, please designate the **Option (V4)**.

Construction Mechanical Rating
Creepage distance: 5.0 mm (min)

Clearance: 5.0 mm (min)

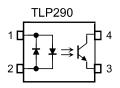
Insulation thickness: 0.4 mm (min)

Unit: mm



Weight: 0.05 g (typ.)

Pin Configuration



- 1: Anode Cathode
- 2: Cathode Anode
- 3: Emitter
- 4: Collector

Start of commercial production 2012-02

Current Transfer Ratio (Unless otherwise specified, Ta = 25°C)

TYPE	Classification (Note1)		fer Ration (%) / I F)		
		I _F = 5 mA, V _{CE} = 5 V, Ta = 25°C		Marking of Classification	
		Min	Max		
	Blank	50	600	Blank, YE, GR, BL, GB	
	Rank Y	50	150	YE	
TLP290	Rank GR	100	300	GR	
	Rank GB	100	600	GB	
	Rank BL	200	600	BL	

Note1: Specify both the part number and a rank in this format when ordering

(e.g.) rank GB: TLP290(GB,SE

For safety standard certification, however, specify the part number alone.

(e.g.) TLP290(GB,SE: TLP290

Absolute Maximum Ratings (Note) (Unless otherwise specified, Ta = 25°C)

	Characteristic	Symbol	Note	Rating	Unit
	R.M.S. forward current	I _F (RMS)		±50	mA
ĺ	Input forward current derating (Ta ≥ 90°C)	ΔI _F /ΔTa		-1.5	mA /°C
	Input forward current (pulsed)	IFP	(Note 2)	±1	А
"	Input power dissipation	PD		100	mW
ĺ	Input power dissipation derating (Ta ≥ 90°C)	ΔΡ _D /ΔΤα		-3.0	mW/°C
ĺ	Junction temperature	Tj		125	°C
	Collector-emitter voltage	VCEO		80	V
	Emitter-collector voltage	VECO		7	V
ctor	Collector current	Ic		50	mA
Detector	Collector power dissipation	Pc		150	mW
	Collector power dissipation derating (Ta ≥ 25°C)	ΔΡ _С /ΔΤα		-1.5	mW /°C
	Junction temperature	Tj		125	°C
Оре	erating temperature range	T _{opr}		-55 to 110	°C
Sto	rage temperature range	T _{stg}		-55 to 125	°C
Lea	d soldering temperature	T _{sol}		260 (10 s)	°C
Tota	al package power dissipation	PT		200	mW
Tota	al package power dissipation derating (Ta ≥ 25°C)	ΔΡΤ /ΔΤα		-2.0	mW /°C
Isol	ation voltage	BVs	(Note3)	3750	Vrms

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note2: Pulse width \leq 100 μ s, frequency 100 Hz

Note3: AC, 60 s., R.H.≤ 60 %, Device considered a two terminal device: LED side pins shorted together and detector side pins shorted together.

Electrical Characteristics (Unless otherwise specified, Ta = 25°C)

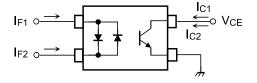
Characteristic		Symbol	Test Condition	Min	Тур	Max	Unit
ED	Input forward voltage	VF	I _F = ±10 mA	1.1	1.25	1.4	V
] "	Input capacitance	CT	V = 0 V, f = 1 MHz	-	60	-	pF
	Collector-emitter breakdown voltage	V(BR) CEO	I _C = 0.5 mA	80	-	-	V
l b	Emitter-collector breakdown voltage	V(BR) ECO	I _E = 0.1 mA	7	-	-	٧
Detector	Dark current	lono	V _{CE} = 48 V,	- 0.	0.01	0.08	μА
╽╸		ICEO	V _{CE} = 48 V, Ta = 85 °C	-	2	50	μА
	Collector-emitter capacitance	C _{CE}	V = 0 V, f = 1 MHz	-	10	ı	pF

Coupled Electrical Characteristics (Unless otherwise specified, Ta = 25°C)

Characteristic	Symbol	Test Condition	MIn	Тур.	Max	Unit
	I _C / I _F	I_F = ±5 mA, V_{CE} = 5 V Rank GB	50	-	600	- %
Current transfer ratio			100	ı	600	
Seturated CTD	IC / IF (sat)	IF = ±1 mA, V _{CE} = 0.4 V	-	60	-	%
Saturated CTR		Rank GB	30	-	-	70
		I _C = 2.4 mA, I _F = ±8 mA	1	-	0.3	
Collector-emitter saturation voltage	VCE (sat)	I _C = 0.2 mA, I _F = ±1 mA	ı	0.2	ı	V
		Rank GB	-	-	0.3	
Off-state collector current	IC(off)	V _F = ± 0.7 V, V _{CE} = 48 V	-	-	10	μΑ
Collector current ratio	IC (ratio)	Ic (I _F = -5 mA) / I _C (I _F = 5 mA) (Fig.1)	0.33	ı	3	ı

Fig.1: Collector current ratio test circuit

$$I_{C(ratio)} = \frac{I_{C2}(I_F = I_{F2}, V_{CE} = 5V)}{I_{C1}(I_F = I_{F1}, V_{CE} = 5V)}$$



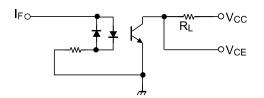
Isolation Characteristics (Unless otherwise specified, Ta = 25°C)

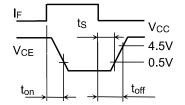
Characteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
Total capacitance (input to output)	Cs	V _S = 0V, f = 1 MHz	-	0.8	-	pF
Isolation resistance	Rs	V _S = 500 V, R.H.≤ 60 %	1×10 ¹²	10 ¹⁴	-	Ω
Isolation voltage	BVs	AC, 60 s	3750	-	-	V _{rms}

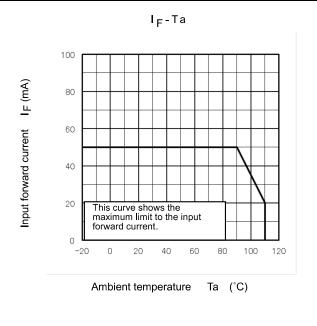
Switching Characteristics (Unless otherwise specified, Ta = 25°C)

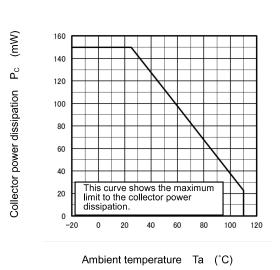
Characteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
Rise time	t _r		-	2	-	
Fall time	tf	V_{CC} = 10 V, I_C = 2 mA RL = 100 Ω	-	3	-	μS
Turn-on time	ton		-	3	-	
Turn-off time	t _{off}		-	3	-	
Turn-on time	ton		-	0.5	-	
Storage time	ts	$R_L = 1.9 \text{ k}\Omega$ (Fig.2) $V_{CC} = 5 \text{ V}$, $I_F = \pm 16 \text{ mA}$	-	30	-	μS
Turn-off time	t _{off}		-	50	-]

(Fig. 2): Switching time test circuit

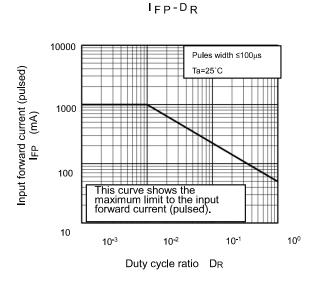


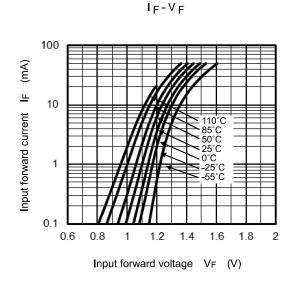


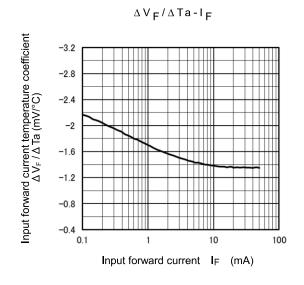


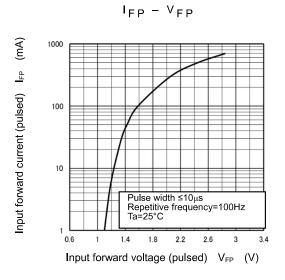


P_C-Ta

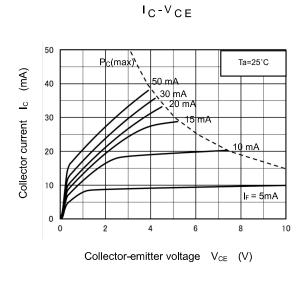


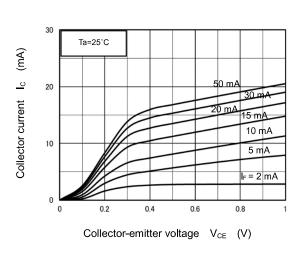




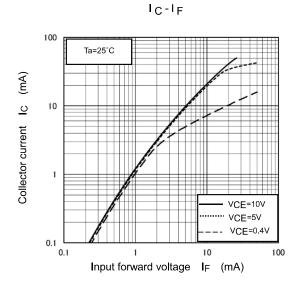


NOTE: The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.

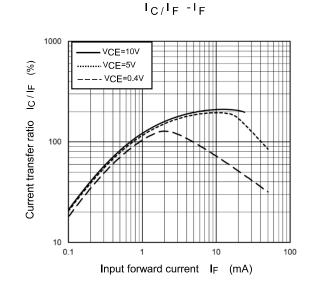




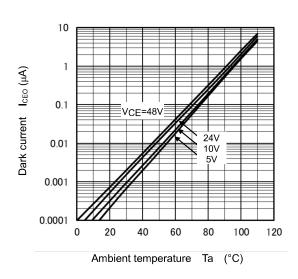
IC-VCE

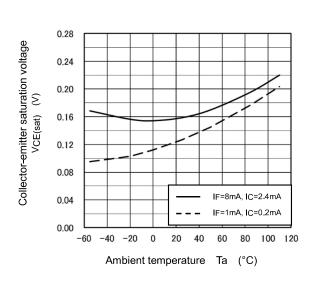


ICEO-Ta



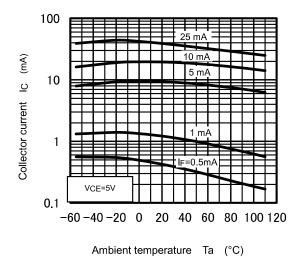
V_{CE(sat)} - Ta



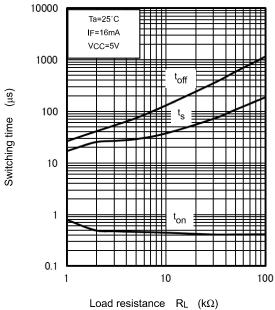


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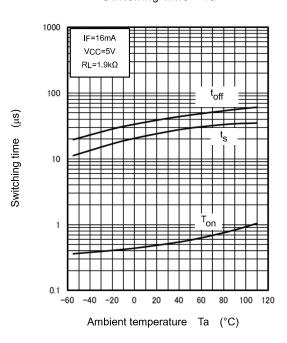
I_C - Ta



Switching time - R_I



Switching time - Ta



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Soldering and Storage

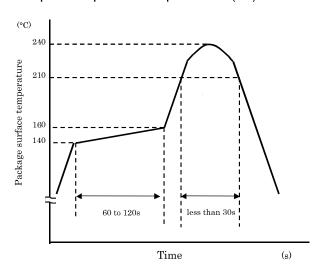
1. Soldering

1.1 Soldering

When using a soldering iron or medium infrared ray/hot air reflow, avoid a rise in device temperature as much as possible by observing the following conditions.

1) Using solder reflow

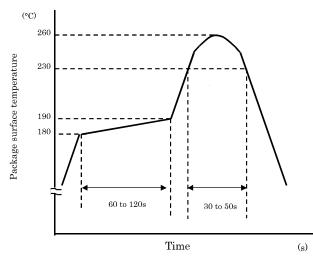
·Temperature profile example of lead (Pb) solder



This profile is based on the device's maximum heat resistance guaranteed value.

Set the preheat temperature/heating temperature to the optimum temperature corresponding to the solder paste type used by the customer within the described profile.

·Temperature profile example of using lead (Pb)-free solder



This profile is based on the device's maximum heat resistance guaranteed value.

Set the preheat temperature/heating temperature to the optimum temperature corresponding to the solder paste type used by the customer within the described profile.

Reflow soldering must be performed once or twice.

The mounting should be completed with the interval from the first to the last mountings being 2 weeks.

- 2) Using solder flow (for lead (Pb) solder, or lead (Pb)-free solder)
 - ·Please preheat it at 150°C between 60 and 120 seconds.
 - ·Complete soldering within 10 seconds below 260°C. Each pin may be heated at most once.
- 3) Using a soldering iron

Complete soldering within 10 seconds below 260°C, or within 3 seconds at 350°C. Each pin may be heated at most once.

2. Storage

- 1) Avoid storage locations where devices may be exposed to moisture or direct sunlight.
- 2) Follow the precautions printed on the packing label of the device for transportation and storage.
- 3) Keep the storage location temperature and humidity within a range of 5°C to 35°C and 45% to 75%, respectively.
- 4) Do not store the products in locations with poisonous gases (especially corrosive gases) or in dusty conditions.
- 5) Store the products in locations with minimal temperature fluctuations. Rapid temperature changes during storage can cause condensation, resulting in lead oxidation or corrosion, which will deteriorate the solderability of the leads.
- 6) When restoring devices after removal from their packing, use anti-static containers.
- 7) Do not allow loads to be applied directly to devices while they are in storage.
- 8) If devices have been stored for more than two years under normal storage conditions, it is recommended that you check the leads for ease of soldering prior to use.

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