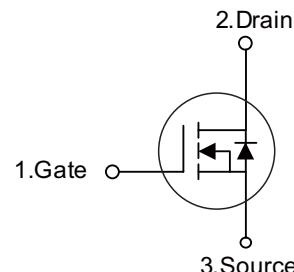


■ PRODUCT CHARACTERISTICS

VDSS	60V
R _{DS(on)} max(@V _{GS} = 10 V)	18mΩ
ID	50A

Symbol



■ APPLICATIONS

- * Switching applications

■ FEATURES

- * R_{DS(ON)} ≤ 18m Ω @ V_{GS} = 10V, I_D = 25A
- * High Switching Speed
- * Improved dv/dt capability



TO-252



TO-251

■ ORDER INFORMATION

Order codes		Package	Packing
Halogen-Free	Halogen		
N/A	MOT50N06D	TO-252	2500 pieces /Reel
N/A	MOT50N06C	TO-251	70 pieces/Tube

■ ABSOLUTE MAXIMUM RATINGS

PARAMETER	SYMBOL	RATINGS	UNIT
Drain-Source Voltage	V _{DSS}	60	V
Gate-Source Voltage	V _{GSS}	±20	V
Continuous Drain Current	I _D	50	A
Pulsed Drain Current (Note 2)	I _{DM}	150	A
Avalanche Energy	E _{AS}	92	mJ
Peak Diode Recovery dv/dt	dv/dt	10	V/ns
Power Dissipation	P _D	46	W
Junction Temperature	T _J	+150	°C
Operation and Storage Temperature	T _{STG}	-55 ~ +150	°C

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged.
 Absolute maximum ratings are stress ratings only and functional device operation is not implied.

2. Repetitive Rating : Pulse width limited by maximum junction temperature.

3. L=43mH, I_{AS}=43A, V_{DD}=25V, R_G=20Ω, Starting T_J=25°C

4. I_{SD} ≤ 30A, V_{DS}=0V, di/dt ≤ 200A/μs, V_{DD} ≤ BV_{DSS}, Starting T_J = 25°C

■ THERMAL DATA

PARAMETER	SYMBOL	RATING	UNIT
Junction to Ambient	θ _{JA}	100	°C/W
Junction to Case	θ _{JC}	2.7 (Note)	°C/W

Note: The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper.

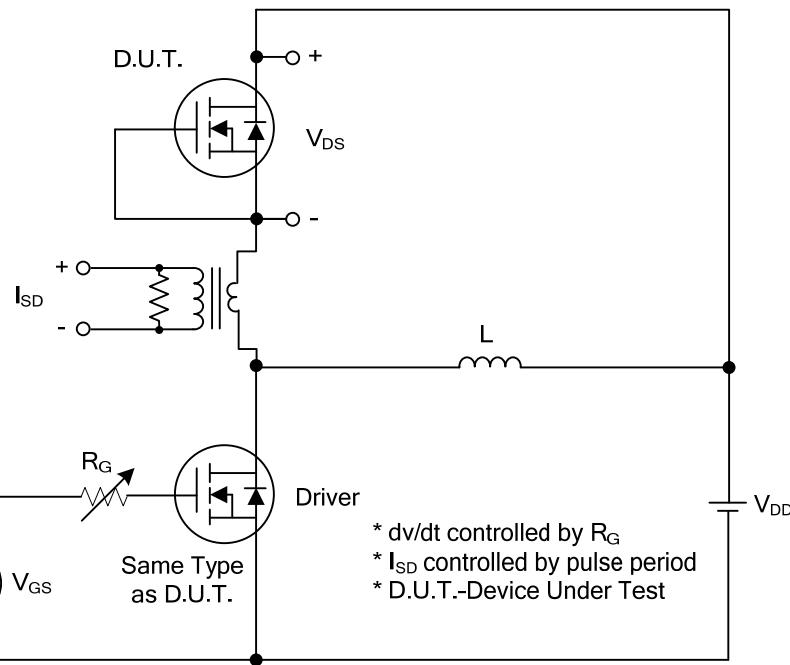
■ ELECTRICAL CHARACTERISTICS ($T_c=25^\circ\text{C}$, unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
OFF CHARACTERISTICS						
Drain-Source Breakdown Voltage	BV_{DSS}	$\text{V}_{\text{GS}}=0\text{V}, \text{I}_D=250\mu\text{A}$	60			V
Drain-Source Leakage Current	I_{DSS}	$\text{V}_{\text{DS}}=60\text{V}, \text{V}_{\text{GS}}=0\text{V}$		10		μA
Gate-Source Leakage Current	Forward	$\text{V}_{\text{GS}}=20\text{V}, \text{V}_{\text{DS}}=0\text{V}$			100	nA
	Reverse	$\text{V}_{\text{GS}}=-20\text{V}, \text{V}_{\text{DS}}=0\text{V}$			-100	nA
ON CHARACTERISTICS						
Gate Threshold Voltage	$\text{V}_{\text{GS(TH)}}$	$\text{V}_{\text{DS}}=\text{V}_{\text{GS}}, \text{I}_D=250\mu\text{A}$	1.0		2.5	V
Static Drain-Source On-State Resistance	$\text{R}_{\text{DS(ON)}}$	$\text{V}_{\text{GS}}=10\text{V}, \text{I}_D=25\text{A}$			18	$\text{m}\Omega$
DYNAMIC CHARACTERISTICS						
Input Capacitance	C_{ISS}	$\text{V}_{\text{GS}}=0\text{V}, \text{V}_{\text{DS}}=25\text{V}, f=1\text{MHz}$		2500		pF
Output Capacitance	C_{OSS}			230		pF
Reverse Transfer Capacitance	C_{RSS}			200		pF
SWITCHING CHARACTERISTICS						
Total Gate Charge	Q_G	$\text{V}_{\text{DS}}=50\text{V}, \text{V}_{\text{GS}}=10\text{V}, \text{I}_D=1.3\text{A}$ $\text{I}_G=3\text{mA}$ (Note1,2)		7.2		nC
Gate-Source Charge	Q_{GS}			0.4		nC
Gate-Drain Charge	Q_{GD}			0.8		nC
Turn-On Delay Time	$t_{\text{D(ON)}}$	$\text{V}_{\text{DS}}=30\text{V}, \text{V}_{\text{GS}}=10\text{V}, \text{I}_D=50\text{A},$ $\text{R}_G=25\Omega$ (Note1,2)		18		ns
Turn-On Rise Time	t_R			46		ns
Turn-Off Delay Time	$t_{\text{D(OFF)}}$			202		ns
Turn-Off Fall Time	t_F			116		ns
DRAIN-SOURCE DIODE CHARACTERISTICS AND MAXIMUM RATINGS						
Maximum Continuous Drain-Source Diode Forward Current	I_S				50	A
Maximum Pulsed Drain-Source Diode Forward Current	I_{SM}				150	A
Drain-Source Diode Forward Voltage	V_{SD}	$\text{I}_S=50\text{A}, \text{V}_{\text{GS}}=0\text{V}$			1.5	V
Body Diode Reverse Recovery Time	t_{rr}	$\text{I}_S=30\text{A}, \text{V}_{\text{GS}}=0\text{V},$ $d\text{I}_S/dt=100\text{A}/\mu\text{s}$		50		ns
Body Diode Reverse Recovery Charge	Q_{rr}			80		nC

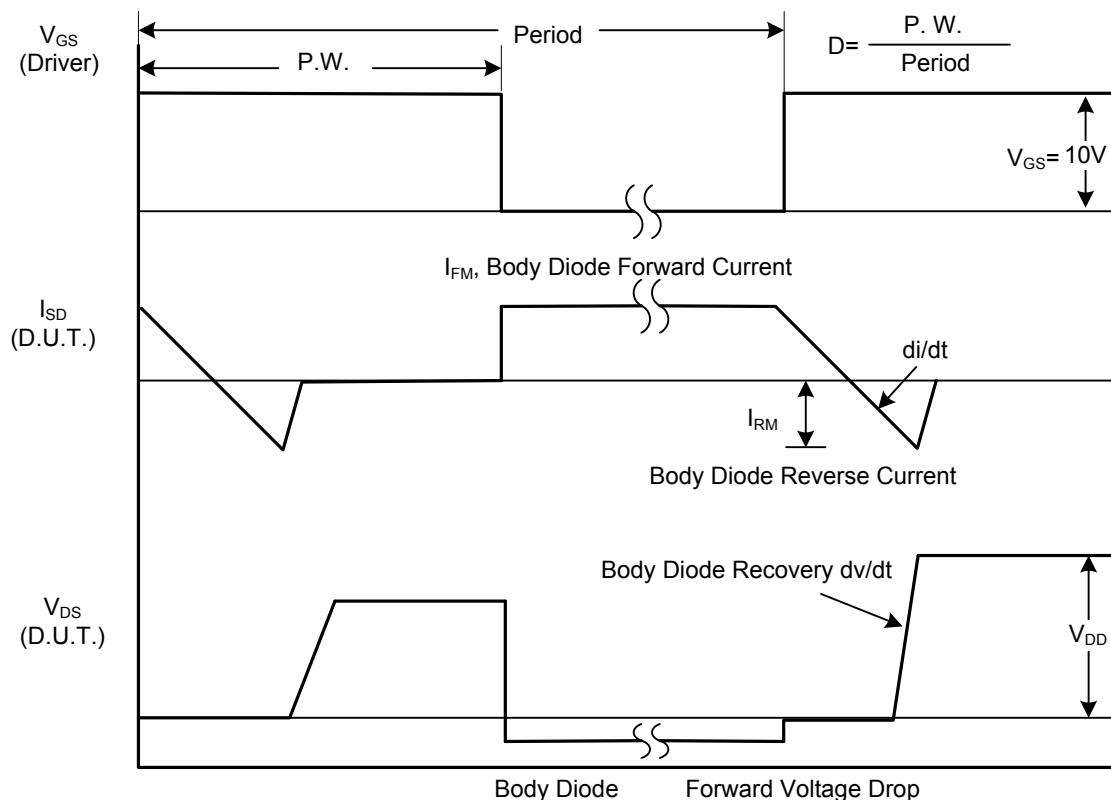
Notes: 1. Pulse Test: Pulse Width $\leq 300\mu\text{s}$, Duty Cycle $\leq 2\%$.

2. Essentially independent of operating temperature.

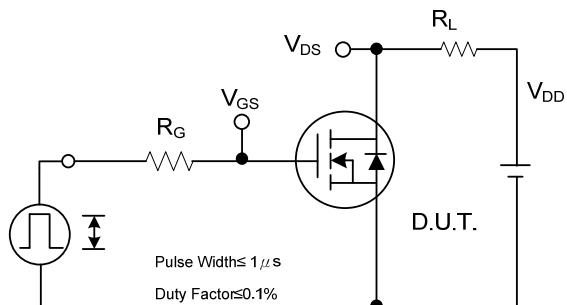
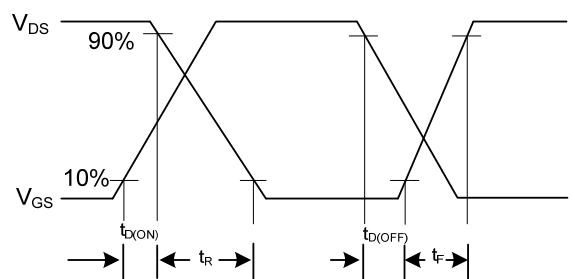
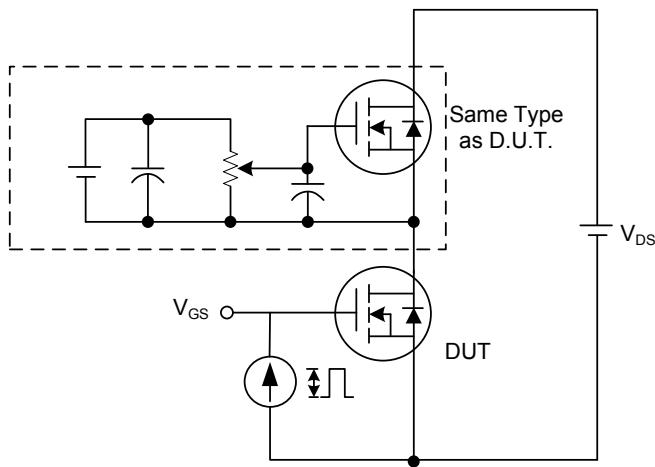
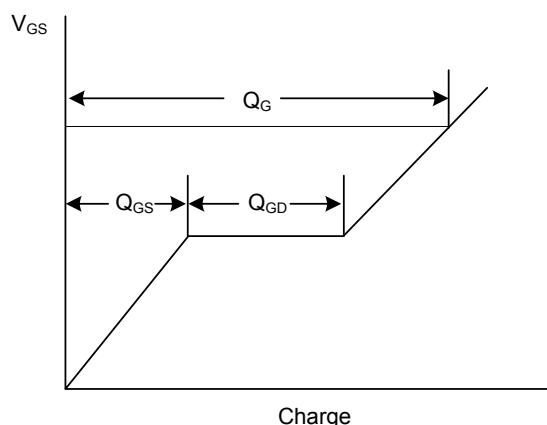
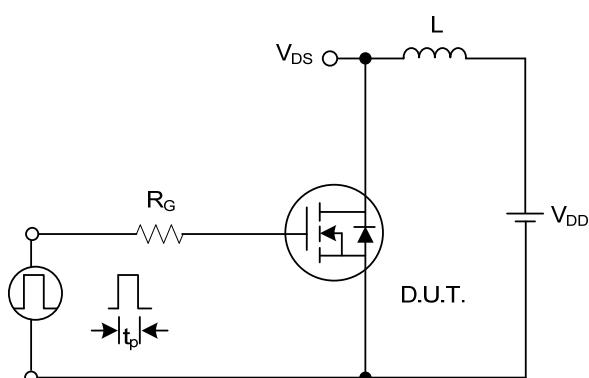
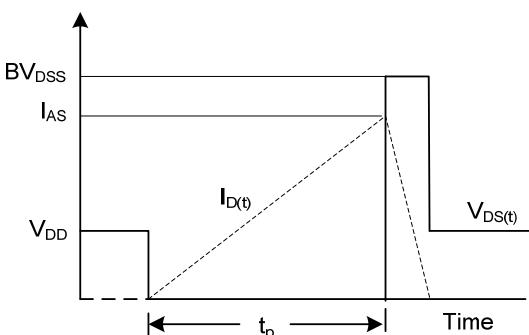
■ TEST CIRCUITS AND WAVEFORMS



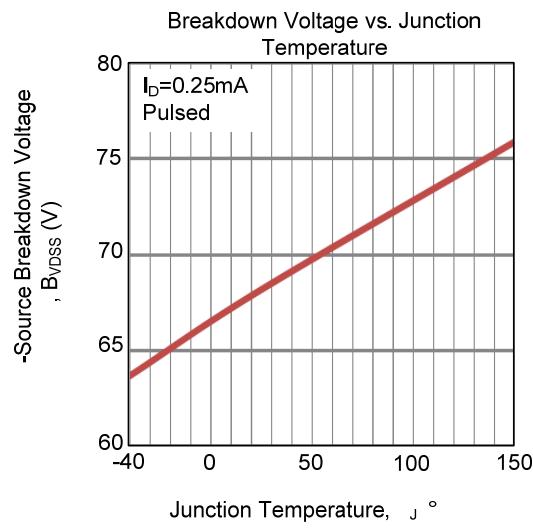
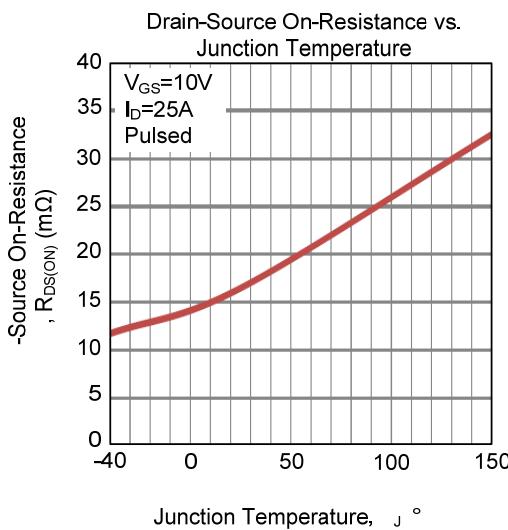
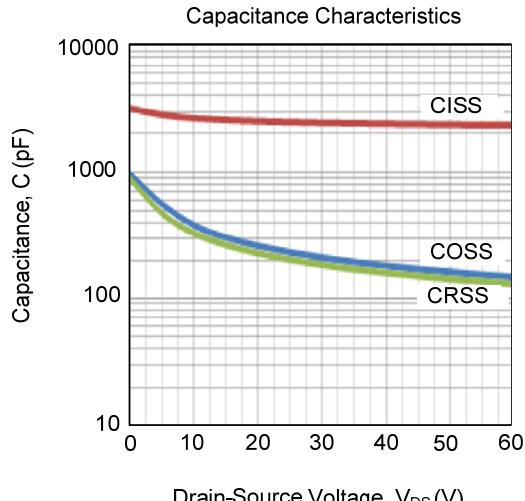
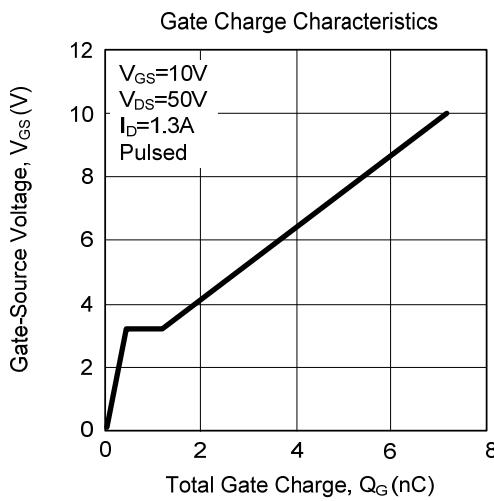
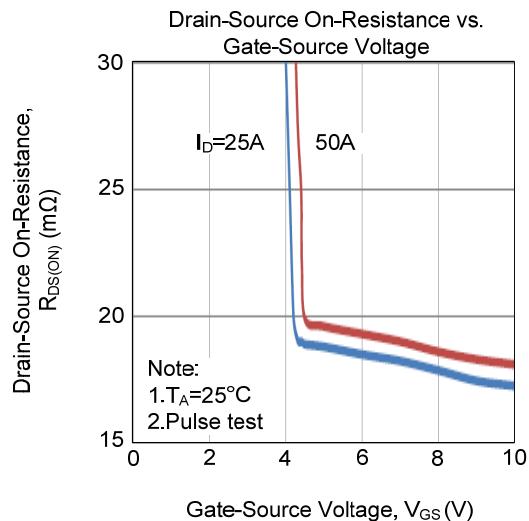
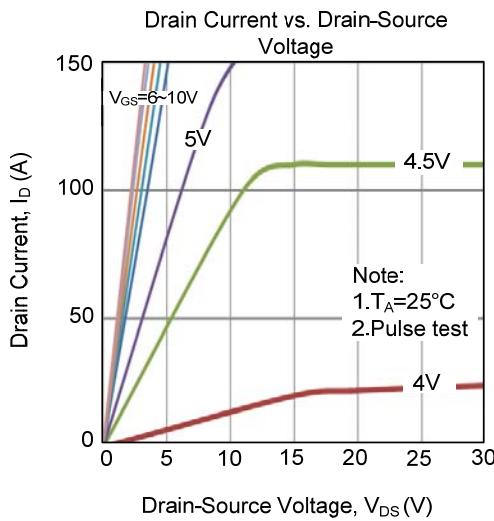
Peak Diode Recovery dv/dt Test Circuit

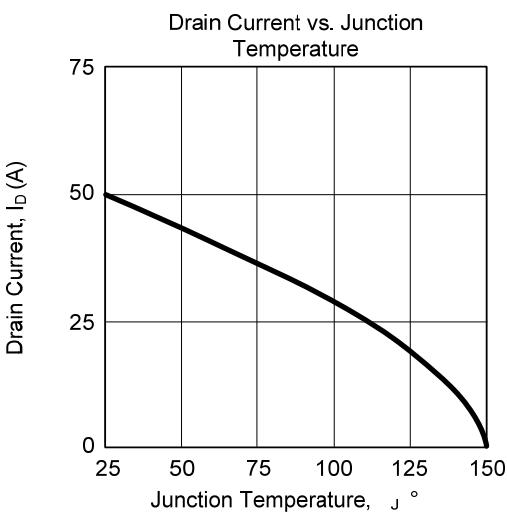
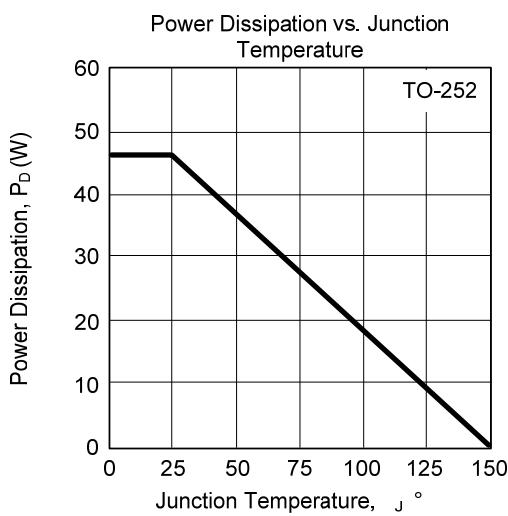
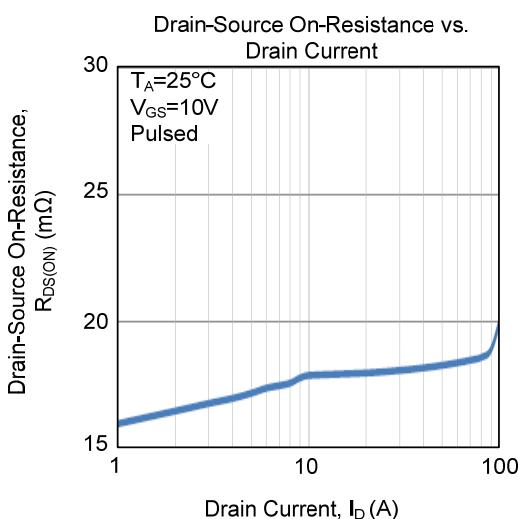
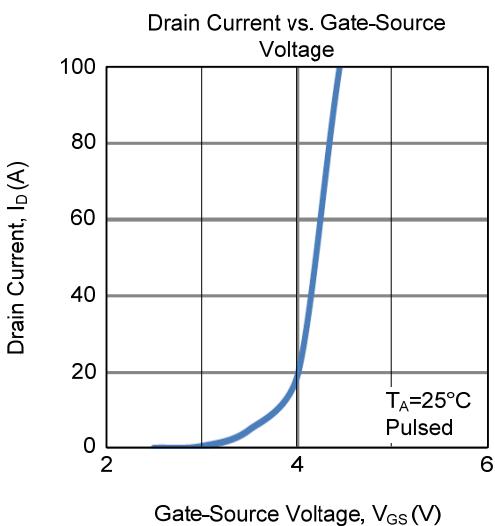
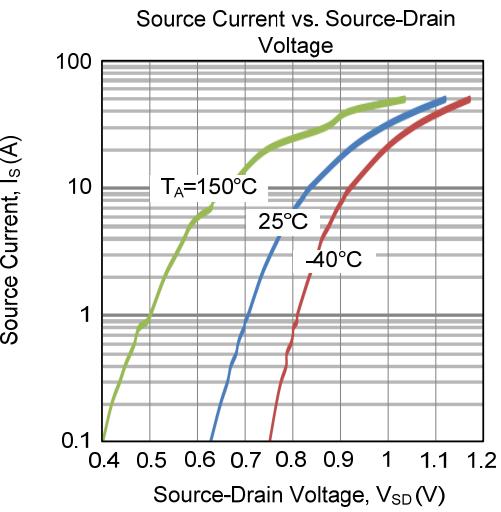
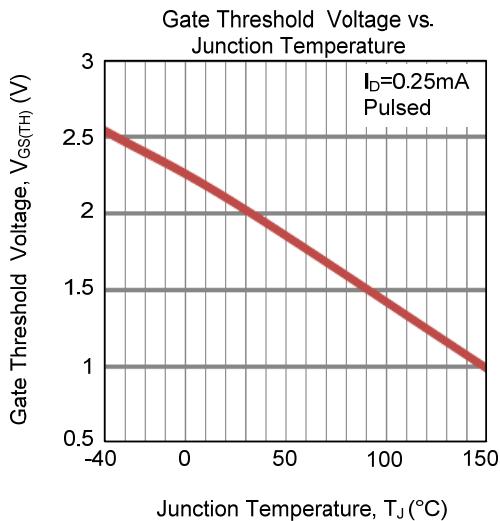


Peak Diode Recovery dv/dt Waveforms

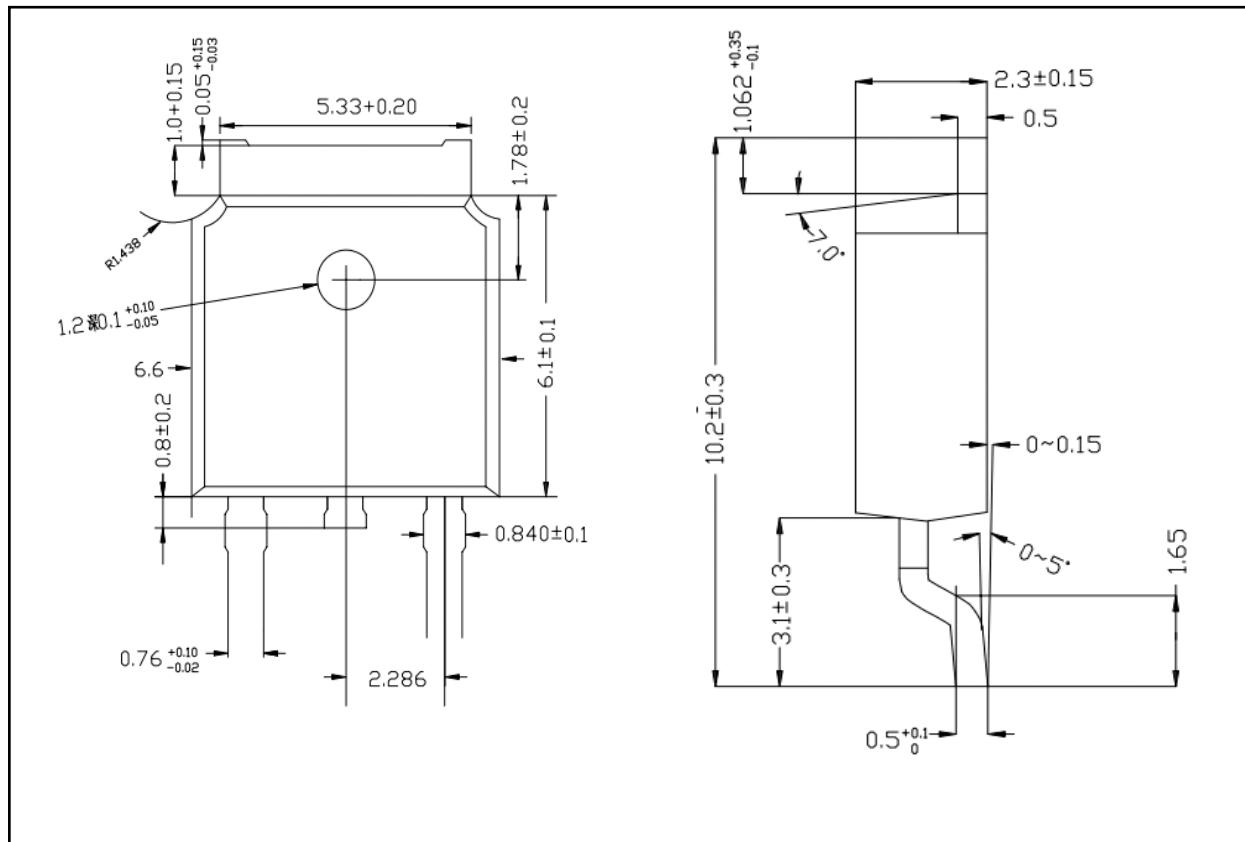
■ TEST CIRCUITS AND WAVEFORMS(Cont.)

Fig. 2A Switching Test Circuit

Fig. 2B Switching Waveforms

Fig. 3A Gate Charge Test Circuit

Fig. 3B Gate Charge Waveform

Fig. 4A Unclamped Inductive Switching Test Circuit

Fig. 4B Unclamped Inductive Switching Waveforms

■ TYPICAL CHARACTERISTICS



■ TYPICAL CHARACTERISTICS(Cont.)


■ TO-252 PACKAGE OUTLINE DIMENSIONS



■ TO-251 PACKAGE OUTLINE DIMENSIONS

