

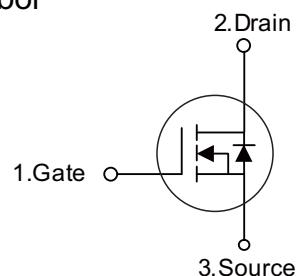


MOT65R380F/MOT65R380A N-CHANNEL MOSFET

■ PRODUCT CHARACTERISTICS

VDSS	650V
R _{DS(on)} max(@V _{GS} = 10 V)	0.38Ω
Qg@type	14nC
ID	11A

Symbol



■ APPLICATIONS

- * Power factor correction
- * Switched mode power supplies
- * Uninterruptible power supply

■ FEATURES

- * low R_{DS(on)}
- * low gate charge
- * 100% UIS tested
- * RoHS compliant



TO-220



TO-220F

■ ORDER INFORMATION

Order codes		Package	Packing
Halogen-Free	Halogen		
N/A	MOT65R380F	TO-220F	50 pieces/Tube
N/A	MOT65R380A	TO-220	50 pieces/Tube

■ ABSOLUTE MAXIMUM RATINGS (T_C=25°C unless otherwise specified)

Parameter	Symbol	Value	Unit	
Drain-Source Voltage	V _{DSS}	650	V	
Continuous drain current (T _C = 25°C)	I _D	11	A	
(T _C = 100°C)		8.2	A	
Pulsed drain current ¹⁾	I _{DM}	30	A	
Gate-Source voltage	V _{GSS}	±30	V	
Avalanche energy, single pulse ²⁾	E _{AS}	245	mJ	
Avalanche current, repetitive ³⁾	I _{AR}	11	A	
Power Dissipation TO-220	P _D	90	W	
(T _C = 25°C) - Derate above 25°C		0.72	W/°C	
Power Dissipation TO-220F (T _C = 25°C)		31.8	W	
- Derate above 25°C		0.26	W/°C	
Operating and Storage Temperature Range	T _J , T _{STG}	-55 to +150	°C	
Continuous diode forward current	I _S	11	A	
Diode pulse current	I _{S,pulse}	30	A	

■ THERMAL CHARACTERISTICS

Parameter		Symbol	Value	Unit
Thermal Resistance, Junction-to-Case	TO-220	R _{θJC}	1.39	°C/W
	TO-220F		3.9	°C/W
Thermal Resistance, Junction-to-Ambient	TO-220	R _{θJA}	45	°C/W
	TO-220F		53	°C/W

■ ELECTRICAL CHARACTERISTICS ($T_J = 25^\circ\text{C}$, unless otherwise specified)

Parameter	Symbol	Test Condition	Min.	Typ.	Max.	Unit
Static characteristics						
Drain-source breakdown voltage	BV_{DSS}	$V_{\text{GS}}=0 \text{ V}, I_{\text{D}}=0.25 \text{ mA}$	650	-	-	V
Gate threshold voltage	$V_{\text{GS}(\text{th})}$	$V_{\text{DS}}=V_{\text{GS}}, I_{\text{D}}=0.25 \text{ mA}$	2.5	3.5	4.5	V
Drain cut-off current	I_{DSS}	$V_{\text{DS}}=650 \text{ V}, V_{\text{GS}}=0 \text{ V}, T_j = 25^\circ\text{C}$			1	μA
		$T_j = 125^\circ\text{C}$	-	10	-	
Gate leakage current, Forward	I_{GSSF}	$V_{\text{GS}}=30 \text{ V}, V_{\text{DS}}=0 \text{ V}$	-	-	100	nA
Gate leakage current, Reverse	I_{GSSR}	$V_{\text{GS}}=-30 \text{ V}, V_{\text{DS}}=0 \text{ V}$	-	-	-100	nA
Drain-source on-state resistance	$R_{\text{DS(on)}}$	$V_{\text{GS}}=10 \text{ V}, I_{\text{D}}=5.5 \text{ A}, T_j = 25^\circ\text{C}$	-	0.33	0.38	Ω
		$T_j = 150^\circ\text{C}$	-	0.9	-	
Gate resistance	R_{G}	f=1 MHz, open drain	-	5.7	-	Ω
Dynamic characteristics						
Input capacitance	C_{iss}	$V_{\text{DS}} = 25 \text{ V}, V_{\text{GS}} = 0 \text{ V}, f = 1 \text{ MHz}$	-	560	-	pF
Output capacitance	C_{oss}		-	216	-	
Reverse transfer capacitance	C_{rss}		-	1.2	-	
Turn-on delay time	$t_{\text{d(on)}}$	$V_{\text{DD}} = 400 \text{ V}, I_{\text{D}} = 5.5 \text{ A}, R_{\text{G}} = 10\Omega, V_{\text{GS}} = 15 \text{ V}$	-	20.6	-	ns
Rise time	t_r		-	32	-	
Turn-off delay time	$t_{\text{d(off)}}$		-	62	-	
Fall time	t_f		-	12.5	-	
Gate charge characteristics						
Gate to source charge	Q_{gs}	$V_{\text{DD}} = 400 \text{ V}, I_{\text{D}} = 5.5 \text{ A}, V_{\text{GS}} = 0 \text{ to } 10 \text{ V}$	-	4.8	-	nC
Gate to drain charge	Q_{gd}		-	4.7	-	
Gate charge total	Q_g		-	14.7	-	
Gate plateau voltage	V_{plateau}		-	6	-	
Reverse diode characteristics						
Diode forward voltage	V_{SD}	$V_{\text{GS}}=0 \text{ V}, I_{\text{F}}=5.5 \text{ A}$	-	1.2	-	V
Reverse recovery time	t_{rr}	$V_R = 400 \text{ V}, I_{\text{F}}=5.5 \text{ A}, dI_{\text{F}}/dt=100 \text{ A}/\mu\text{s}$	-	234	-	ns
Reverse recovery charge	Q_{rr}		-	4.4	-	
Peak reverse recovery current	I_{rrm}		-	18.7	-	

Notes:

1. Limited by maximum junction temperature, maximum duty cycle is 0.75.
2. $I_{\text{AS}} = 3 \text{ A}, V_{\text{DD}} = 60 \text{ V}$, Starting $T_j = 25^\circ\text{C}$.
3. Repetitive Rating: Pulse width limited by maximum junction temperature.

■ ELECTRICAL CHARACTERISTICS DIAGRAMS

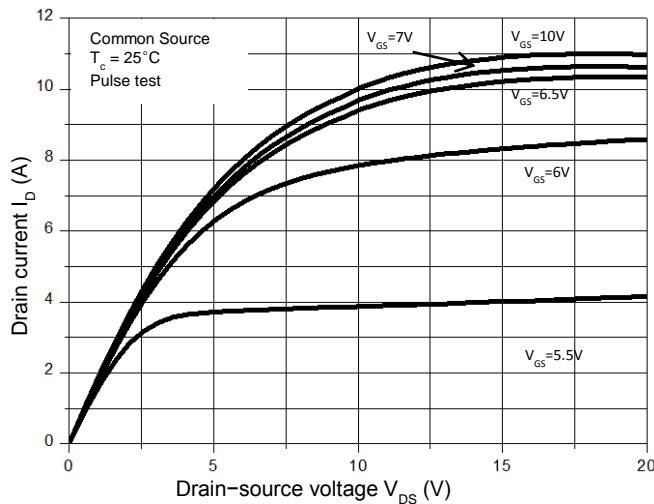


Figure 1. On-Region Characteristics

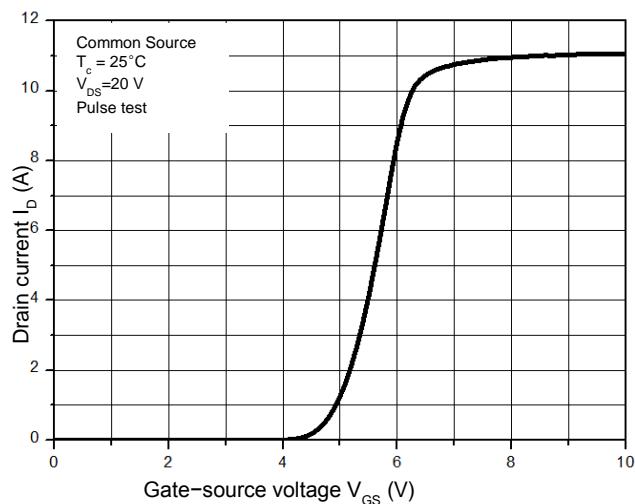


Figure 2. Transfer Characteristics

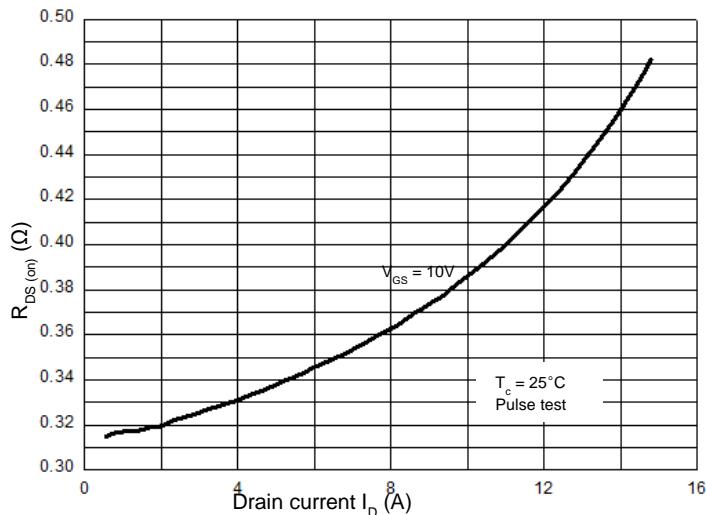


Figure 3. On-Resistance Variation vs. Drain Current

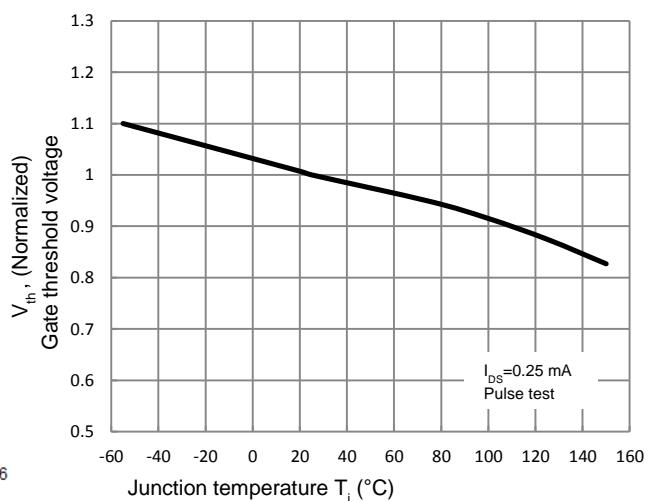


Figure 4. Threshold Voltage vs. Temperature

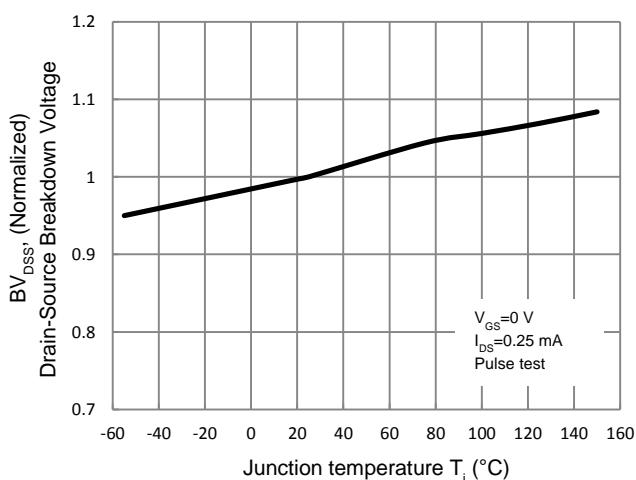


Figure 5. Breakdown Voltage vs. Temperature

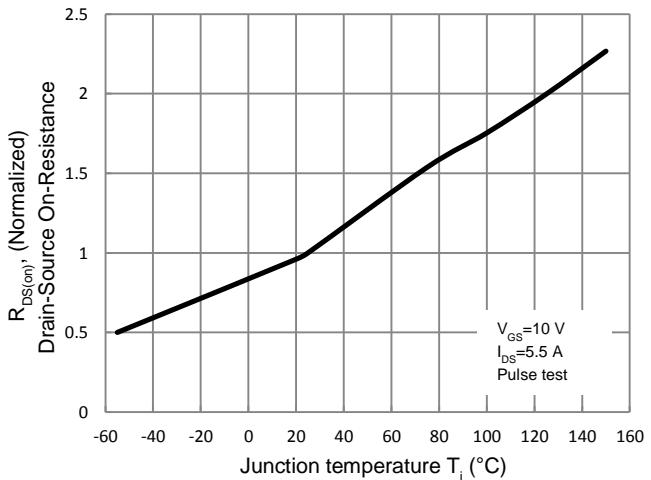


Figure 6. On-Resistance vs. Temperature

■ ELECTRICAL CHARACTERISTICS DIAGRAMS(Cont.)

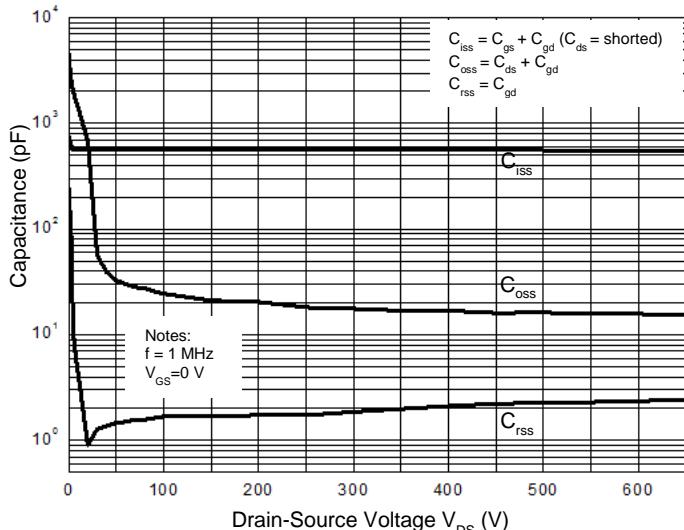


Figure 7. Capacitance Characteristics

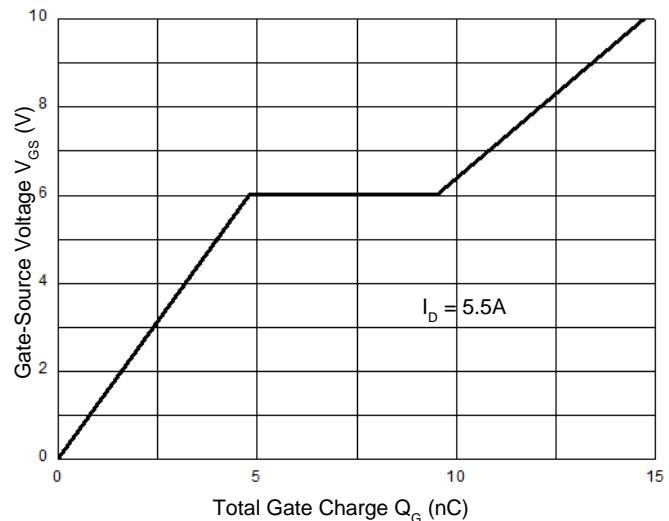


Figure 8. Gate Charge Character

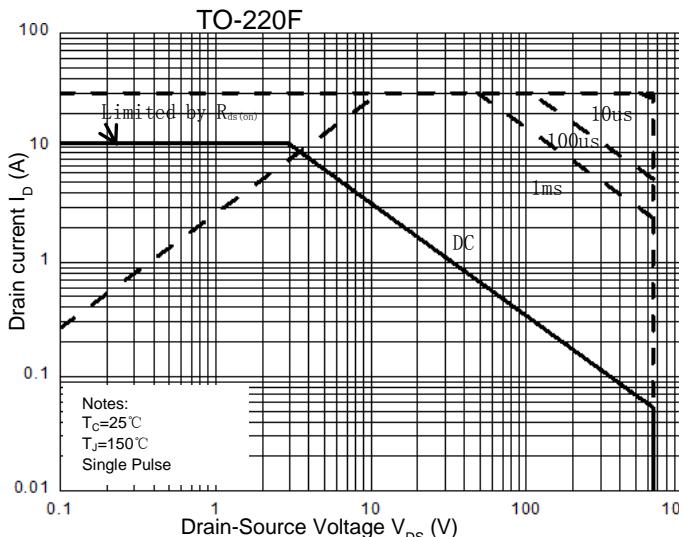


Figure 9.1 Maximum Safe Operating Area

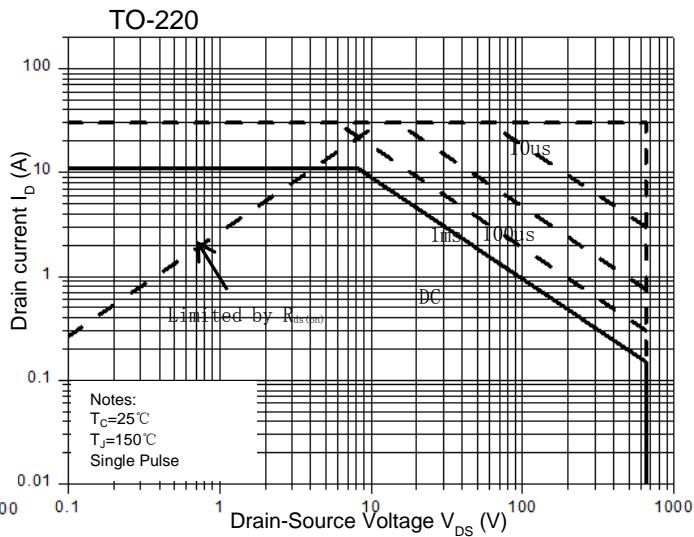


Figure 9.2 Maximum Safe Operating Area

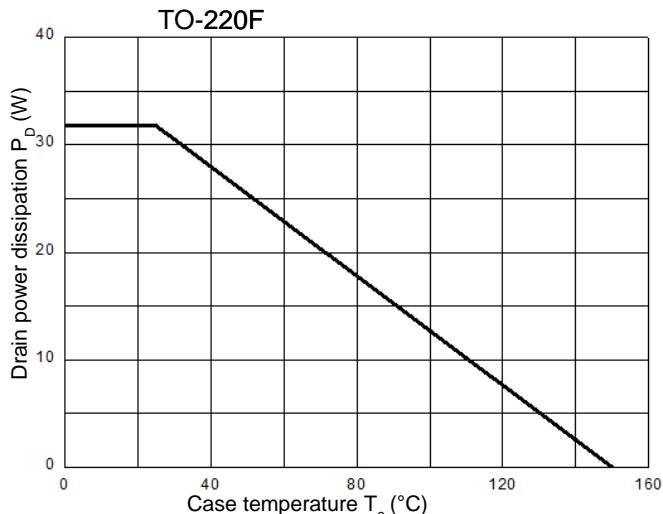


Figure 10.1 Power Dissipation vs. Temperature

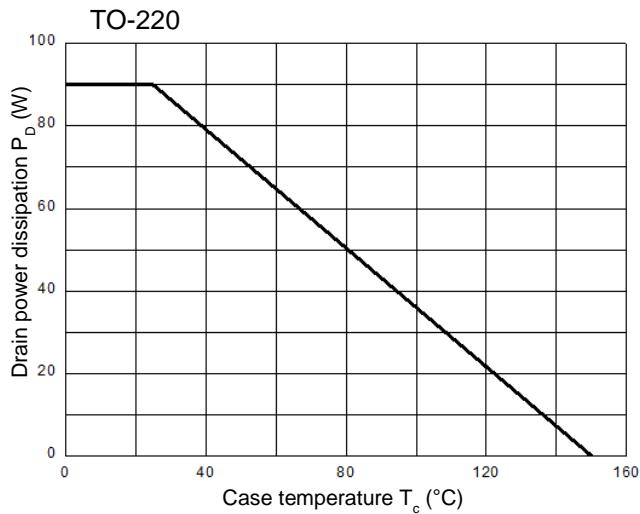
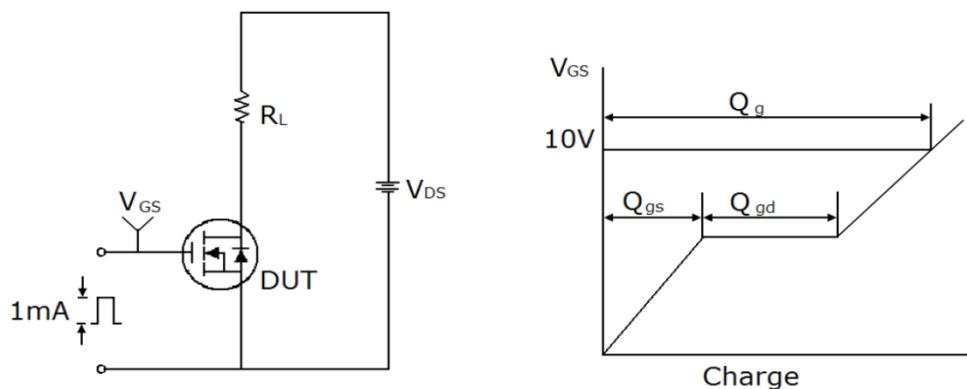
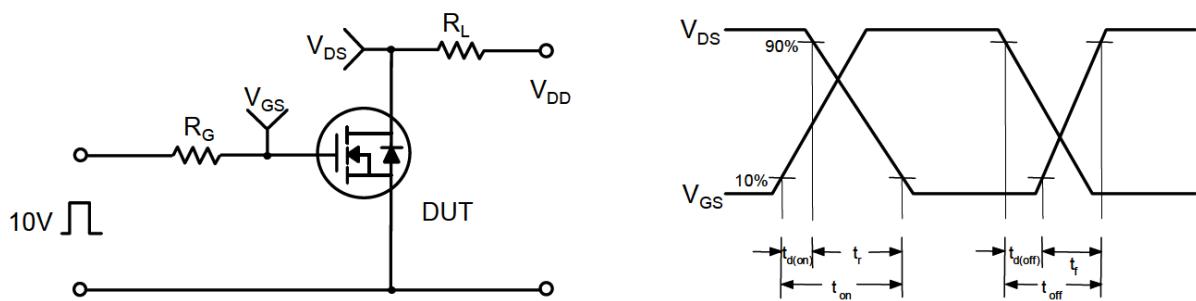


Figure 10.2 Power Dissipation vs. Temperature

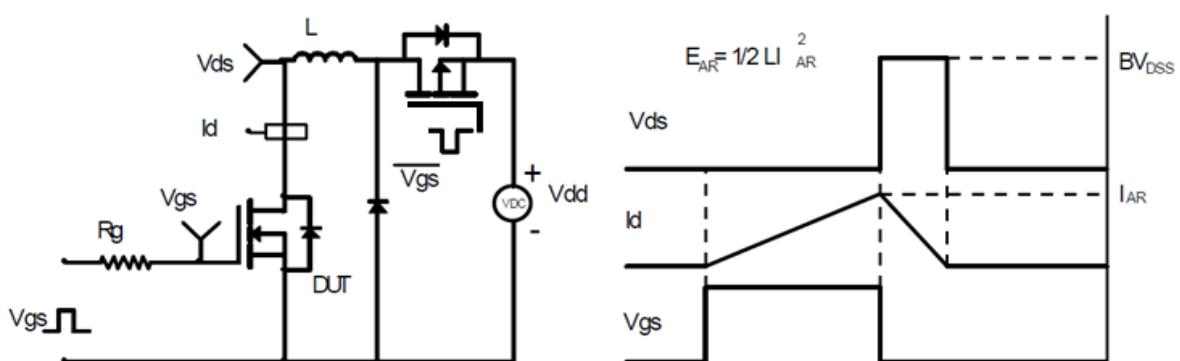
■ TEST CIRCUITS AND WAVEFORMS



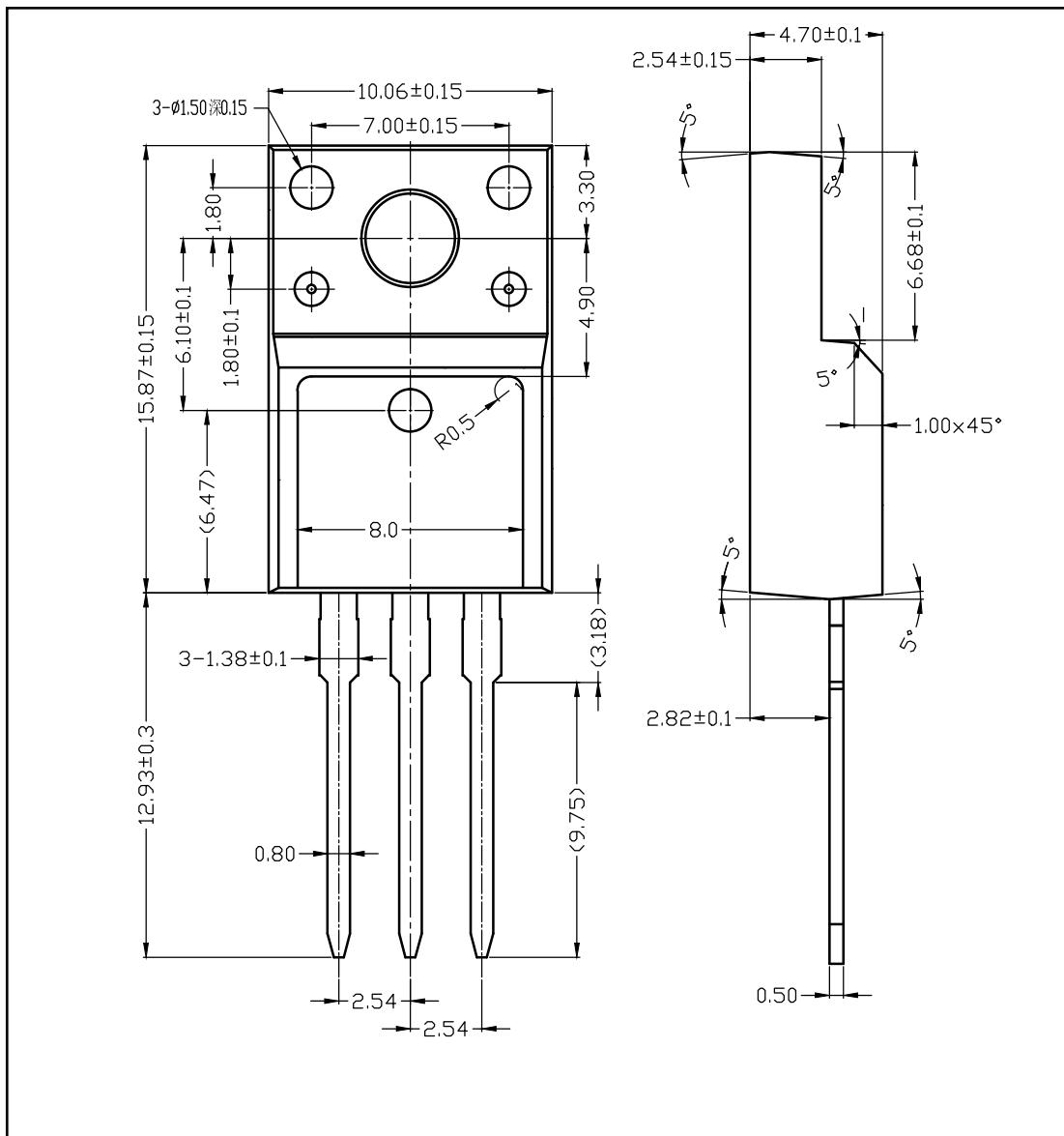
Switching Test Circuit & Waveforms



Unclamped Inductive Switching Test Circuit & Waveforms



■ TO-220F-3L PACKAGE OUTLINE DIMENSIONS



■ TO-220-3L PACKAGE OUTLINE DIMENSIONS

