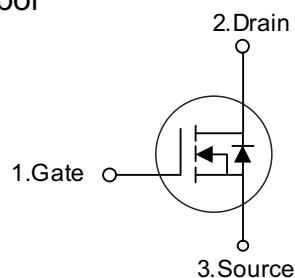


PRODUCT CHARACTERISTICS

VDSS	650V
$R_{DS(on)}$ max(@V _{GS} = 10 V)	0.28Ω
Qg@type	19nC
ID	15A

Symbol

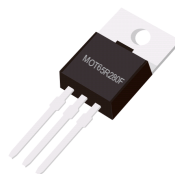


FEATURES

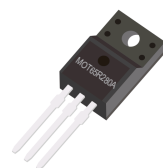
- Ultra low $R_{DS(on)}$
- Ultra low gate charge (typ. $Q_g = 19$ nC)
- 100% UIS tested
- RoHS compliant

APPLICATIONS

- Power facion correction
- Switched mode power supplies
- Uninterruptible power supply



TO-220



TO-220F

ORDER INFORMATION

Order codes		Package	Packing
Halogen-Free	Halogen		
N/A	MOT65R280F	TO-220F	50 pieces/Tube
N/A	MOT65R280A	TO-220	50 pieces/Tube

ABSOLUTE MAXIMUM RATINGS

Parameter	Symbol	Value	Unit
Drain-Source Voltage (V _{GS} = 0V)	V _{DSS}	650	V
Continuous Drain Current	I _D	15	A
Pulsed Drain Current (note1)	I _{DM}	45	A
Gate-Source Voltage	V _{GSS}	± 30	V
Single Pulse Avalanche Energy (note2)	E _{AS}	290	mJ
Avalanche Current (note1)	I _{AS}	2.4	A
MOSFET dv/dt ruggedness, V _{DS} = 0...480V	dv/dt	50	V/ns
Reverse diode dv/dt, V _{DS} = 0...480V, I _{SD} ≤ I _D	dv/dt	15	V/ns
Power Dissipation (T _C = 25°C)	TO-220F	32	W
	TO-220	104	
Operating Junction and Storage Temperature Range	T _J , T _{stg}	-55~+150	°C

THERMAL CHARACTERISTICS

Parameter		Symbol	Value	Unit
Thermal Resistance, Junction-to-Case	TO-220	R _{θJC}	1.2	°C/W
	TO-220F	R _{θJC}	3.9	°C/W
Thermal Resistance, Junction-to-Ambient	TO-220	R _{θJA}	80	°C/W
	TO-220F	R _{θJA}	62	°C/W



■ ELECTRICAL CHARACTERISTICS

Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
Drain-Source Breakdown Voltage	V _{(BR)DSS}	V _{GS} = 0V, I _D = 250μA	650	--	--	V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = 650V, V _{GS} = 0V, T _J = 25°C	--	--	1	μA
		V _{DS} = 650V, V _{GS} = 0V, T _J = 150°C	--	--	100	
Gate-Source Leakage	I _{GSS}	V _{GS} = ±30V	--	--	±100	nA
Gate-Source Threshold Voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = 250μA	2.5	--	4.0	V
Drain-Source On-Resistance (Note3)	R _{DS(on)}	V _{GS} = 10V, I _D = 7.5A	--	0.23	0.28	Ω
Forward Transconductance (Note3)	g _{fs}	V _{DS} = 10V, I _D = 7.5A	--	10	--	S
Dynamic						
Input Capacitance	C _{iss}	V _{GS} = 0V, V _{DS} = 50V, f = 1.0MHz	--	1250	--	pF
Output Capacitance	C _{oss}		--	81	--	
Reverse Transfer Capacitance	C _{rss}		--	7.1	--	
Total Gate Charge	Q _g	V _{DD} = 520V, I _D = 15A, V _{GS} = 10V	--	30	--	nC
Gate-Source Charge	Q _{gs}		--	9	--	
Gate-Drain Charge	Q _{gd}		--	10	--	
Turn-on Delay Time	t _{d(on)}	V _{DD} = 400V, I _D = 15A, R _G = 25Ω	--	42	--	ns
Turn-on Rise Time	t _r		--	17	--	
Turn-off Delay Time	t _{d(off)}		--	135	--	
Turn-off Fall Time	t _f		--	6	--	
Drain-Source Body Diode Characteristics						
Continuous Body Diode Current	I _S	T _C = 25°C	--	--	15	A
Pulsed Diode Forward Current	I _{SM}		--	--	45	
Body Diode Voltage	V _{SD}	T _J = 25°C, I _{SD} = 15A, V _{GS} = 0V	--	0.9	1.2	V
Reverse Recovery Time	t _{rr}	V _R = 480V, I _F = I _S , di _F /dt = 100A/μs	--	335	--	ns
Reverse Recovery Charge	Q _{rr}		--	3.4	--	μC
Peak Reverse Recovery Current	I _{rrm}		--	20	--	A

Notes

1. Repetitive Rating: Pulse Width limited by maximum junction temperature
2. $I_{AS} = 2.4A, V_{DD} = 50V, R_G = 25\Omega$, Starting $T_J = 25^\circ C$
3. Pulse Test: Pulse Width $\leq 300\mu s$, Duty Cycle $\leq 1\%$

■ ELECTRICAL CHARACTERISTICS

Figure 1. Output Characteristics

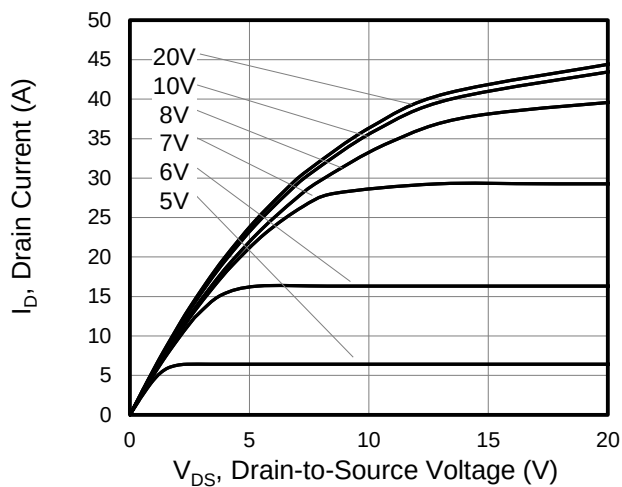


Figure 2. Transfer Characteristics

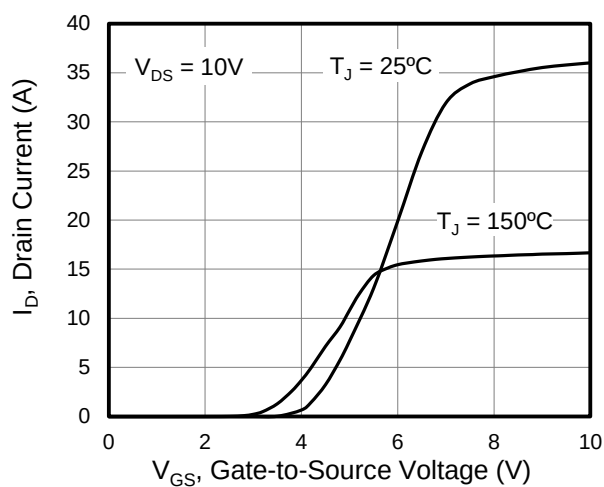


Figure 3. On-Resistance vs. Drain Current

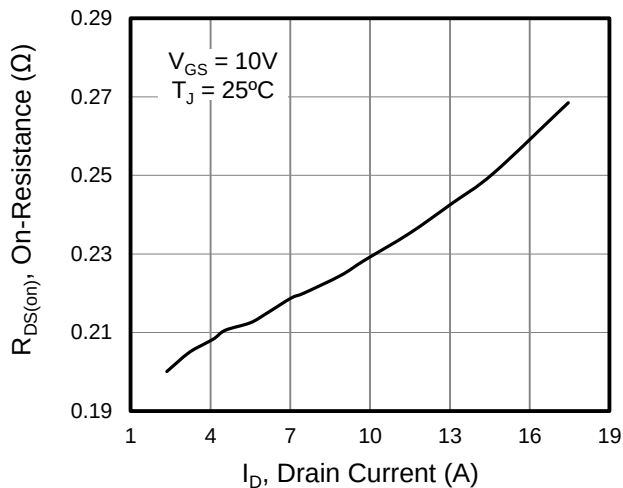


Figure 4. Capacitance

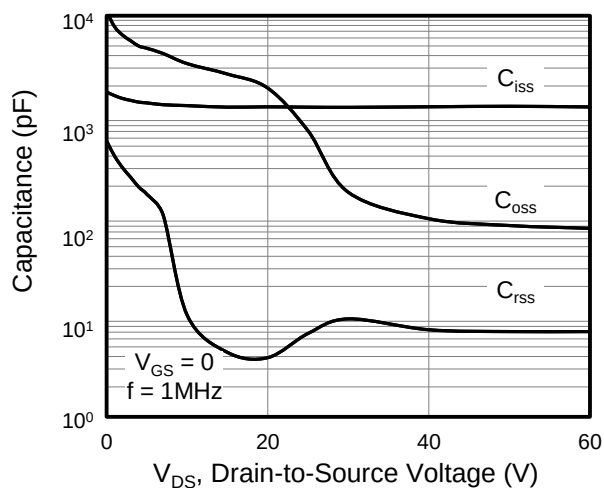


Figure 5. Gate Charge

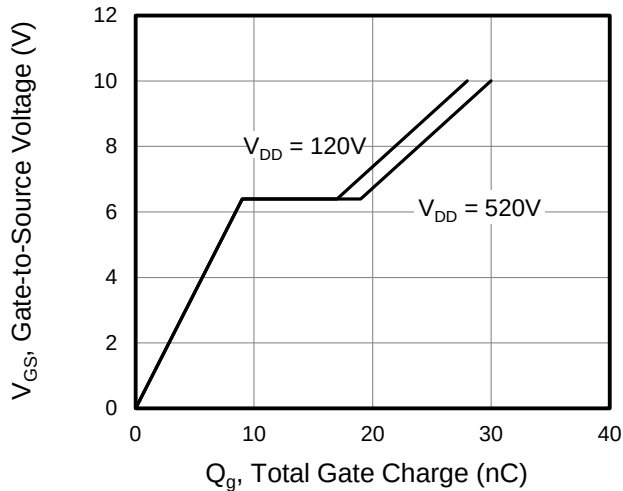
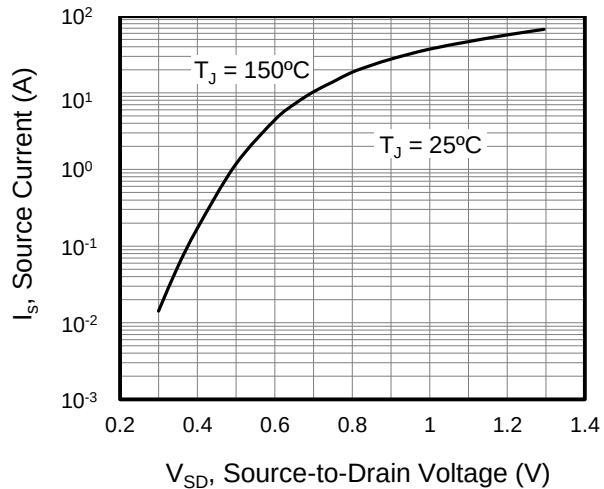


Figure 6. Body Diode Forward Voltage



■ ELECTRICAL CHARACTERISTICS(Cont.)

Figure 7. On-Resistance vs. Junction Temperature

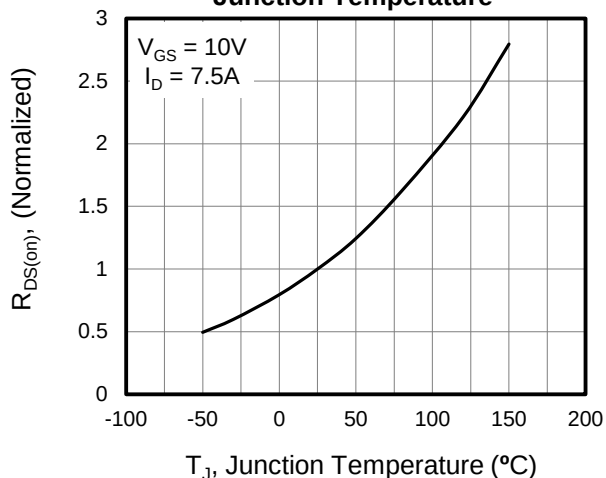


Figure 8. Threshold Voltage vs. Junction Temperature

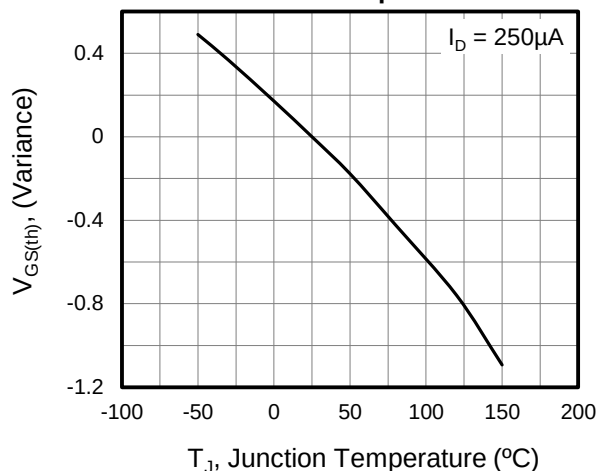


Figure 9. Transient Thermal Impedance TO-220,TO-251,TO-252,TO-262,TO-263

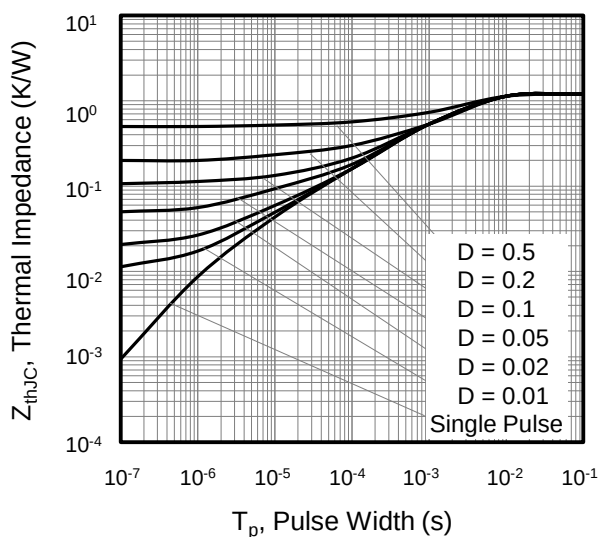


Figure 10. Transient Thermal Impedance TO-220F

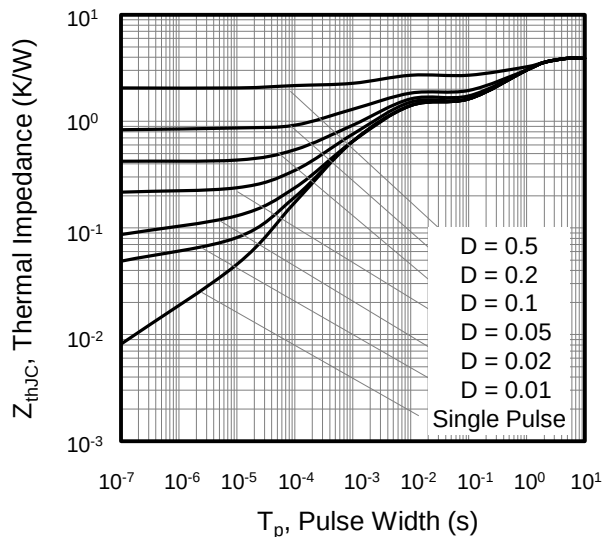


Figure 11. Safe operation area for TO-220,TO-251,TO-252,TO-262,TO-263

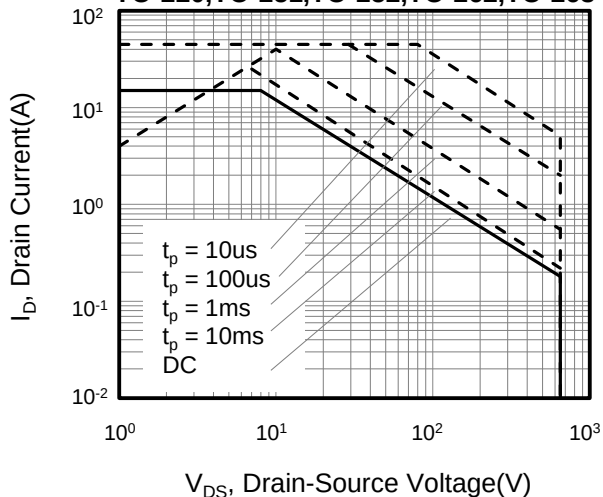
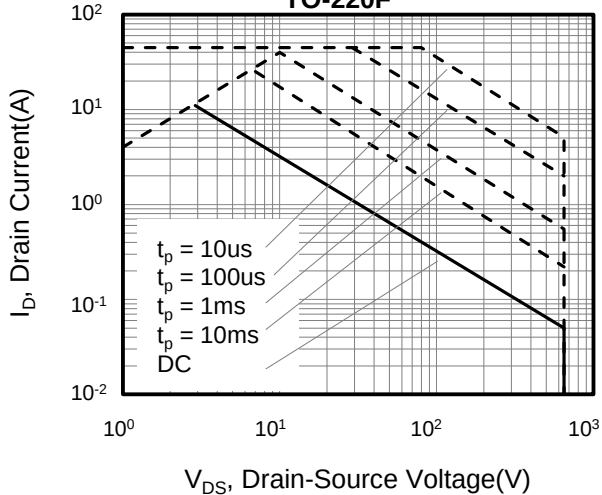


Figure 12. Safe operation area for TO-220F



■ GATE CHARGE TEST CIRCUIT WAVEFORM

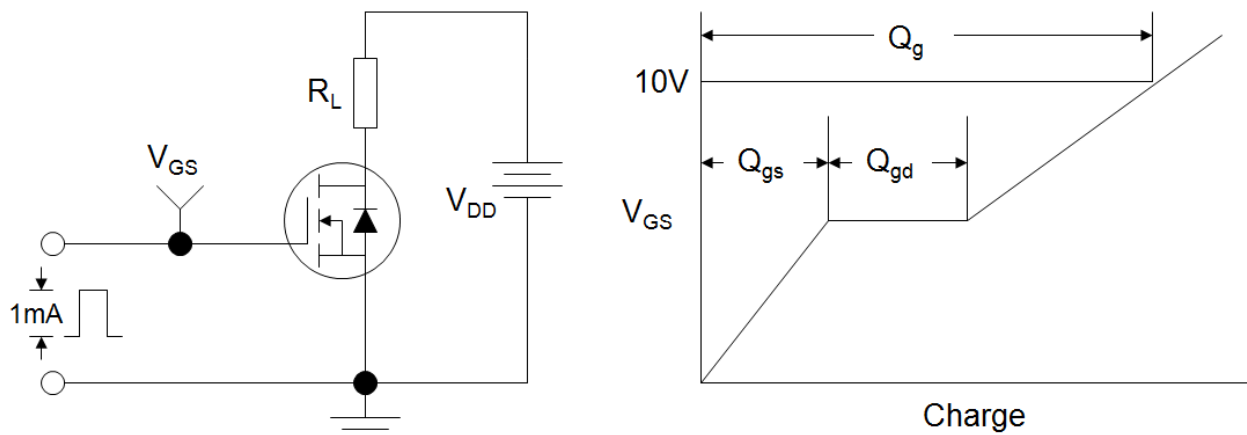


Figure B: Resistive Switching Test Circuit and Waveform

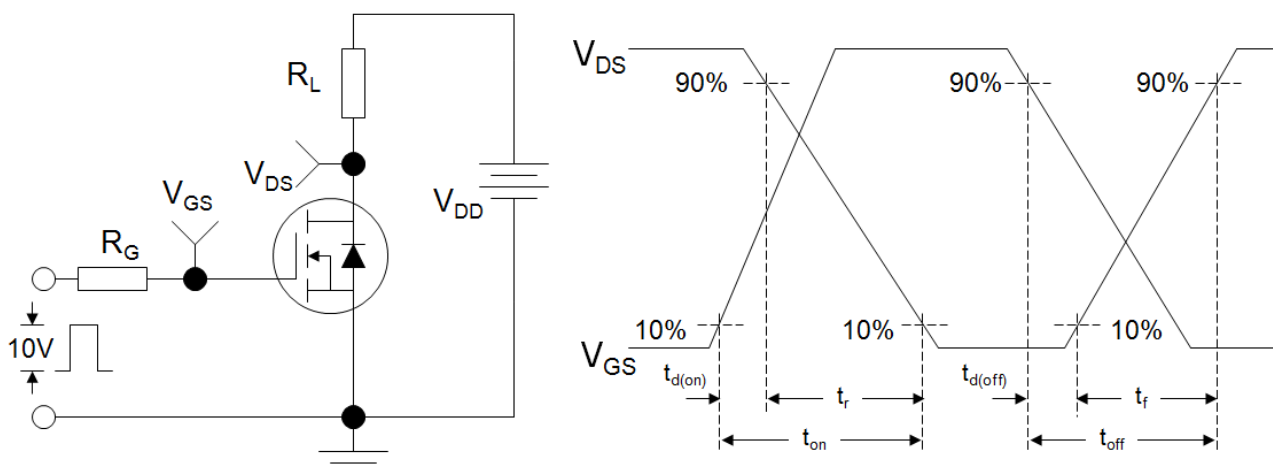
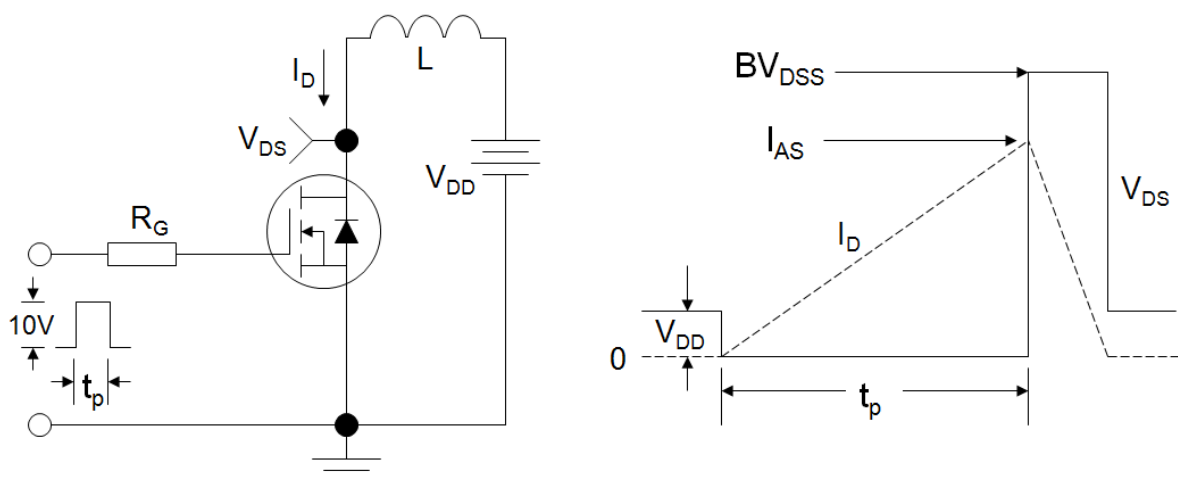
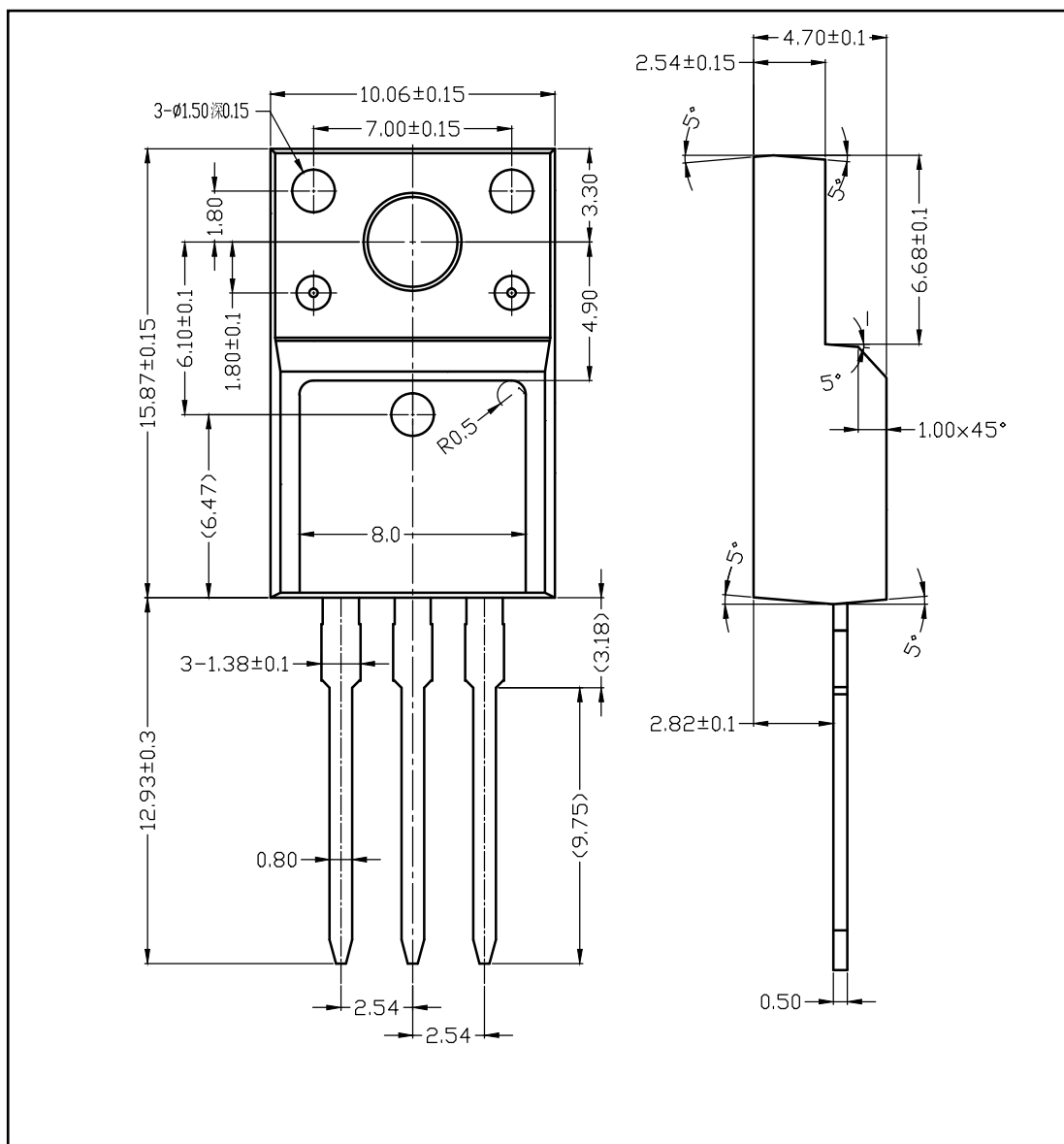


Figure C: Unclamped Inductive Switching Test Circuit and Waveform



■ TO-220F-3L PACKAGE OUTLINE DIMENSIONS



■ TO-220-3L PACKAGE OUTLINE DIMENSIONS

