

GLF71301 Nano-Current Consumed, IoSmart[™] LoadSwitch with Slew Rate Control

Product Brief

DESCRIPTION

The GLF71301 is an ultra-efficiency, 2.0 A rated, Load Switch with integrated slew rate control. The best in class efficiency makes it an ideal choice for use in IoT, mobile, and wearable electronics.

The GLF71301 features an ultra-efficient I_QSmart^{TM} technology that supports the lowest quiescent current (I_Q) and shutdown current (I_{SD}) in the industry. Low I_Q and I_{SD} solutions help designers to reduce parasitic leakage current, improve system efficiency, and increase battery lifetime.

The GLF71301 integrated slew rate control can also enhance system reliability by mitigating bus voltage swings during switching events. Where uncontrolled switches can generate high inrush currents that result in voltage droop and/or bus reset events, the GLF slew rate control specifically limits inrush current during turn-on to minimize voltage droop.

The GLF71301 Load Switch device supports an industry leading wide input voltage range and helps to improve operating life and system robustness. Furthermore, one device can be used in multiple voltage rail applications which helps to simplify inventory management and reduces operating cost.

The GLF71301 Load Switch device is small utilizing a wafer level chip scale package with 4 bumps in a 0.77 mm x 0.77 mm x 0.46 mm die size and a 0.4 mm bump pitch.

FEATURES

- Ultra-Low I_Q: 1 nA Typ @ 5.5 V_{IN}
- Ultra-Low I_{SD}: 19 nA Typ @ 5.5 V_{IN}
- Low $R_{ON} = 34 \text{ m}\Omega \text{ Typ.} @ 5.5 V_{IN}$
- IOUT Max = 2.0 A
- Wide Input Range: 1.1 V to 5.5 V
 6 Vabs max
- Controlled Rise Time: 430 us at 3.3 VIN
- Internal EN Pull-Down Resistor
- Integrated Output Discharge Switch
- Ultra-Small: 0.77 mm x 0.77 mm

APPLICATIONS

- Wearables
- Data Storage, SSD
- Mobile Devices
- Low Power Subsystems



0.77 mm x 0.77 mm x 0.46 mm WLCSP



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PACKAGE OUTLINE



TAPE AND REEL INFORMATION

INTEGRATED POWER



Device	Package	Pins	SPQ	Reel Diameter(mm)	Reel Width W1	A0	В0	К0	Ρ	w	Pin1
GLF71301	WLCSP	4	4000	180	9	0.85	0.85	0.59	4	8	Q1

Remark:

- A0: Dimension designed to accommodate the component width
- B0: Dimension designed to accommodate the component length
- C0: Dimension designed to accommodate the component thickness
- W: Overall width of the carrier tape
- P: Pitch between successive cavity centers