

Description

The SR05LC is a 2-line ultra-low capacitance TVS diode array, utilizing leading monolithic silicon technology to provide fast response time and low ESD clamping voltage, making this device an ideal solution for protecting voltage sensitive high-speed data lines. The SR05LC has a very low capacitance with a typical value at 0.3pF, and complies with the IEC 61000-4-2 (ESD) standard with ±15kV air and ±8kV contact discharge. It is assembled into a 4-pin SOT-143 lead-free package. The small size, very low capacitance and high ESD surge protection make SR05LC an ideal choice to protect cell phone, digital video interfaces, high speed data ports, and many other portable applications.

Features

- Ultra low capacitance: 0.3pF typical
- Ultra low leakage: nA level
- Operating voltage: 5V
- Low clamping voltage
- Complies with following standards:
 - IEC 61000-4-2 (ESD) immunity test
 Air discharge: ±25kV

Contact discharge: ±20kV

- IEC61000-4-5 (Lightning) 5A (8/20µs)
- RoHS Compliant

Dimensions & Symbol (Unit: mm Max)





Pin Schematic

Mechanical Characteristics

- Package: SOT-143
- Lead Finish: Matte Tin
- Case Material: "Green" Molding Compound.
- UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 3 per J-STD-020
- Terminal Connections: See Diagram Below
- Marking Information: See Below

Applications

- Cellular Handsets and Accessories
- Notebooks and Handhelds
- Personal Digital Assistants
- Portable Instrumentation
- Digital Cameras
- Peripherals
- Audio Players, Keypads, Side Keys, LCD
- USB 2.0

Marking information



Details marking code reference customer approval list

Ordering Information

Part Number	Packaging	Reel Size	
SR05LC	3000/Tape & Reel	7 inch	

Absolute Maximum Ratings (T_A=25°C unless otherwise specified)

Parameter	Symbol	Value	Unit
Peak Pulse Power (8/20µs)	Ppk	75	W
Peak Pulse Current (8/20µs)	lpp	5	А
ESD per IEC 61000-4-2 (Air)		±25	
ESD per IEC 61000-4-2 (Contact)	Vesd	±20	kV
Operating Temperature Range	TJ	-55 to +125	°C
Storage Temperature Range	Tstg	−55 to +150	°C

Electrical Characteristics (T_A=25°C unless otherwise specified)

Parameter	Symbol	Min	Тур	Мах	Unit	Test Condition
Reverse Working Voltage	VRWM			5	V	Any I/O pin to ground
Breakdown Voltage	VBR	6			V	IT = 1mA, any I/O pin to ground
Reverse Leakage Current	I _R			0.5	μA	VRWM = 5V, any I/O pin to ground
Clamping Voltage	Vc			10	V	IPP = 1A (8 x 20µs pulse), any I/O pin to ground
Clamping Voltage	Vc			15	V	IPP = 5A (8 x 20µs pulse), any I/O pin to ground
Junction Capacitance	CJ		0.3	0.4	pF	VR = 0V, f = 1MHz, between I/O pins
Junction Capacitance	CJ			0.8	pF	VR = 0V, f = 1MHz, any I/O pin to ground

Note 1: I/O pins are pin 2 & 3

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Typical Performance Characteristics (T_A=25°C unless otherwise Specified)



Junction Capacitance vs. Reverse Voltage



Clamping Voltage vs. Peak Pulse Current



8 X 20µs Pulse Waveform



Peak Pulse Power vs. Pulse Time



Power Derating Curve





Soldering parameters

Reflow Condition		Pb-Free assembly (see FIG.2)
Pre Heat	-Temperature Min (T _{s(min)})	+150℃
	-Temperature Max(T _{s(max)})	+200 ℃
	-Time (Min to Max) (ts)	60-180 secs.
Average ramp up rate (Liquid us Temp (T _L) to peak)		3℃/sec. Max
T _{s(max)} to T _L - Ramp-up Rate		3℃/sec. Max
Reflow	-Temperature(T _L) (Liquid us)	+217℃
	-Temperature(t _L)	60-150 secs.
Peak Temp (T _p)		+260(+0/-5) ℃
Time within 5 $^\circ \!\!\!\!\!^{\rm C}$ of actual Peak Temp (t_p)		30 secs. Max
Ramp-down Rate		6℃/sec. Max
Time 25℃ to Peak Temp (T _P)		8 min. Max
Do not exceed		+260 ℃





Package mechanical data



SIDE VIEW

0.25

	MILLIMETERS		
SYS YM	MIN	NOM	MAX
Α	0.90	-	1.15
A1	0.00	0.05	0.10
A2	0.90	-	1.05
b	0.30	0.40	0.50
b1	0.75	-	0.90
С	0.08	-	0.15
D	2.80	2.90	3.00
d	0.20 Тур		
E	1.20	1.30	1.40
E1	2.25	2.40	2.55
е	0. 95 Typ		
e1	1.80	1.90	2.00
L	0.55 Ref		
L1	0.30	0.40	0.50
Θ	0°	-	8°

Suggested Land Pattern



Unit(mm)

Contact information

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