

CMOS general purpose timer

General Description

The TLC555 is CMOS RC timers providing significantly improved performance over the standard SE/NE555 and 355 timers, while at the same time being direct replacements for those devices in most applications. Improved parameters include low supply current, wide operating supply voltage range, low THRESHOLD, TRIGGER and RESET currents, no crowbaring of the supply current during output transitions, higher frequency performance and no requirement to decouple CONTROL VOLTAGE for stable operation. Specifically, the TLC555 is stable controller capable of producing accurate time delays of frequencies.

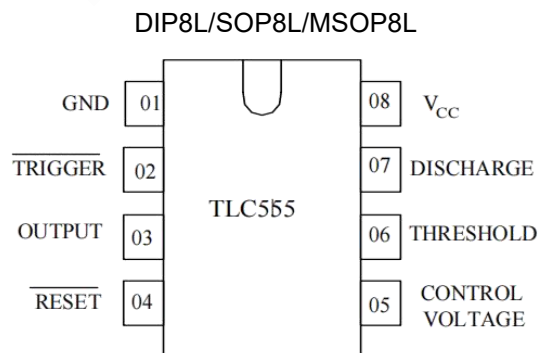
Features

- Exact equivalent in most cases for SE/NE555.
- Low Supply Current.
- High speed operation – 500 kHz guaranteed.
- Wide operation supply voltage range – 2 to 18 volts.
- Timing from microseconds through hours.
- Operates in both astable and monostable modes.
- Adjustable duty cycle.
- High output source/sink driver can drive TTL/CMOS

ORDERING INFORMATION

DEVICE	Package Type	MARKING	Packing	Packing Qty
TLC555PG	DIP8L	TLC555	TUBE	2000pcs/box
TLC555DRG	SOP8L	TLC555	REEL	2500pcs/reel
TLC555DGKRG	MSOP8L	C555	REEL	3000pcs/reel

PIN ASSIGNMENT



TRUTH TABLE

THRESHOLD	TRIGGER	RESET	OUTPUT	DISCHARGE
X	X	L	L	ON
$> 2/3 V_{CC}$	$> 1/3 V_{CC}$	H	L	ON
$< 2/3 V_{CC}$	$> 1/3 V_{CC}$	H	STABLE	STABLE
X	$< 1/3 V_{CC}$	H	H	OFF

MAXIMUM RATINGS AND RECOMMENDED OPERATING CONDITIONS

Parameter, unit	Symbol	Recommended operating conditions		Maximum ratings	
		Value		Value	
		min	max	min	max
Supply Voltage, V	V _{CC}	2.0	18.0	0	18.0
Output Current, mA	I _o	-	20	-	100
Input Voltage, V	V _{TH} , V _{TRIG} , V _{RST}	-	-	-0.3	V _{CC} +0.3
Power Dissipation, mW	P _D	-	-	-	200
Operating Temperature, °C	T _{OPR}	-20	70	-20	85
Storage Temperature, °C	T _{STG}	-	-	-65	150
Lead Temperature, 1 mm from Case for 10 Seconds, °C	T _{SOLDER}	-	-	-	260

DC ELECTRICAL CHARACTERISTICS (Voltages Referenced to GND)

Parameter, units	Symbol	Test Conditions		Value		Temperature, °C
		I _{OL} , I _{OH}	V _{CC} , B	min	max	
Threshold Voltage, V	V _{TH}		5.0	0.65 V _{CC}	0.70 V _{CC}	25 10
				0.60 V _{CC}	0.80 V _{CC}	-20, 70
Trigger voltage, V	V _{TRIG}		5.0	0.31 V _{CC}	0.36 V _{CC}	25 10
				0.28 V _{CC}	0.40 V _{CC}	-20, 70
Reset voltage, V	V _{RST}		2.0	0.4	1.0	25±10
			18.0			
			2.0	0.2	1.5	-20, 70
			18.0			
Control Voltage Lead, V	V _{CV}			0.65 V _{CC}	0.69 V _{CC}	25 10
				0.60 V _{CC}	0.80 V _{CC}	-20, 70
Output voltage Low, V	V _{OL}	I _{OL} = 3.2 mA	5.0		0.4	25±10
		I _{OL} = 20 mA	15.0		1.0	
		I _{OL} = 3.2 mA	5.0		0.6	-20, 70
		I _{OL} = 20 mA	15.0		1.5	
Output voltage High, V	V _{OH}	I _{OH} = -0.8 mA	5.0	4.0		25±10
			15.0	14.3		
			5.0	3.5		-20, 70
			15.0	14.0		
Supply Current, μA	I _{CC}		2.0		200	25±10
			18.0		300	
			2.0		400	-20, 70
			18.0		600	

AC ELECTRICAL CHARACTERISTICS

Parameter, unit	Symbol	Test Conditions		Value		Temperature, °C
		R_L, C_L	V_{CC}, V	Min	Max	
Rise (Fall) Time of Output, ns	t_{THL}, t_{TLH}	$R_L = 10 M \Omega, C_L = 10 pF$	5.0	35 70	75 150	25 10 -20, 70
Guaranteed Max Osc Freq, kHz	f_{MAX}	Astable Operation	2.0- 18.0	500 200		25 10 -20, 70
Initial accuracy, %				5		
Drift with Temperature, %/°C	of	$R_L = 1 - 100 k \Omega, C_L = 0.1 \mu F$	5.0		0.02	-20, 70
			10.0		0.03	
			15.0		0.06	
Drift with Supply Voltage, %/B	Δf		5.0		3 6	25 10 -20, 70

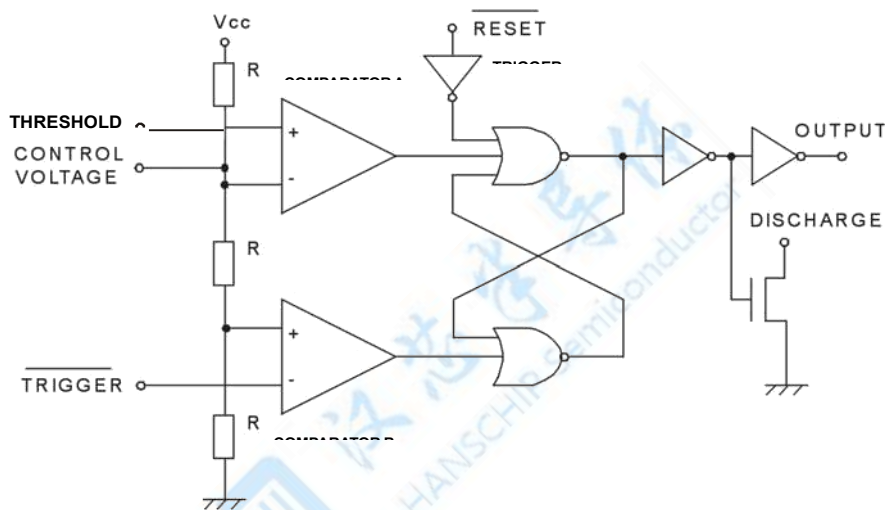


Figure 1. Block Diagram

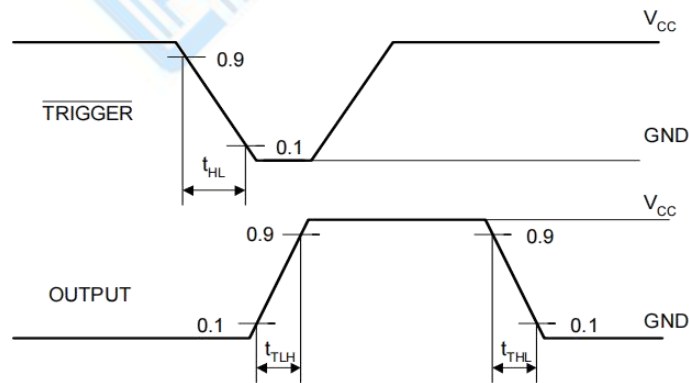
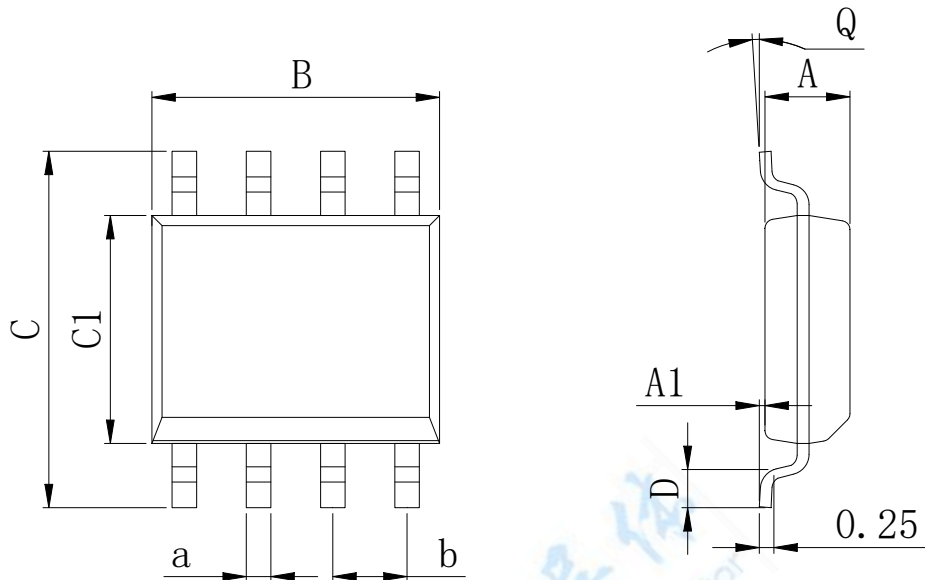


Figure 2. Switching Waveforms

Physical Dimensions

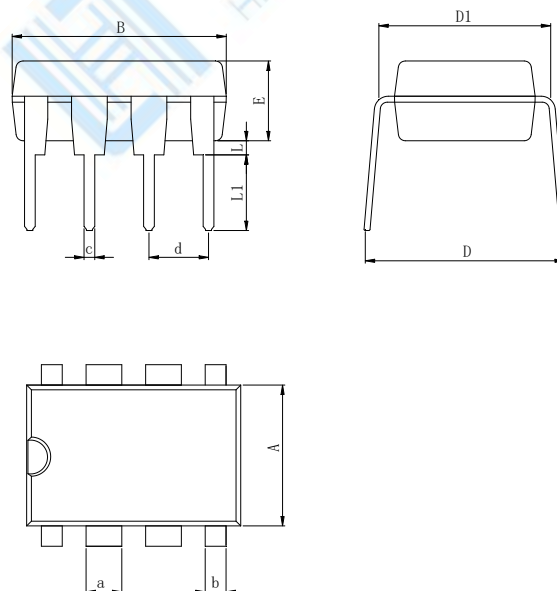
SOP8 (150mil)



Dimensions In Millimeters(SOP8)

Symbol:	A	A1	B	C	C1	D	Q	a	b
Min:	1.35	0.05	4.90	5.80	3.80	0.40	0°	0.35	1.27 BSC
Max:	1.55	0.20	5.10	6.20	4.00	0.80	8°	0.45	

DIP8

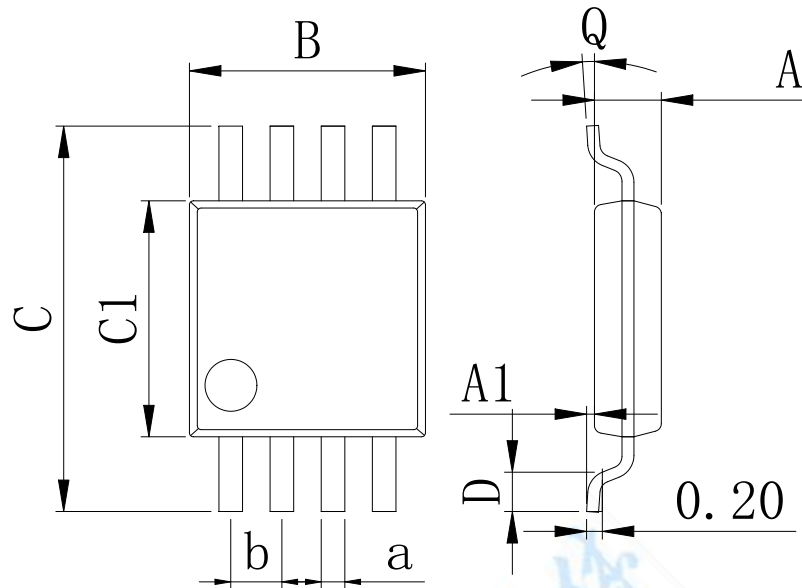


Dimensions In Millimeters(DIP8)

Symbol:	A	B	D	D1	E	L	L1	a	b	c	d
Min:	6.10	9.00	8.40	7.42	3.10	0.50	3.00	1.50	0.85	0.40	2.54 BSC
Max:	6.68	9.50	9.00	7.82	3.55	0.70	3.60	1.55	0.90	0.50	

Physical Dimensions

MSOP8


Dimensions In Millimeters(MSOP8)

Symbol:	A	A1	B	C	C1	D	Q	a	b
Min:	0.80	0.05	2.90	4.75	2.90	0.35	0°	0.25	0.65 BSC
Max:	0.90	0.20	3.10	5.05	3.10	0.75	8°	0.35	

IMPORTANT STATEMENT:

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