

### General Description

The CMN3401M uses advanced trench technology to provide excellent RDS(ON), low gate charge and operation with gate voltages as low as 2.5V. This device is suitable for use as a Battery protection or in other Switching application.

### Features

- RDS(ON)<60mΩ @ VGS=-10V
- RDS(ON)<75mΩ @ VGS=-4.5V
- Simple drive requirement
- Surface mount package

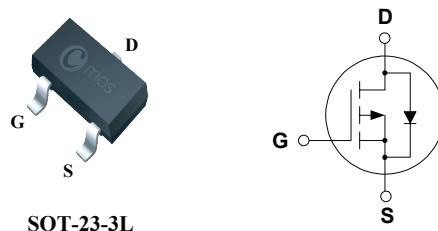
### Product Summary

BVDSS	RDS(ON)	ID
-30V	60mΩ	-4A

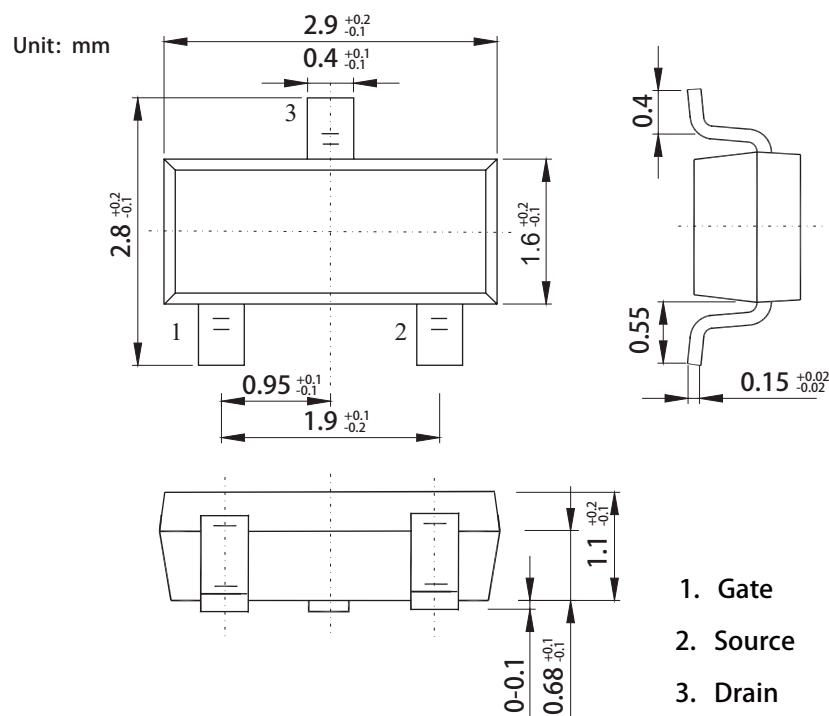
### Applications

- PWM applications
- Load switch
- Power management
- PA Switch

### SOT-23 Pin Configuration



Type	Package	Marking
CMN3401M	SOT-23-3L	X15



## P-Channel Enhancement Mode Field Effect Transistor

## Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
$V_{DS}$	Drain-Source Voltage	-30	V
$V_{GS}$	Gate-Source Voltage	$\pm 20$	V
$I_D@T_C=25^\circ C$	Continuous Drain Current	-4	A
$I_{DM}$	Pulsed Drain Current	-12	A
$P_D@T_C=25^\circ C$	Total Power Dissipation	1.4	W
$T_{STG}$	Storage Temperature Range	-55 to 150	$^\circ C$
$T_J$	Operating Junction Temperature Range	-55 to 150	$^\circ C$

## Thermal Data

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction-ambient (Steady State)	---	90	$^\circ C/W$

Electrical Characteristics ( $T_J=25^\circ C$ , unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$BV_{DSS}$	Drain-Source Breakdown Voltage	$V_{GS}=0V, I_D=-250\mu A$	-30	---	---	V
$R_{DS(ON)}$	Static Drain-Source On-Resistance	$V_{GS}=-10V, I_D=-4A$	---	---	60	$m\Omega$
		$V_{GS}=-4.5V, I_D=-3.5A$	---	---	75	
$V_{GS(th)}$	Gate Threshold Voltage	$V_{GS}=V_{DS}, I_D=-250\mu A$	-0.5	---	-1.5	V
$I_{DSS}$	Drain-Source Leakage Current	$V_{DS}=-30V, V_{GS}=0V$	---	---	-1	$\mu A$
$I_{GSS}$	Gate-Source Leakage Current	$V_{GS}=\pm 20V, V_{DS}=0V$	---	---	$\pm 100$	nA
$g_{fs}$	Forward Transconductance	$V_{DS}=-5V, I_D=-4A$	---	10	---	S
$Q_g$	Total Gate Charge	$I_D=-3.2A$	---	11	---	$nC$
$Q_{gs}$	Gate-Source Charge	$V_{DS}=-24V$	---	2	---	
$Q_{gd}$	Gate-Drain Charge	$V_{GS}=-4.5V$	---	4	---	
$T_{d(on)}$	Turn-On Delay Time	$V_{DS}=-15V, I_D=-3.2A$	---	8	---	$ns$
$T_r$	Rise Time	$R_G=3.3\Omega, R_D=4.6\Omega$	---	16	---	
$T_{d(off)}$	Turn-Off Delay Time	$V_{GS}=-10V$	---	22	---	
$T_f$	Fall Time		---	16	---	
$C_{iss}$	Input Capacitance		---	700	---	$pF$
$C_{oss}$	Output Capacitance	$V_{DS}=-25V, V_{GS}=0V, f=1MHz$	---	100	---	
$C_{rss}$	Reverse Transfer Capacitance		---	80	---	

## Diode Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$V_{SD}$	Diode Forward Voltage	$V_{GS}=0V, I_S=-1A$	---	---	-1	V

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Cmos reserves the right to improve product design, functions and reliability without notice.