

Silicon Carbide (SiC) MOSFET – 13.5 mohm, 750 V, M2, TO-247-4L

NVH4L018N075SC1

Features

- Typ. $R_{DS(on)} = 13.5 \text{ m}\Omega @ V_{GS} = 18 \text{ V}$
Typ. $R_{DS(on)} = 18 \text{ m}\Omega @ V_{GS} = 15 \text{ V}$
- Ultra Low Gate Charge ($Q_{G(tot)} = 262 \text{ nC}$)
- High Speed Switching with Low Capacitance ($C_{oss} = 365 \text{ pF}$)
- 100% Avalanche Tested
- AEC-Q101 Qualified and PPAP Capable
- This Device is Halide Free and RoHS Compliant with exemption 7a, Pb-Free 2LI (on second level interconnection)

Typical Applications

- Automotive On Board Charger
- Automotive DC-DC Converter for EV/HEV
- Automotive Traction Inverter

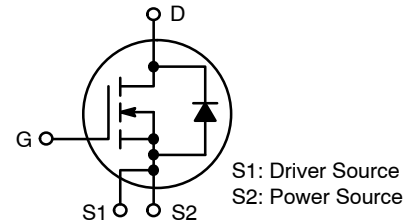
MAXIMUM RATINGS ($T_J = 25^\circ\text{C}$ unless otherwise noted)

Parameter			Symbol	Value	Unit
Drain-to-Source Voltage			V _{DSS}	750	V
Gate-to-Source Voltage			V _{GS}	-8/+22	V
Recommended Operation Values of Gate-to-Source Voltage		T _C < 175°C	V _{GSop}	-5/+18	V
Continuous Drain Current (Note 1)	Steady State	T _C = 25°C	I _D	140	A
Power Dissipation (Note 1)			P _D	500	W
Continuous Drain Current (Note 1)	Steady State	T _C = 100°C	I _D	99	A
Power Dissipation (Notes 1)			P _D	250	W
Pulsed Drain Current (Note 2)	T _C = 25°C		I _{DM}	483	A
Single Pulse Surge Drain Current Capability	T _A = 25°C, t _p = 10 μs, R _G = 4.7 Ω		I _{DSC}	807	A
Operating Junction and Storage Temperature Range			T _J , T _{stg}	-55 to +175	°C
Source Current (Body Diode)			I _S	108	A
Single Pulse Drain-to-Source Avalanche Energy (I _{L(pk)} = 18 A, L = 1 mH) (Note 3)			E _{AS}	162	mJ
Maximum Lead Temperature for Soldering (1/8" from case for 5 s)			T _L	300	°C

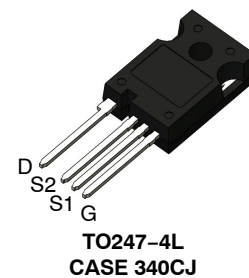
Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

- The entire application environment impacts the thermal resistance values shown, they are not constants and are only valid for the particular conditions noted.
- Repetitive rating, limited by max junction temperature.
- EAS of 162 mJ is based on starting $T_J = 25^\circ\text{C}$; $L = 1 \text{ mH}$, $I_{AS} = 18 \text{ A}$, $V_{DD} = 50 \text{ V}$, $V_{GS} = 18 \text{ V}$.

$V_{(BR)DSS}$	$R_{DS(on)} \text{ MAX}$	$I_D \text{ MAX}$
750 V	18 m Ω @ 18 V	140 A



N-CHANNEL MOSFET



MARKING DIAGRAM



H4L018N075SC = Specific Device Code
A = Assembly Location
Y = Year
WW = Work Week
ZZ = Lot Traceability

ORDERING INFORMATION

Device	Package	Shipping
NVH4L018N075SC1	TO247-4L	30 Units / Tube

Table 1. THERMAL RESISTANCE MAXIMUM RATINGS

Parameter	Symbol	Max	Unit
Junction-to-Case – Steady State (Note 4)	$R_{\theta JC}$	0.3	°C/W
Junction-to-Ambient – Steady State (Notes 4)	$R_{\theta JA}$	40	

4. The entire application environment impacts the thermal resistance values shown, they are not constants and are only valid for the particular conditions noted.

Table 2. ELECTRICAL CHARACTERISTICS ($T_J = 25^\circ\text{C}$ unless otherwise specified)

Parameter	Symbol	Test Condition	Min	Typ	Max	Unit
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OFF CHARACTERISTICS

Drain-to-Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS} = 0\text{ V}, I_D = 1\text{ mA}$	750	–	–	V
Drain-to-Source Breakdown Voltage Temperature Coefficient	$V_{(BR)DSS}/T_J$	$I_D = 1\text{ mA}$, referenced to 25°C	–	0.06	–	V/°C
Zero Gate Voltage Drain Current	I_{DSS}	$V_{GS} = 0\text{ V}, V_{DS} = 750\text{ V}$	$T_J = 25^\circ\text{C}$	–	–	10 μA
			$T_J = 175^\circ\text{C}$	–	–	1 mA
Gate-to-Source Leakage Current	I_{GSS}	$V_{GS} = +18/-5\text{ V}, V_{DS} = 0\text{ V}$	–	–	250	nA

ON CHARACTERISTICS (Note 2)

Gate Threshold Voltage	$V_{GS(TH)}$	$V_{GS} = V_{DS}, I_D = 22\text{ mA}$	1.8	2.7	4.3	V
Recommended Gate Voltage	V_{GOP}		–5	–	+18	V
Drain-to-Source On Resistance	$R_{DS(on)}$	$V_{GS} = 15\text{ V}, I_D = 66\text{ A}, T_J = 25^\circ\text{C}$	–	18		m Ω
		$V_{GS} = 18\text{ V}, I_D = 66\text{ A}, T_J = 25^\circ\text{C}$	–	13.5	18	
		$V_{GS} = 18\text{ V}, I_D = 66\text{ A}, T_J = 175^\circ\text{C}$	–	19		
Forward Transconductance	g_{FS}	$V_{DS} = 10\text{ V}, I_D = 66\text{ A}$	–	40	–	S

CHARGES, CAPACITANCES & GATE RESISTANCE

Input Capacitance	C_{ISS}	$V_{GS} = 0\text{ V}, f = 1\text{ MHz}, V_{DS} = 375\text{ V}$	–	5010	–	pF
Output Capacitance	C_{OSS}		–	365	–	
Reverse Transfer Capacitance	C_{RSS}		–	31	–	
Total Gate Charge	$Q_{G(TOT)}$	$V_{GS} = -5/18\text{ V}, V_{DS} = 600\text{ V}, I_D = 66\text{ A}$	–	262	–	nC
Gate-to-Source Charge	Q_{GS}		–	75	–	
Gate-to-Drain Charge	Q_{GD}		–	72	–	
Gate-Resistance	R_G	$f = 1\text{ MHz}$	–	1.6	–	Ω

SWITCHING CHARACTERISTICS, $V_{GS} = 10\text{ V}$

Turn-On Delay Time	$t_{d(ON)}$	$V_{GS} = -5/18\text{ V}, V_{DS} = 400\text{ V}, I_D = 66\text{ A}, R_G = 2.2\text{ }\Omega$ Inductive load	–	24	–	ns
Rise Time	t_r		–	24	–	
Turn-Off Delay Time	$t_{d(OFF)}$		–	46	–	
Fall Time	t_f		–	9.6	–	
Turn-On Switching Loss	E_{ON}		–	144	–	μJ
Turn-Off Switching Loss	E_{OFF}		–	207	–	
Total Switching Loss	E_{tot}		–	351	–	

SOURCE-DRAIN DIODE CHARACTERISTICS

Continuous Source-Drain Diode Forward Current	I_{SD}	$V_{GS} = -5\text{ V}, T_J = 25^\circ\text{C}$	–	–	108	A
Pulsed Drain-Source Diode Forward Current (Note 2)	I_{SDM}		–	–	483	
Forward Diode Voltage	V_{SD}	$V_{GS} = -5\text{ V}, I_{SD} = 66\text{ A}, T_J = 25^\circ\text{C}$	–	4.5	–	V

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Table 2. ELECTRICAL CHARACTERISTICS ($T_J = 25^\circ\text{C}$ unless otherwise specified) (continued)

Parameter	Symbol	Test Condition	Min	Typ	Max	Unit
SOURCE-DRAIN DIODE CHARACTERISTICS						
Reverse Recovery Time	t_{RR}	$V_{GS} = -5/18\text{ V}$, $I_{SD} = 66\text{ A}$, $dI_S/dt = 1000\text{ A}/\mu\text{s}$	–	28	–	ns
Reverse Recovery Charge	Q_{RR}		–	221	–	nC
Reverse Recovery Energy	E_{REC}		–	19	–	μJ
Peak Reverse Recovery Current	I_{RRM}		–	16	–	A
Charge Time	T_a		–	17	–	ns
Discharge Time	T_b		–	11	–	ns

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

TYPICAL CHARACTERISTICS

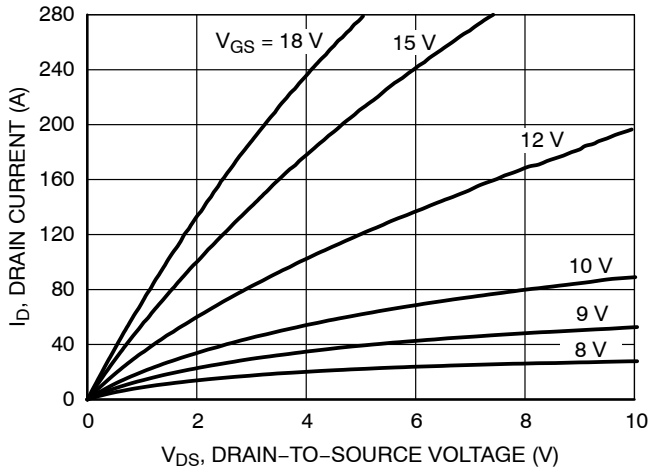


Figure 1. On-Region Characteristics

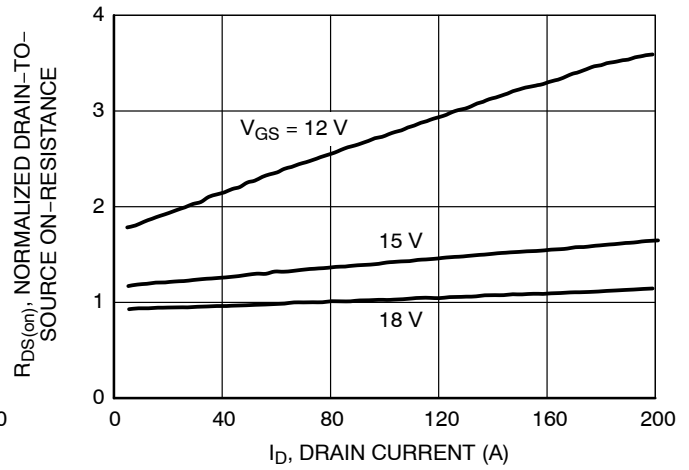


Figure 2. Normalized On-Resistance vs. Drain Current and Gate Voltage

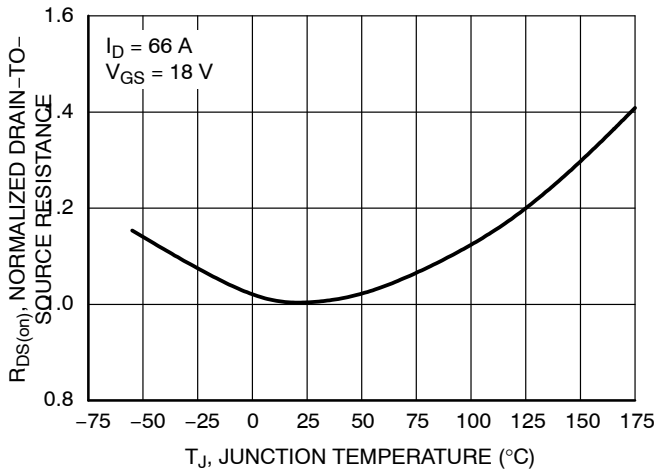


Figure 3. On-Resistance Variation with Temperature

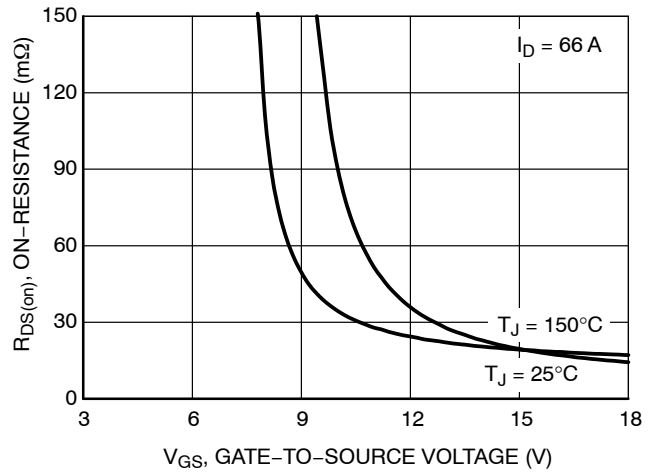


Figure 4. On-Resistance vs. Gate-to-Source Voltage

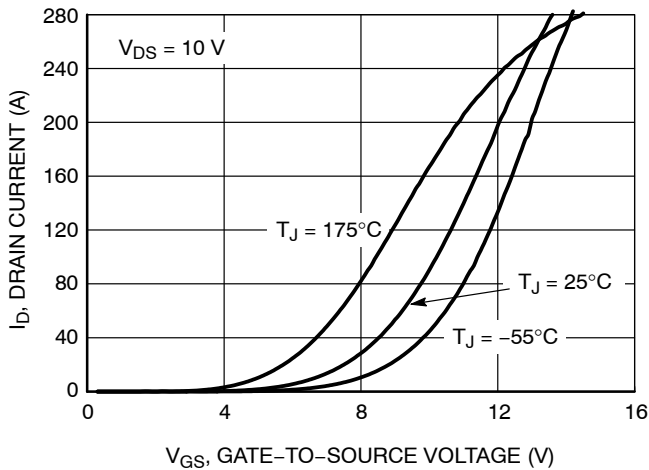


Figure 5. Transfer Characteristics

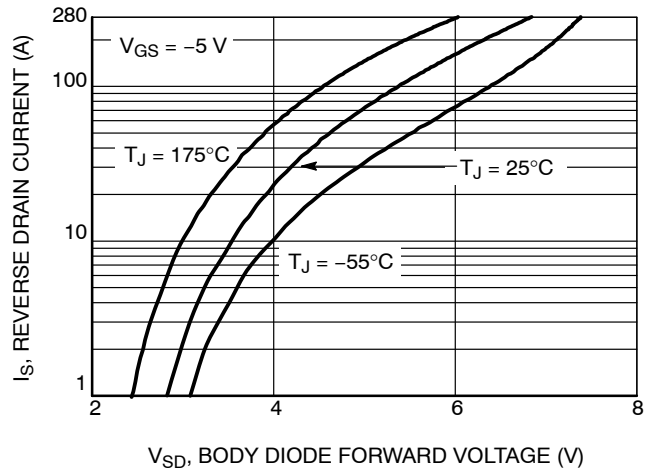


Figure 6. Diode Forward Voltage vs. Current

TYPICAL CHARACTERISTICS (continued)

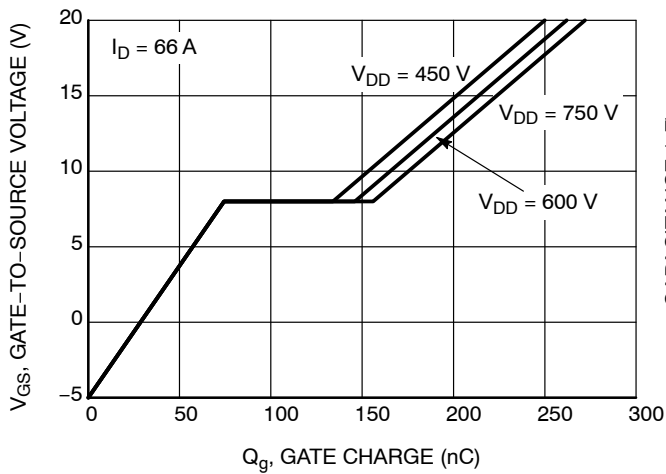


Figure 7. Gate-to-Source Voltage vs. Total Charge

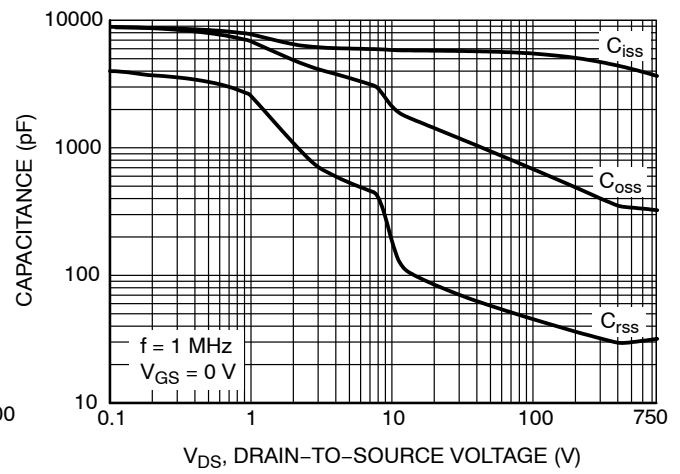


Figure 8. Capacitance vs. Drain-to-Source Voltage

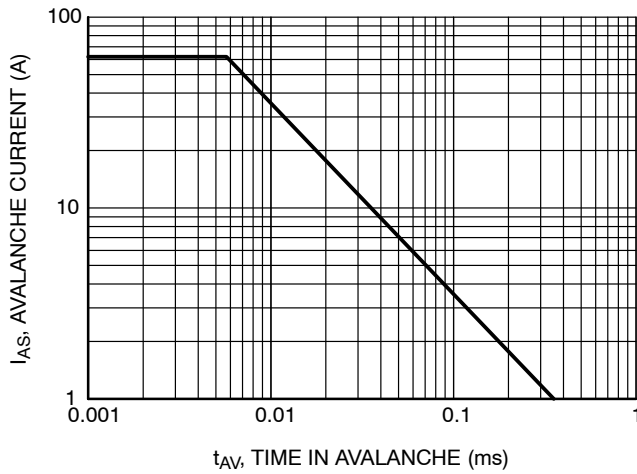


Figure 9. Unclamped Inductive Switching Capability

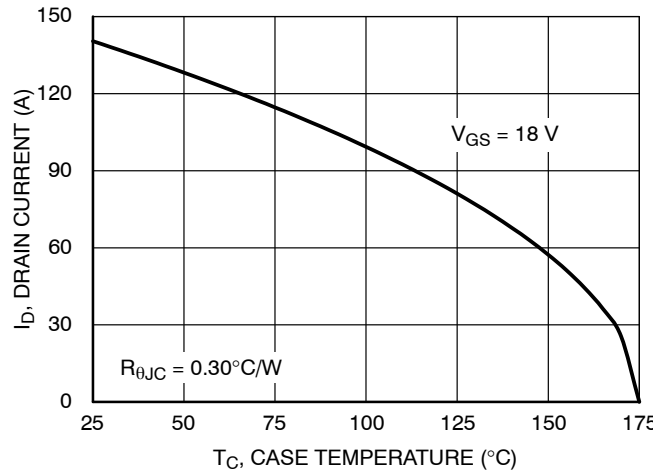


Figure 10. Maximum Continuous Drain Current vs. Case Temperature

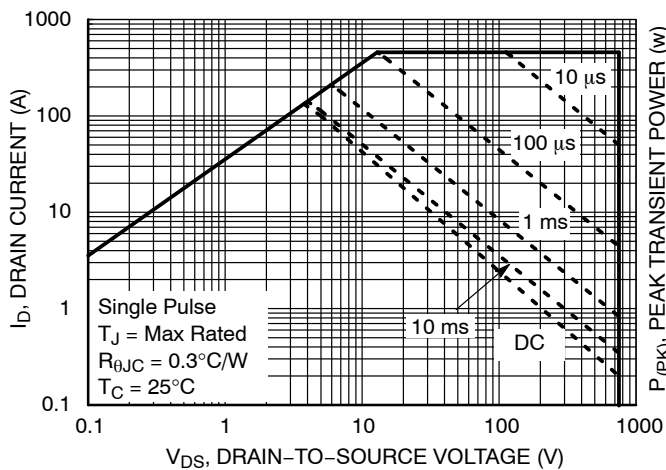


Figure 11. Safe Operating Area

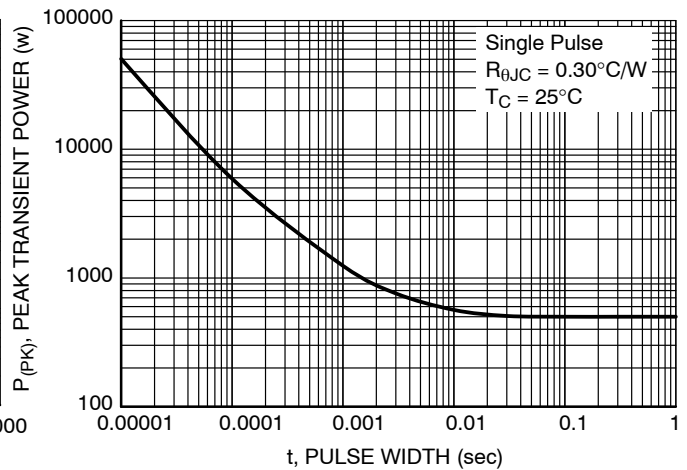


Figure 12. Single Pulse Maximum Power Dissipation

TYPICAL CHARACTERISTICS (continued)

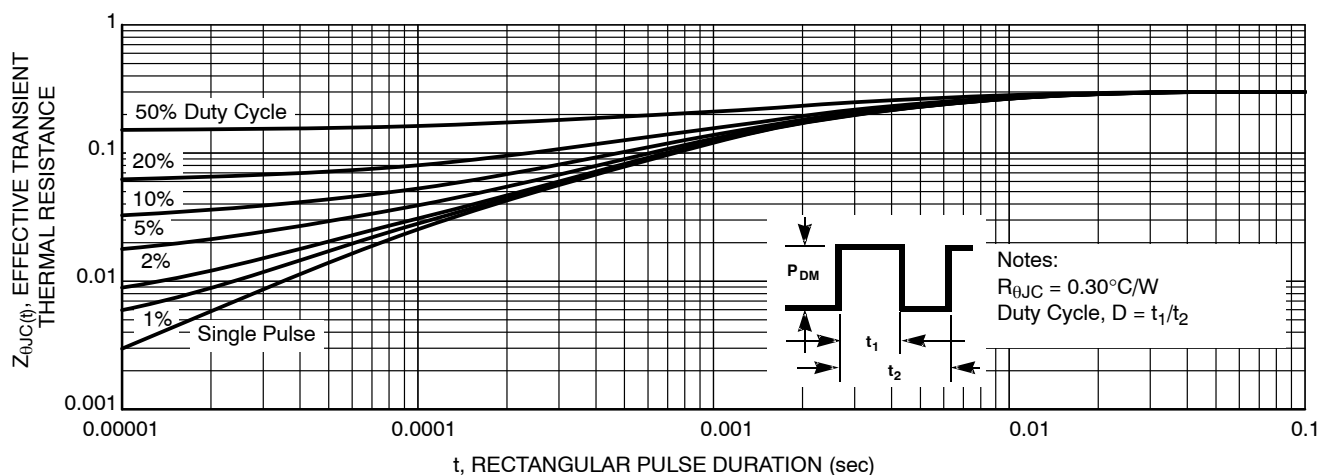


Figure 13. Junction-to-Case Thermal Response

MECHANICAL CASE OUTLINE

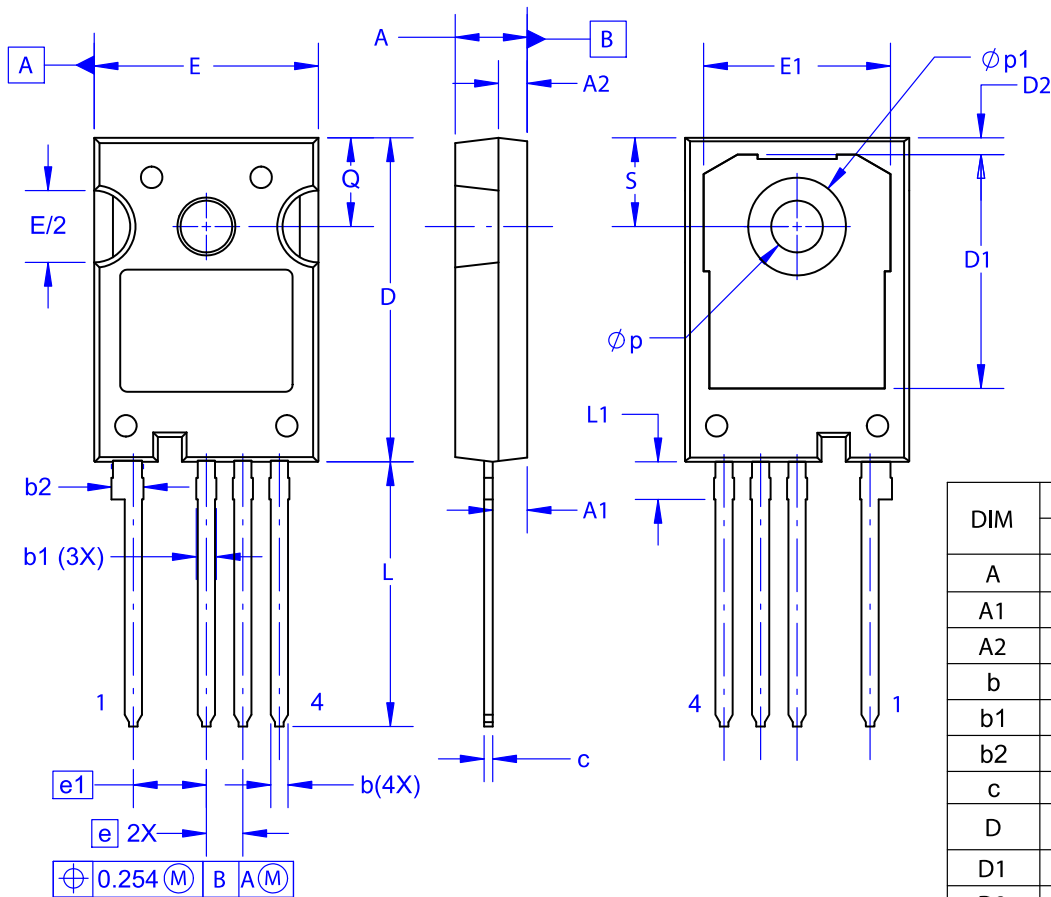
PACKAGE DIMENSIONS

ON Semiconductor®



TO-247-4LD
CASE 340CJ
ISSUE A

DATE 16 SEP 2019



DIM	MILLIMETERS		
	MIN	NOM	MAX
A	4.80	5.00	5.20
A1	2.10	2.40	2.70
A2	1.80	2.00	2.20
b	1.07	1.20	1.33
b1	1.20	1.40	1.60
b2	2.02	2.22	2.42
c	0.50	0.60	0.70
D	22.34	22.54	22.74
D1	16.00	16.25	16.50
D2	0.97	1.17	1.37
e	2.54 BSC		
e1	5.08 BSC		
E	15.40	15.60	15.80
E1	12.80	13.00	13.20
E/2	4.80	5.00	5.20
L	18.22	18.42	18.62
L1	2.42	2.62	2.82
p	3.40	3.60	3.80
p1	6.60	6.80	7.00
Q	5.97	6.17	6.37
S	5.97	6.17	6.37

NOTES:

- NO INDUSTRY STANDARD APPLIES TO THIS PACKAGE.
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