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2013年11月

FAN73833

半桥栅极驱动 IC

特性

- 浮动通道可实现高达 +600V 的自举运行
- 两个通道的 350 mA/650 mA 的典型源电流/灌电流驱动能力
- 将容许负 V_S 摆幅扩展至 -9.8V, 在当 V_{DD}=V_{BS}=15V 时 的信号传播时
- 兼容 3.3V 和 5V 逻辑输入电平
- 输出与输入信号同相
- 两个通道均内置欠压闭锁 (UVLO) 功能
- 内置直通预防电路
- 内置共模 dv/dt 噪声消除电路
- 内部死区时间典型值 400 ns

说明

FAN73833 是半桥栅极驱动 IC,用于驱动 MOSFET 和IGBT,工作电压高达 +600V。

飞兆的高压流程和共模噪声消除技术可使高端驱动器在高 dv/dt 噪声环境下稳定运行。

先进的电平转换电路使高侧栅极驱动器在 V_{BS} =15 V 时可耐受高达 V_{S} =-9.8 V (典型值)时仍然正常工作。

当 V_{DD} 和 V_{BS} 小于指定阈值电压时,两个通道的欠压闭锁 (UVLO) 电路可防止发生故障。

输出驱动器的源电流 / 灌电流典型值分别为 350mA/650mA,适用于各种各样的半桥和全桥逆变器。

8-SOP



应用

- SMPS
- 电机驱动逆变器
- 萤光灯镇流器
- HID 镇流器

订购信息

器件编号	封装	工作温度范围	包装方法
FAN73833M	8-SOP	40°0 ₹ .405°0	塑料管
FAN73833MX	0-30P	-40°C 至 +125°C	卷带和卷盘

典型应用电路

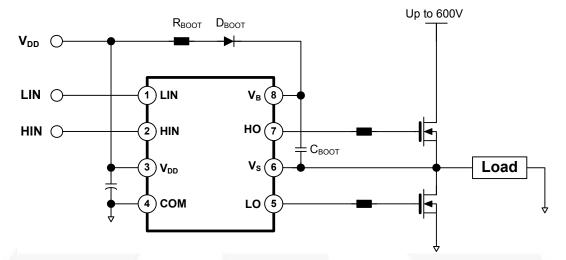


图 1. 半桥应用电路

内部框图

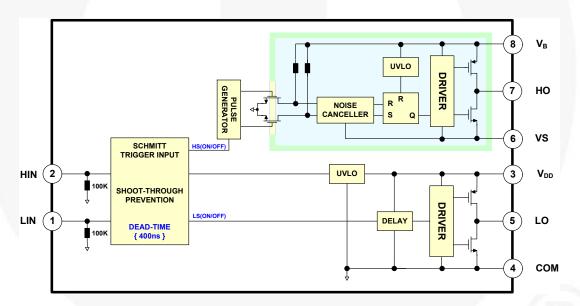


图 2. 功能框图

引脚布局

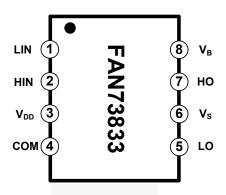


图 3. 引脚布局 (顶视图)

引脚定义

3101707				
引脚号	名称		说明	
1	LIN	低侧驱动器的逻辑输入		
2	HIN	高侧驱动器的逻辑输入		
3	V _{DD}	低端电源电压		
4	COM	逻辑接地和低侧驱动器返回		
5	LO	低侧驱动输出		
6	V _S	高侧浮动电源返回		
7	НО	高侧驱动输出		
8	V _B	高侧浮动电源		

绝对最大额定值

应力若超过绝对最大额定值,可能会损坏器件。在超出推荐的工作条件的情况下,该器件可能无法正常工作,所以不建议让器件在这些条件下长期工作。此外,长期在超出推荐的工作条件下工作,会影响器件的可靠性。绝对最大额定值仅是应力规格值。除非另有规定,否则 $T_A=25^{\circ}C$ 。

符号	参数	最小值	最大值	单位
V _S	高侧偏置电压	V _B -25	V _B +0.3	V
V _B	高侧浮动电源电压	-0.3	625	V
V _{HO}	高侧浮动输出电压 HO	V _S -0.3	V _B +0.3	V
V_{DD}	低侧和逻辑固定电源电压	-0.3	25	V
V_{LO}	低侧输出电压 LO	-0.3	V _{DD} +0.3	V
V _{IN}	逻辑输入电压 (HIN/LIN)	-0.3	V _{DD} +0.3	V
COM	逻辑接地和低侧驱动器返回	V _{DD} -25	V _{DD} +0.3	V
dV _S /dt	容许偏置电压变化速率		50	V/ns
$P_D^{(1)(2)(3)}$	功耗		0.625	W
θ_{JA}	结至环境热阻		200	°C/W
T _J	结温		150	°C
T _{STG}	存储温度	-55	150	°C

注意:

- 1. 安装到 76.2 x 114.3 x 1.6 mm PCB 板 (FR-4 环氧玻璃材料)。
- 2. 参考以下标准:

JESD51-2: 集成电路热测试方法环境条件 - 自然对流 JESD51-3: 含铅表面贴装封装的低有效导热系数测试板

3. 在任何情况下,都不要超过 P_D。

推荐工作条件

推荐的操作条件表明确了器件的真实工作条件。指定推荐的工作条件,以确保器件的最佳性能达到数据表中的规格。飞兆半导体建议不要超过推荐工作条件,也不能按照绝对最大额定值进行设计。

符号	参数	最小值	最大值	单位
V_{B}	高侧浮动电源电压	V _S +11	V _S +20	V
V _S	高侧浮动电源偏置电压	6-V _{DD}	600	V
V_{DD}	低侧电源电压	11	20	V
V _{HO}	高侧 (HO) 输出电压	V _S	V_{B}	V
V_{LO}	低侧 (LO) 输出电压	COM	V_{DD}	V
V _{IN}	逻辑输入电压 (HIN/LIN)	COM	V_{DD}	V
T _A	环境温度	-40	125	°C

电气特性

除非另有规定,否则 V_{BIAS} $(V_{DD},~V_{BS})$ = 15.0 V 和 T_A =25°C。 V_{IN} 和 I_{IN} 参数是以 COM 为参考点的值。 V_O 和 I_O 参数是以 V_S 和 COM 为参考点的值,并分别适用于输出 HO 和 LO。

符号	参数	条件	最小值	典型值	最大值	单位
电源电流部						
I _{QBS}	静态 V _{BS} 电源电流	V _{IN} =0 V 或 5 V		35	100	μΑ
I _{QDD}	静态 V _{DD} 电源电流	V _{IN} =0 V 或 5 V		80	200	μΑ
I _{PBS}	工作 V _{BS} 电源电流	f _{IN} =20 kHz,rms 值		420	750	μΑ
I _{PDD}	工作 V _{DD} 电源电流	f _{IN} =20 kHz,rms 值		420	750	μΑ
I_{LK}	偏置电源的漏电流	V _B =V _S =600 V			10	μΑ
电源部分						
$V_{\mathrm{DDUV}+} \ V_{\mathrm{BSUV}+}$	V _{DD} 和 V _{BS} 电源欠压正向(电压从高 到低)阈值		8.2	9.2	10.1	V
V _{DDUV-} V _{BSUV-}	V _{DD} 和 V _{BS} 电源欠压负向(电压从低到高)阈值		7.2	8.3	9.2	V
V _{DDUVH} V _{BSUVH}	V _{DD} 电源欠压闭锁滞回电压回差			0.9		V
栅极驱动器	输出部分					
V_{OH}	高电平输出电压, V _{BIAS} -V _O	I _O =20 mA			1.0	V
V_{OL}	低电平输出电压, V _O				0.6	V
I _{O+} ⁽⁴⁾	输出高电平短路脉冲电流	V _O =0 V, V _{IN} =5 V和PW<10 μs	250	350		mA
I _{O-} ⁽⁴⁾	输出低电平短路脉冲电流	V_O =15 V, V_{IN} =0 V 和 PW<10 μs	500	650		mA
V_S	IN 信号传播到 HO 时允许的 VS 引脚负电压			-9.8	-7.0	V
逻辑输入部	分 (输入和关断)					
V _{IH}	逻辑 "1" 输入电压		2.5			V
V _{IL}	逻辑 "0" 输入电压				1.2	V
I _{IN+}	逻辑 "1" 输入偏置电流	V _{IN} =5 V		50	100	μΑ
I _{IN-}	逻辑 "0" 输入偏置电流	V _{IN} =0 V	- /		2.0	μΑ
R _{PD}	输入下拉电阻			100	J.	ΚΩ

注:

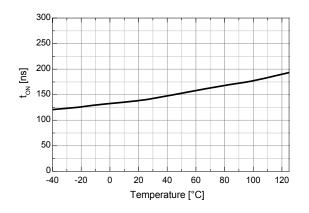
4. 参数由设计者提供

动态电气特性

除非另有规定,否则 V_{BIAS} $(V_{DD},~V_{BS})$ =15.0 $V,~V_{S}$ =COM, C_{L} =1000 pF 和 T_{A} = 25°C。

符号	参数	工作条件	最小值	典型值	最大值	单位
t _{ON}	导通传播延迟时间	V _S =0 V		150	270	ns
t _{OFF}	关断传播延迟时间	V _S =0 V		140	250	ns
t _R	导通上升时间			50	100	ns
t _F	关断下降时间			30	80	ns
DT	死区时间		330	450	580	ns

典型特性



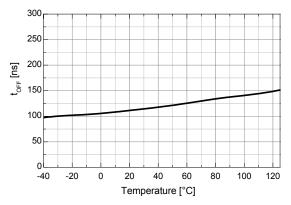
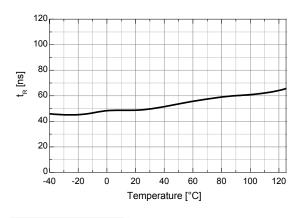


图 4. 导通传播延时 vs 温度

图 5. 关断传播延时 vs 温度



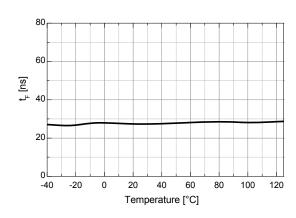
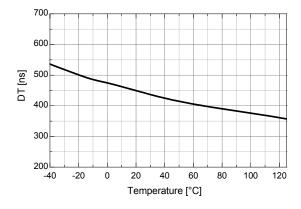


图 6. 导通上升时间 vs 温度

图 7. 关断下降时间 vs 温度



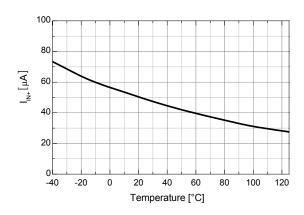
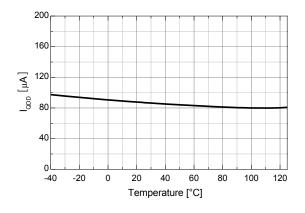


图 8. 死区时间 vs 温度

图 9. 逻辑输入高偏置电流 vs 温度

典型特性 (续)



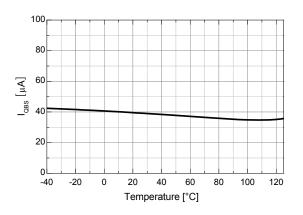
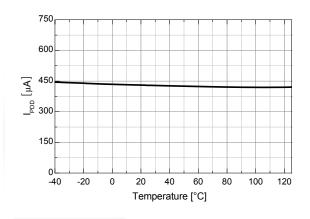


图 10. 静态 V_{DD} 电源电流 与温度的关系

图 11. 静态 V_{BS} 电源电流 与 温度的关系



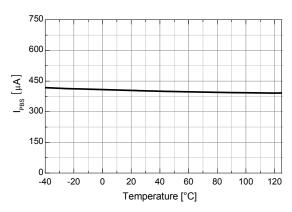
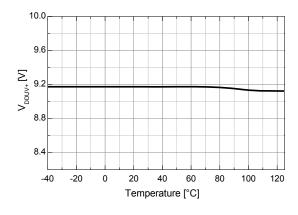


图 12. 工作 V_{DD} 电源电流与温度的关系

图 13. 工作 V_{BS} 电源电流与温度的关系



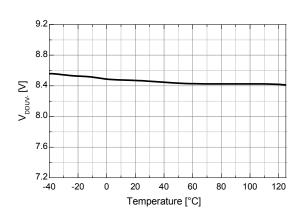
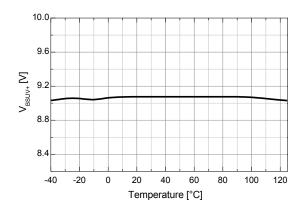


图 14.V_{DD} UVLO+ 与温度的关系

图 15.V_{DD} UVLO- 与 温度的关系

典型特性(续)



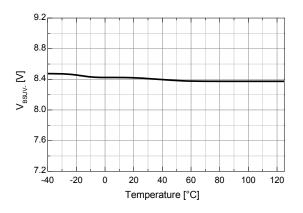
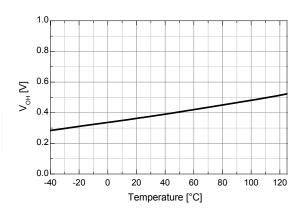


图 16.V_{BS} UVLO+ 与温度的关系

图 17.V_{BS} UVLO- 与 温度的关系



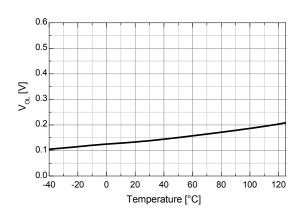
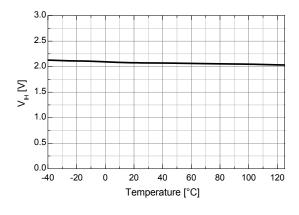


图 18. 高电平输出电压 vs 温度

图 19. 低电平输出电压与 温度的关系



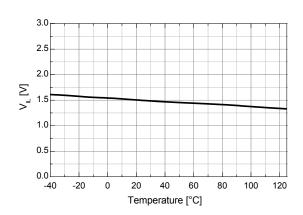


图 20. 逻辑高输入电压 vs 温度

图 21. 逻辑低输入电压 vs 温度

典型特性(续)

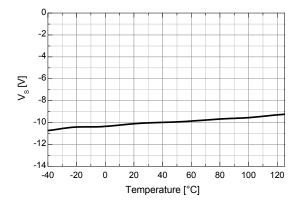


图 22. 容许负 V_S 电压与 温度的关系

应用信息

1. 保护功能

1.1 欠压锁定 (UVLO)

高侧和低侧驱动器的每个通道包括欠压闭锁 (UVLO) 保护电路,用于分别监控电源电压 (V_{DD}) 和自举电容电压 (V_{BS}) 。当 V_{DD} 和 V_{BS} 小于指定阈值电压时,欠压闭锁 (UVLO) 保护电路启动保护可防止发生故障。 UVLO 滞回 特性防止电源变化过程中的抖动。

1.2 直通预防功能

FAN73833 具有直通预防电路,用于监控高侧和低侧控制输入。可防止同时导通高侧和低侧输出,如图 23 和 28 中所示。

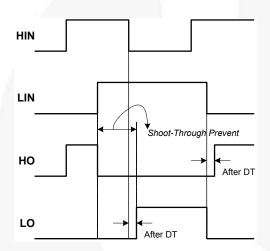


图 23. 直通预防波形

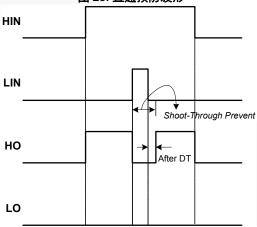


图 24. 直通预防波形

2. 开关时间定义

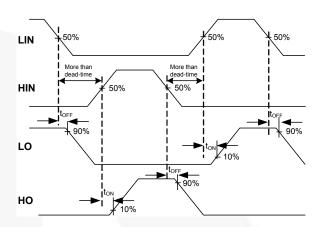


图 25. 开关时间定义

机械尺寸

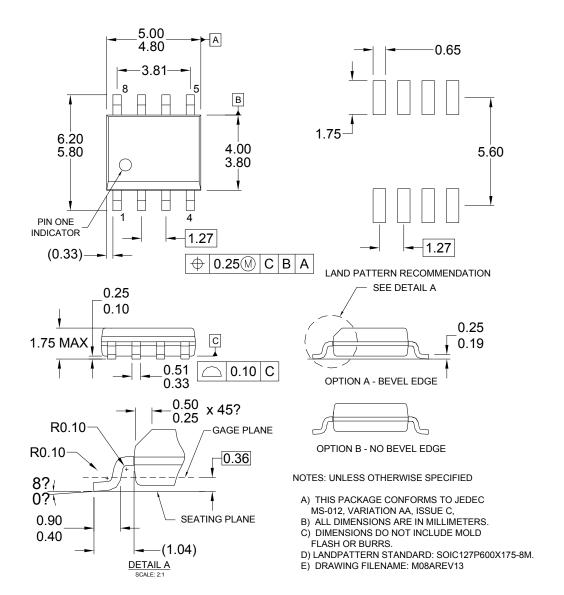


图 26. 8- 引脚式小尺寸封装 (SOP)

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