

N-Ch MOSFET

General Description

The WSM180N15 is the highest performance trench N-ch MOSFET with extreme high cell density, which provide excellent RDSON and gate charge for most of the synchronous buck converter applications.

The WSM180N15 meet the RoHS and Green Product requirement, 100% EAS guaranteed with full function reliability approved.

Features

- Advanced high cell density Trench technology
- Super Low Gate Charge
- Excellent CdV/dt effect decline
- 100% EAS Guaranteed
- Green Device Available

Product Summery

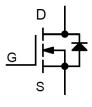
BVDSS	RDSON	ID
150V	6.2mΩ	180A

Applications

- High Frequency Point-of-Load Synchronous
 Buck Converter
- Networking DC-DC Power System

TOLLA-8-XZ Pin Configuration





1:G 2,3,4,5,6,7,8:S

Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
V _{DS}	Drain-Source Voltage	150	V
V _{GS}	Gate-Source Voltage	±20	V
Ι _D	Continuous Drain Current, V _{GS} @ 10V(T _C =25 [°] C)	180	А
I _{DM}	Pulsed Drain Current	550	A
EAS	Single Pulse Avalanche Energy	506	mJ
PD	Total Power Dissipation _C =25 [°] C)	210	W
RθJA	Thermal resistance, junction-ambient	40	°C/W
RθJC	Thermal resistance, junction-case	0.84	°C/W
T _{STG}	Storage Temperature Range -55 to 155		°C
TJ	Operating Junction Temperature Range	-55 to 155	°C



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Electrical Characteristics (T_J=25⁻¹C, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V , I _D =250uA	150			V
R _{DS(ON)}	Static Drain-Source On-Resistance ²	V _{GS} =10V , I _D =30A		6.2	7.5	mΩ
V _{GS(th)}	Gate Threshold Voltage	$V_{GS}=V_{DS}$, $I_{D}=250uA$	2.0	2.9	4.0	V
I _{DSS}	Drain-Source Leakage Current	$V_{\text{DS}}\text{=}100\text{V}$, $V_{\text{GS}}\text{=}0\text{V}$, $T_{\text{J}}\text{=}25^\circ\!\!\mathrm{C}$			1	uA
I _{GSS}	Gate-Source Leakage Current	$V_{GS}=\pm20V$, $V_{DS}=0V$			±100	nA
Qg	Total Gate Charge	V _{DS} =50V , V _{GS} =10V , I _D =20A		72		nC
Q _{gs}	Gate-Source Charge			18		
Q _{gd}	Gate-Drain Charge			10		
T _{d(on)}	Turn-On Delay Time			22		
Tr	Rise Time			115		ns
T _{d(off)}	Turn-Off Delay Time			44		
T _f	Fall Time			105		
C _{iss}	Input Capacitance	V _{DS} =50V , V _{GS} =0V , f=1MHz		5240		
Coss	Output Capacitance			412		pF
C _{rss}	Reverse Transfer Capacitance			30		

Diode Characteristics

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
Is	Continuous Source Current ^{1,6}				150	А
I _{SM}	Pulsed Source Current ^{2,6}	-V _G =V _D =0V , Force Current			500	А
V _{SD}	Diode Forward Voltage ²	V _{GS} =0V , I _S =20A , T _J =25℃			1.3	V

■ Note

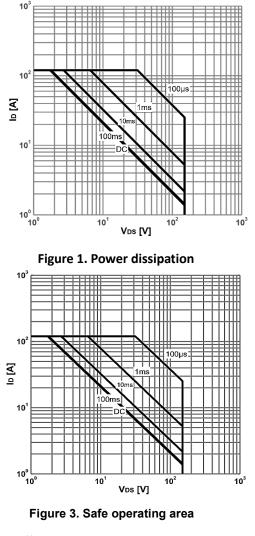
- ¹) Repetitive rating; pulse width limited by max. junction temperature.
- ²) Pd is based on max. junction temperature, using junction-case thermal resistance.
- ³) The value of RθJA is measured with the device mounted on 1 in 2 FR-4 board with 2oz. Copper, in a still air environment with Ta=25 °C.
- 4) VDD=50 V, RG=50 $\Omega,$ L=0.5 mH, starting Tj=25 °C.
- ⁵) Calculated continuous current based on maximum allowable junction temperature.



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Typical Characteristics



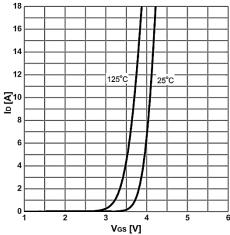


Figure 5. Typ. transfer characteristics

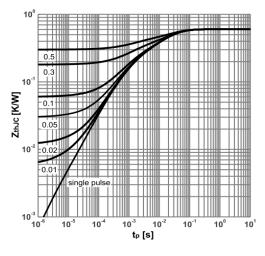


Figure 2. Max. transient thermal impedance

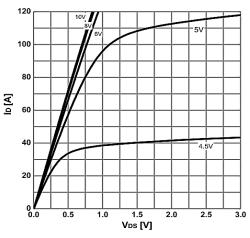


Figure 4. Typ. output characteristics

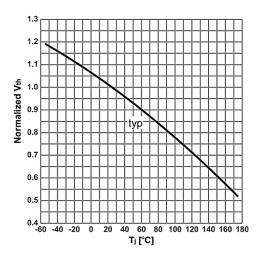


Figure 6. Gate threshold voltage vs. Junction Temperature



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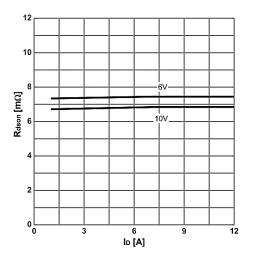


Figure 7. On-state resistance vs. Drain current

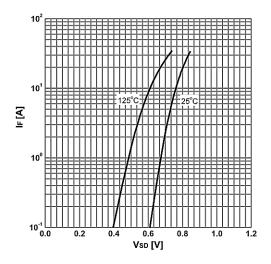


Figure 9. Forward characteristics of reverse diode

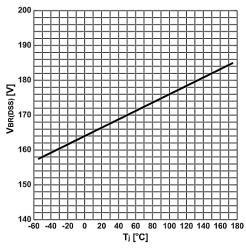


Figure 10: Breakdown Voltage Variation vs. Temperature

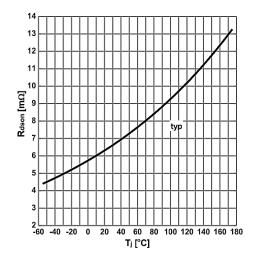


Figure 8. On-state resistance vs. Junction temperature

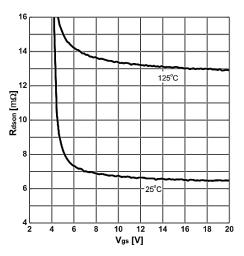


Figure10. On-state resistance vs. Vgs characteristics

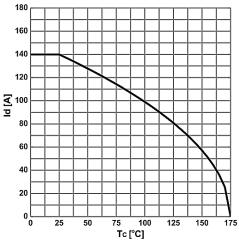


Figure 11: Maximum Drain Current



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