

WCR380N65TF/TG

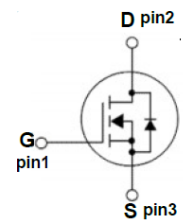
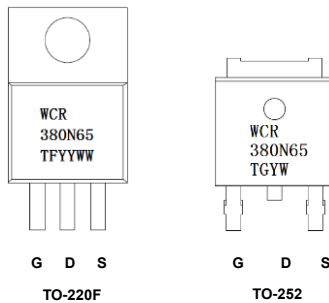
650V N-Channel Super Junction MOSFET

Description

The WCR380N65 series is new generation of high voltage MOSFET family that is utilizing an advanced charge balance mechanism for outstanding low on-resistance and lower gate charge performance. This advanced technology has been tailored to minimize conduction loss, provide superior switching performance, and withstand extreme dv/dt rate and higher avalanche energy. This device is suitable for various AC/DC power conversion in switching mode operation for higher efficiency.

Features

- 700V@ $T_J=150^{\circ}\text{C}$
- Typ. $R_{DS(on)}=0.31\Omega$
- Low gate charge
- 100% avalanche tested
- 100% R_g tested



Order Information

| Device | Package | Marking | Units/Tube | Units/Real |
|------------------|------------|------------------------------|------------|------------|
| WCR380N65TF-3/T | TO-220F | WCR380N65TFYW ⁽¹⁾ | 50 | |
| WCR380N65TG-3/TR | TO-252E-2L | WCR380N65TGYW ⁽²⁾ | | 2500 |

Note 1: WCR380N65TF=Device code ;Y=Year ;W=Week (A~z);
 Note 2: WCR380N65TG=Device code ;Y=Year ;W=Week (A~z);

| Absolute Maximum Ratings $T_A=25^{\circ}\text{C}$ unless otherwise noted | | | | |
|--|-----------------------------------|-----------------|-------------|----------------------|
| Parameter | Symbol | WCR380N65TG | WCR380N65TF | Unit |
| Drain-Source Voltage | V_{DS} | 650 | | V |
| Gate-Source Voltage | V_{GS} | ± 30 | | |
| Continuous Drain Current ^A | $T_C=25^{\circ}\text{C}$ | 11 | 6.1 | A |
| | $T_C=100^{\circ}\text{C}$ | 7.1 | 3.9 | |
| Pulsed Drain Current | I_{DM} | 27 | | A |
| Single Pulsed Avalanche Energy ^B | E_{AS} | 162 | | mJ |
| Power Dissipation | $T_C=25^{\circ}\text{C}$ | 100 | 31.2 | W |
| | Derate above 25°C | 0.8 | 0.25 | |
| Operating and Storage Temperature Range | T_J, T_{STG} | -55~150 | | $^{\circ}\text{C}$ |
| Lead Temperature | T_L | 260 | | $^{\circ}\text{C}$ |
| Thermal Resistance Ratings | | | | |
| Maximum Junction-to-Ambient | $R_{th(ch-A)}$ | 62 ^D | 80 | $^{\circ}\text{C/W}$ |
| Maximum Junction-to-Case | $R_{th(ch-c)}$ | 1.25 | 4 | |

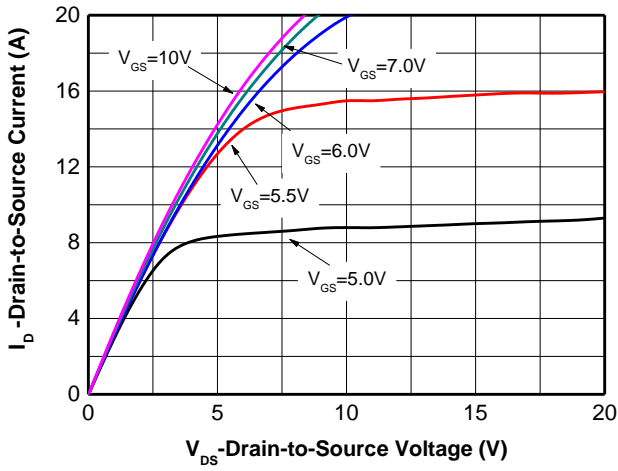
Electronics Characteristics (T_A=25°C, unless otherwise noted)

| Parameter | Symbol | Test Conditions | Min | Typ | Max | Unit |
|--|----------------------------------|---|-----|------|------|------|
| OFF CHARACTERISTICS | | | | | | |
| Drain-to-Source Breakdown Voltage | BV _{DSS} | V _{GS} = 0 V, I _D = 250uA, T _J =25°C | 650 | | | V |
| Zero Gate Voltage Drain Current | I _{DSS} | V _{DS} =650V, V _{GS} = 0V, T _J =25°C | | | 1 | uA |
| Gate-to-source Leakage Current | I _{GSS} | V _{DS} = 0 V, V _{GS} = ±30V | | | ±100 | nA |
| ON CHARACTERISTICS | | | | | | |
| Gate Threshold Voltage | V _{GS(TH)} | V _{GS} = V _{DS} , I _D = 250uA | 2 | 3 | 4 | V |
| Drain-to-source On-resistance | R _{DS(on)} ^C | V _{GS} = 10V, I _D = 5.5A | | 0.31 | 0.38 | Ω |
| DYNAMIC PARAMETERS | | | | | | |
| Input Capacitance | C _{ISS} | V _{GS} = 0 V, f = 1.0 MHz, V _{DS} = 400 V | | 813 | | pF |
| Output Capacitance | C _{OSS} | | | 19.6 | | |
| Reverse Transfer Capacitance | C _{RSS} | | | 1 | | |
| Total Gate Charge | Q _{G(TOT)} | V _{GS} = 10 V, V _{DS} = 400 V, I _D = 11A | | 23 | | nC |
| Gate-to-Source Charge | Q _{GS} | | | 6 | | |
| Gate-to-Drain Charge | Q _{GD} | | | 9 | | |
| Gate resistance | R _g | V _{GS} =0V , F=1MHZ, drain open | | 6.5 | | Ω |
| SWITCHING CHARACTERISTICS | | | | | | |
| Turn-On Delay Time | t _{d(on)} | V _{GS} = 10V, V _{DS} = 400 V, I _D = 5.5A, R _G =10 Ω | | 11.5 | | ns |
| Rise Time | t _r | | | 23.5 | | |
| Turn-Off Delay Time | t _{d(off)} | | | 43 | | |
| Fall Time | t _f | | | 21.5 | | |
| Drain to Source Diode Characteristics and Maximum Ratings | | | | | | |
| Forward Voltage | V _{SD} | V _{GS} = 0 V, I _S = 5.5A | | | 1.5 | V |
| Body-Diode Continuous Current | I _S | | | | 11 | A |
| Body-Diode Pulsed Current | I _{SM} | | | | 27 | A |
| Body Diode Reverse Recovery Time | T _{rr} | I _F =5.5A, dI/dt=100A/us, V _{DS} =400V | | 240 | | nS |
| Body Diode Reverse Recovery Charge | Q _{rr} | | | 1.8 | | uC |
| Peak reverse recovery Current | I _{rrm} | | | 14.9 | | A |

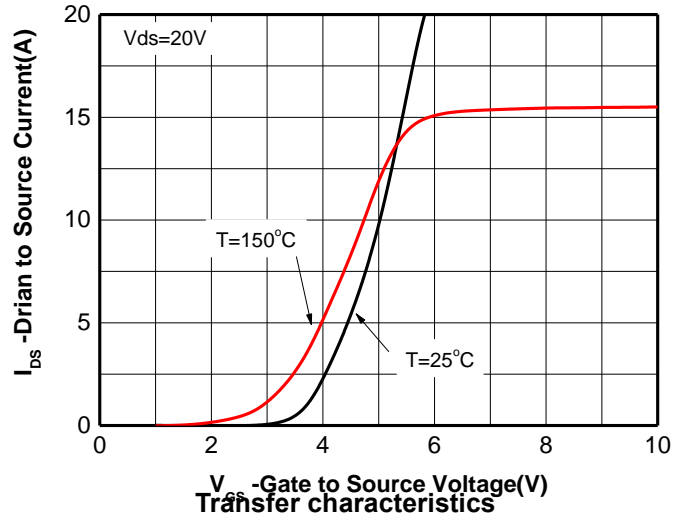
NOTES:

- Drain current limited by maximum junction temperature. Maximum duty cycle D=0.75
- L=100mH, I_{AS}=1.8A, V_{DD}=50V, Starting T_J=25°C
- Pulse Test: Pulse width ≤300us, Duty Cycle ≤2% sensitively Independent of Operating Temperature Typical Characteristics
- These tests are performed with the device mounted on 1 in₂ FR-4 board with 2oz. Copper, in a still air environment with T_A=25°C.

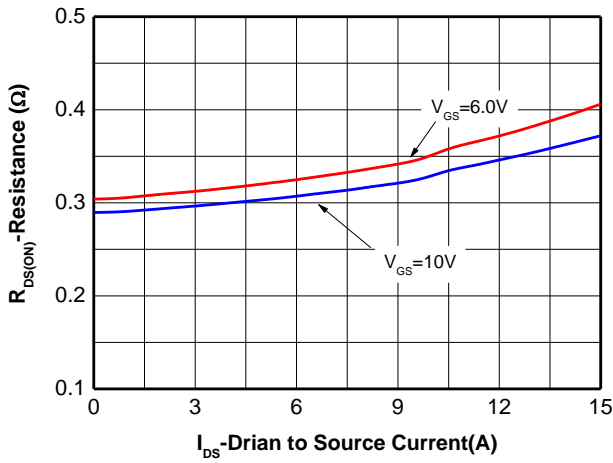
Typical Characteristics ($T_A=25^{\circ}\text{C}$, unless otherwise noted)



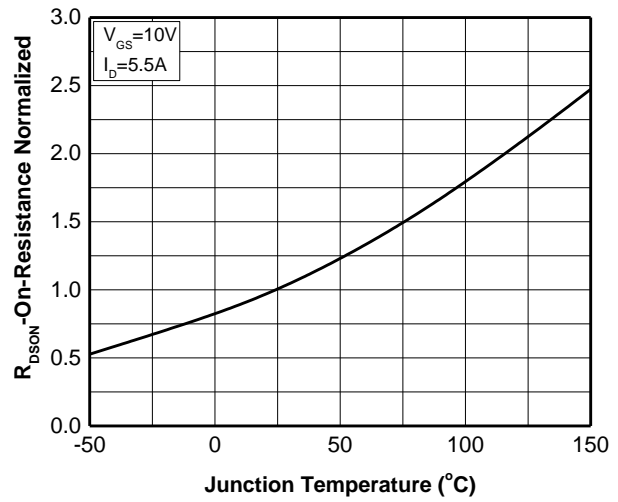
Output characteristics



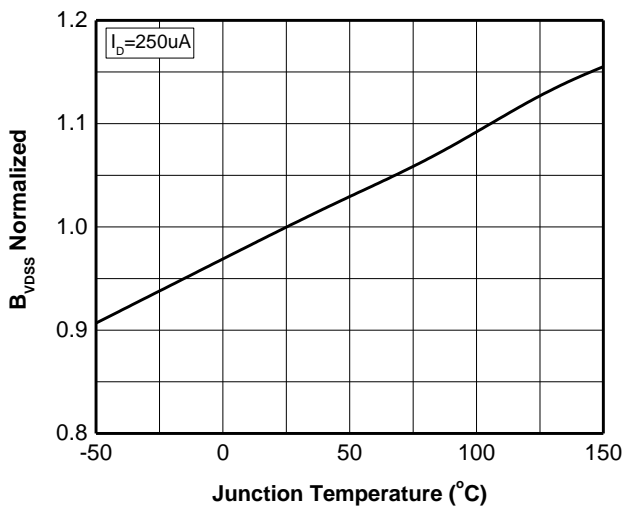
Transfer characteristics



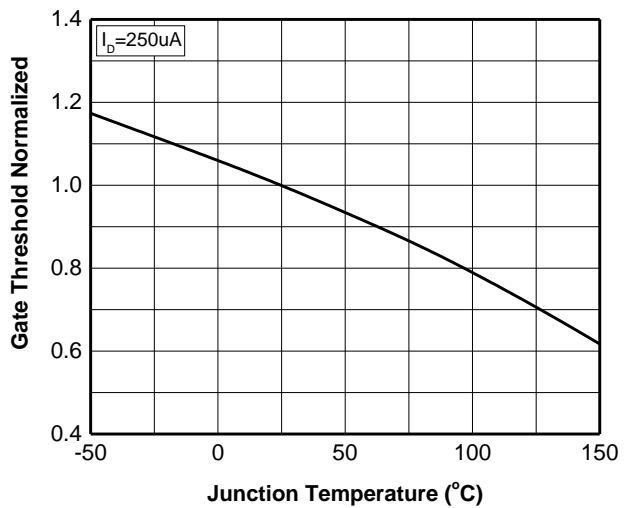
On-Resistance vs. Drain current



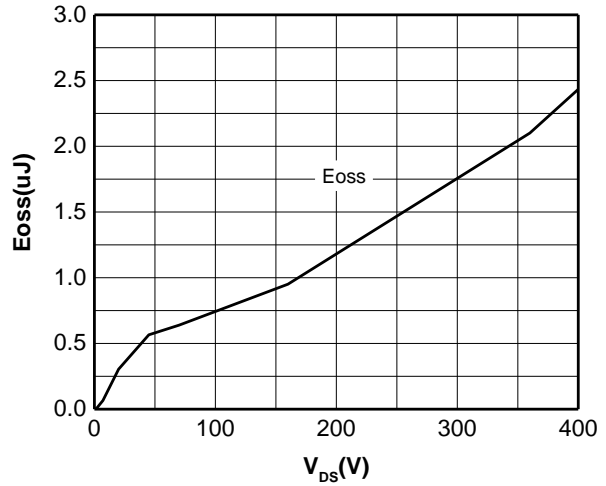
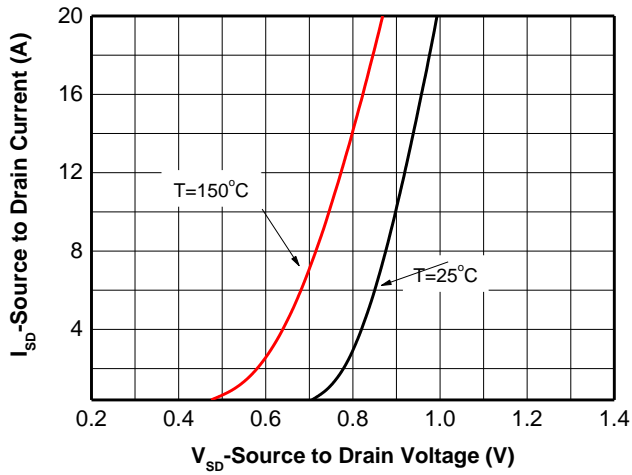
On-Resistance vs. Junction temperature



Breakdown Voltage vs. Junction temperature

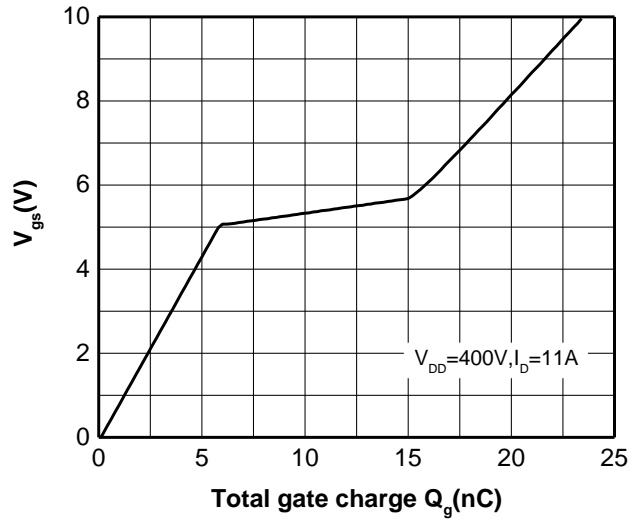
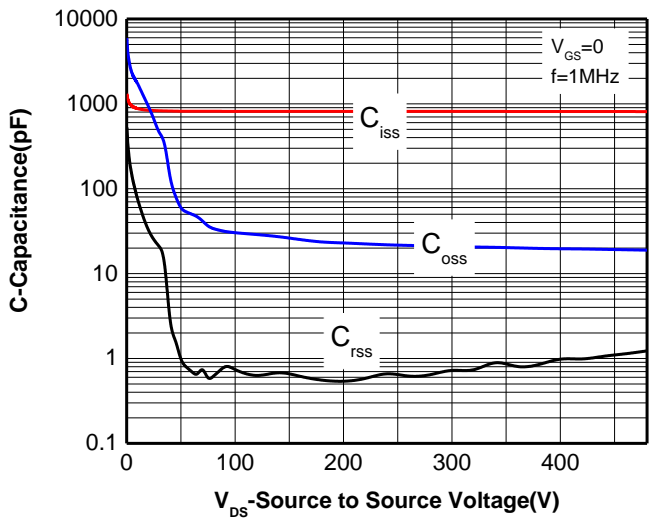


Threshold voltage vs. Junction temperature



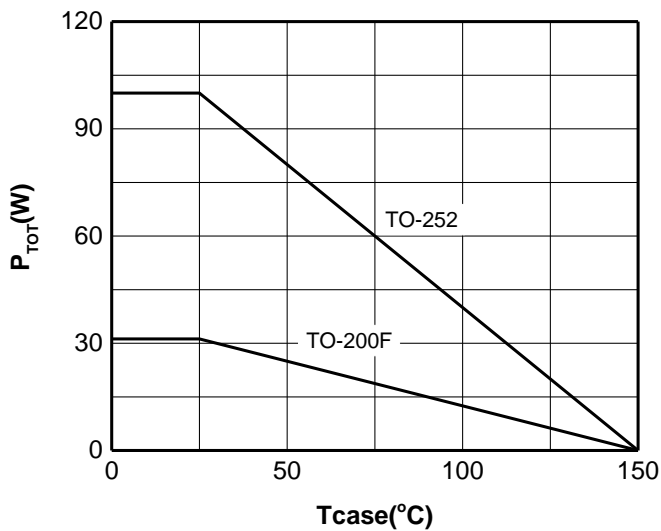
Body diode forward voltage

Cosstored Energy

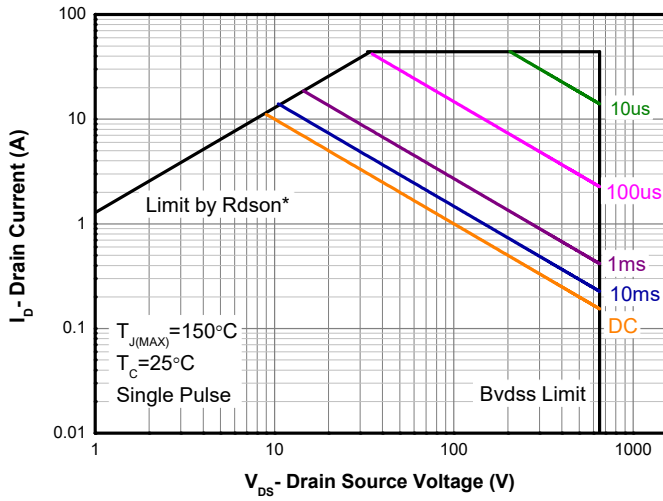


Capacitance

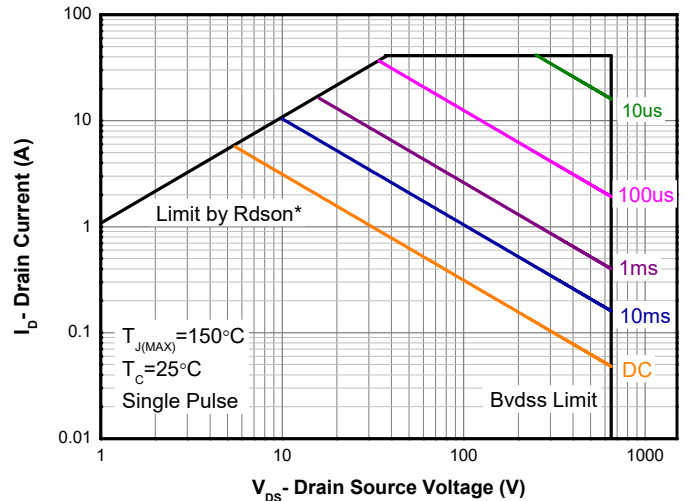
Gate charge Characteristics



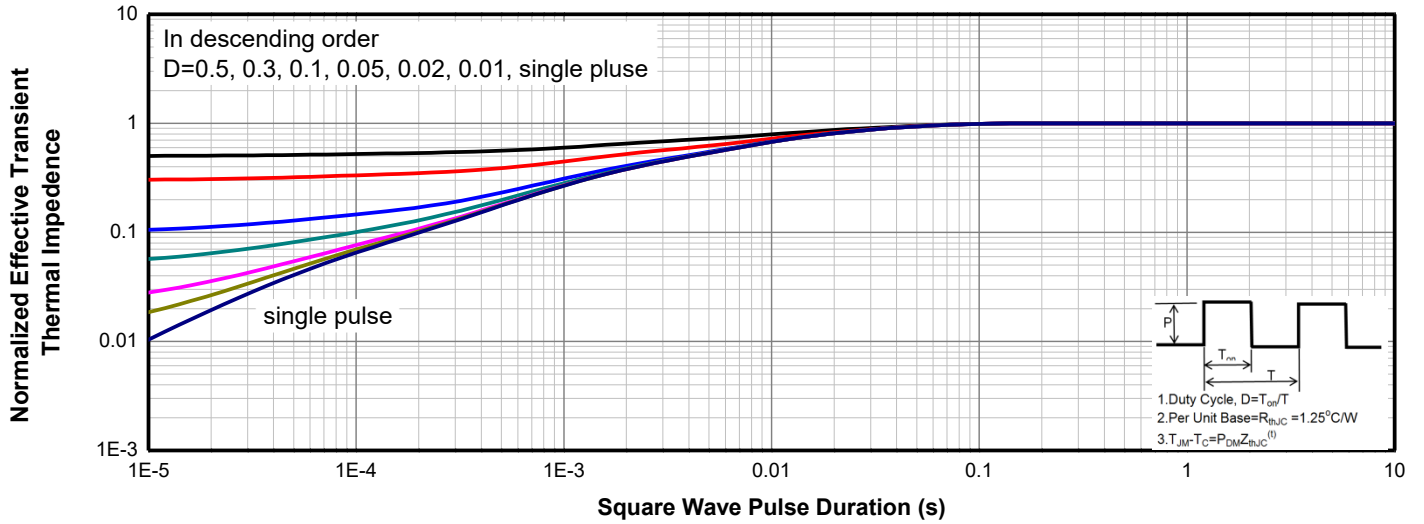
Power dissipation



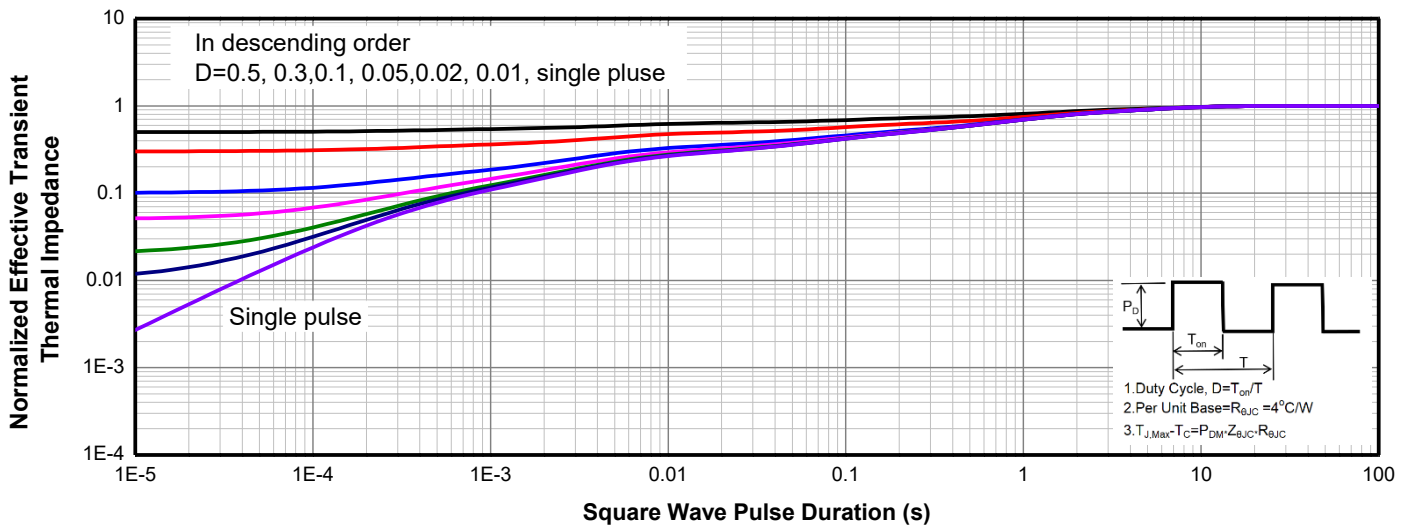
TO-252E-2
Safe operating area



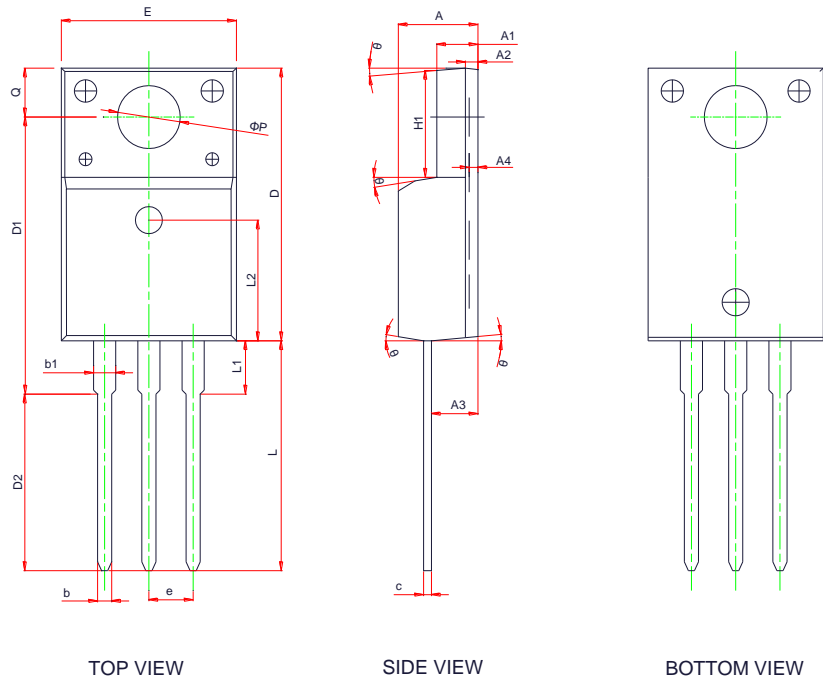
TO-220F
Safe operating area



TO-252E-2 Transient thermal response(Junction to case)



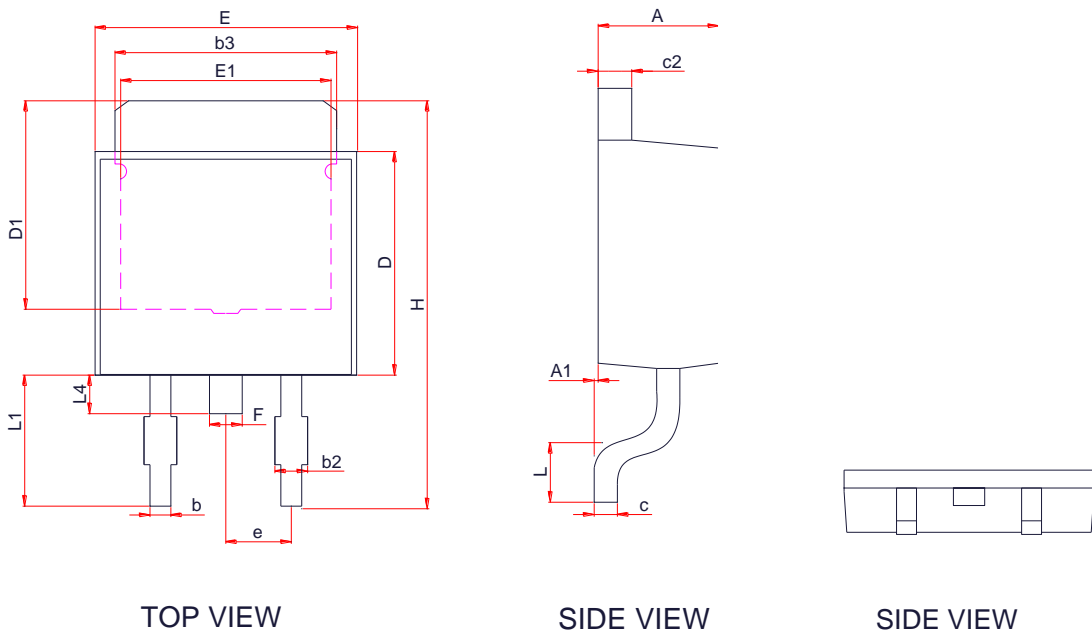
TO-220F Transient thermal response(Junction to case)

PACKAGE OUTLINE DIMENSIONS
TO-220F-3L


| Symbol | Dimensions in Millimeters | | |
|--------|---------------------------|-------|-------|
| | Min. | Typ. | Max. |
| A | 4.50 | 4.72 | 4.90 |
| A1 | 2.45 | 2.56 | 2.65 |
| A2 | 0.72Ref | | |
| A3 | 2.68 | 2.78 | 2.88 |
| A4 | - | - | 0.45 |
| b | 0.70 | 0.80 | 0.90 |
| b1 | 1.18 | 1.28 | 1.38 |
| c | 0.45 | 0.52 | 0.60 |
| D | 15.67 | 15.87 | 16.07 |
| D1 | 15.55 | 15.75 | 15.95 |
| E | 9.96 | 10.16 | 10.36 |
| e | 2.45BSC | | |
| H1 | 6.48 | 6.68 | 6.88 |
| L | 12.68 | 12.98 | 13.28 |
| L1 | - | - | 3.50 |
| L2 | 2.54BSC | | |
| φP | 3.08 | 3.18 | 3.28 |
| Q | 3.20 | - | 3.40 |
| θ | 3° | 5° | 7° |

PACKAGE OUTLINE DIMENSIONS

TO-252E-2L

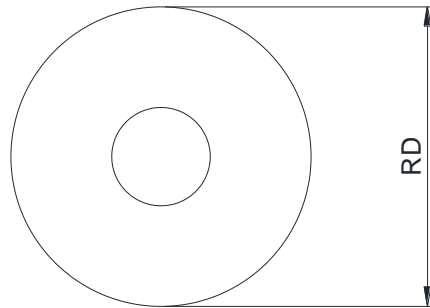


| Symbol | Dimensions in Millimeters | | |
|--------|---------------------------|------|-------|
| | Min. | Typ. | Max. |
| A | 2.20 | 2.30 | 2.40 |
| A1 | 0 | 0.08 | 0.15 |
| b | 0.50 | 0.60 | 0.70 |
| b2 | 0.60 | 0.75 | 0.90 |
| b3 | 5.20 | 5.35 | 5.50 |
| c2 | 0.45 | 0.50 | 0.55 |
| c | 0.51Ref | | |
| D | 5.40 | 5.60 | 5.80 |
| D1 | 4.57 | - | - |
| E | 6.40 | 6.60 | 6.80 |
| E1 | 3.81 | - | - |
| e | 2.30Ref | | |
| F | 0.70 | 0.80 | 0.90 |
| H | 9.40 | 9.80 | 10.20 |
| L | 1.40 | 1.59 | 1.77 |
| L1 | 2.40 | 2.70 | 3.00 |
| L4 | 0.80 | 1.00 | 1.20 |

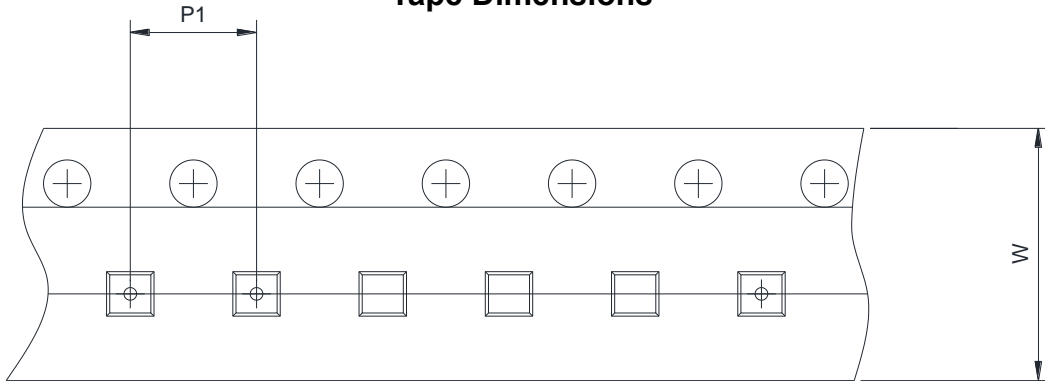
TAPE AND REEL INFORMATION

TO-252E-2L

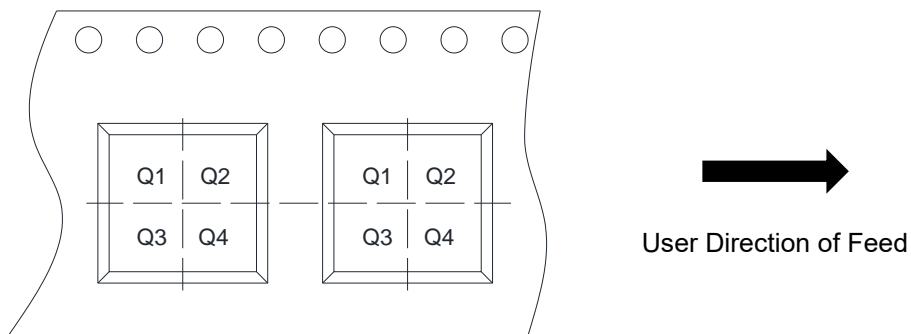
Reel Dimensions



Tape Dimensions



Quadrant Assignments For PIN1 Orientation In Tape



| | | | |
|------|---|--------------------------------|--|
| RD | Reel Dimension | <input type="checkbox"/> 7inch | <input checked="" type="checkbox"/> 13inch |
| W | Overall width of the carrier tape | <input type="checkbox"/> 8mm | <input type="checkbox"/> 12mm <input checked="" type="checkbox"/> 16mm |
| P1 | Pitch between successive cavity centers | <input type="checkbox"/> 2mm | <input type="checkbox"/> 4mm <input checked="" type="checkbox"/> 8mm |
| Pin1 | Pin1 Quadrant | <input type="checkbox"/> Q1 | <input checked="" type="checkbox"/> Q2 <input type="checkbox"/> Q3 <input type="checkbox"/> Q4 |