

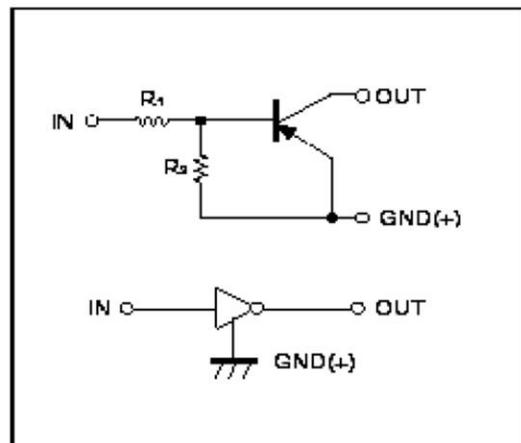
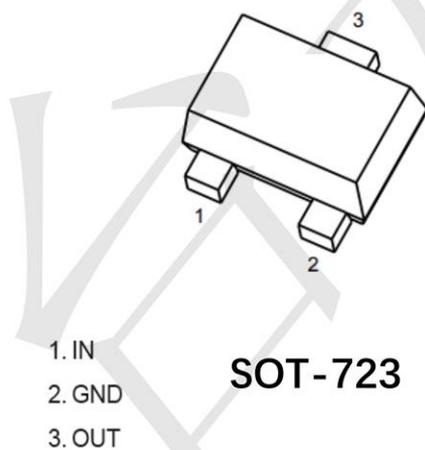
## DESCRIPTION

- Epoxy meets UL-94 V-0 flammability rating
- Moisture Sensitivity Level 1
- Built-in bias resistors enable the configuration of an inverter without connecting external input resistors
- Surface mount package ideally Suited for Automatic Insertion
- PNP

## FEATURES

- **Package:** SOT-723
- **Terminals:** Tin plated leads, solderable per J-STD-002 and JESD22-B102
- **Marking:** 16

## PIN DESCRIPTION



### ABSOLUTE MAXIMUM RATINGS

$T_A = 25^\circ\text{C}$ , unless otherwise noted

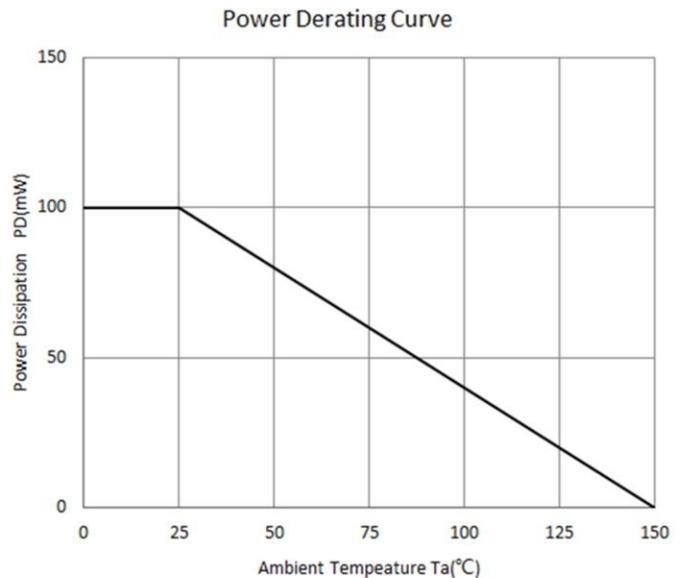
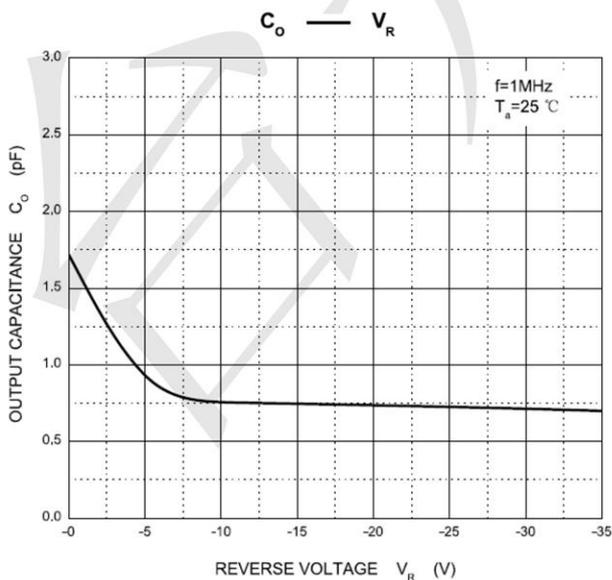
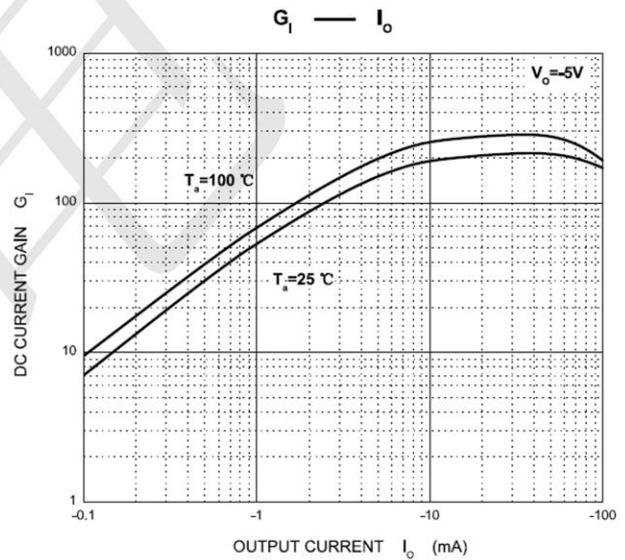
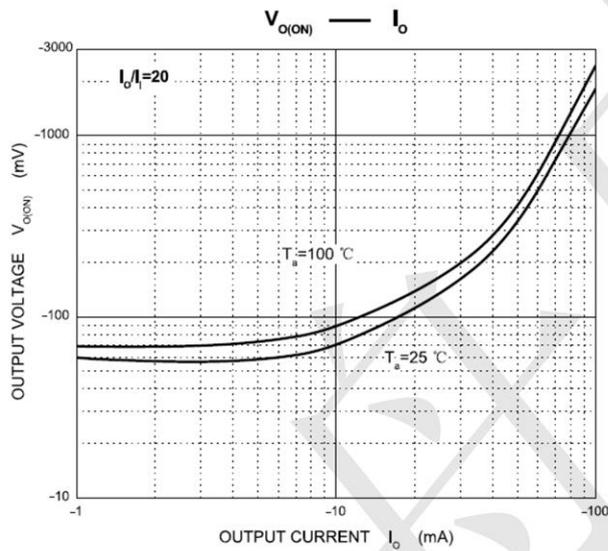
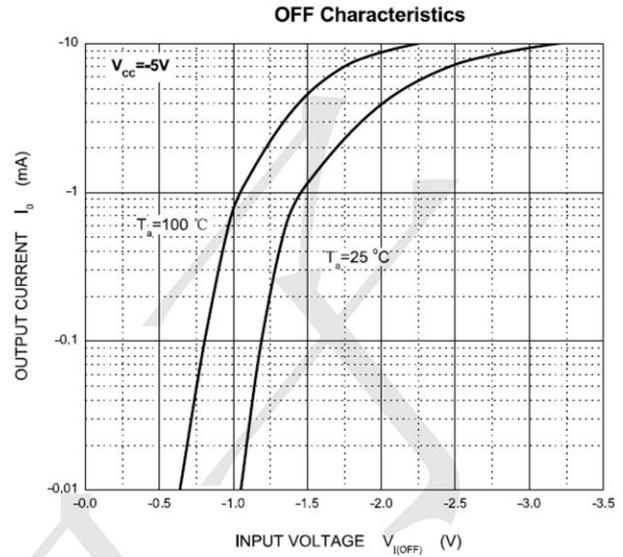
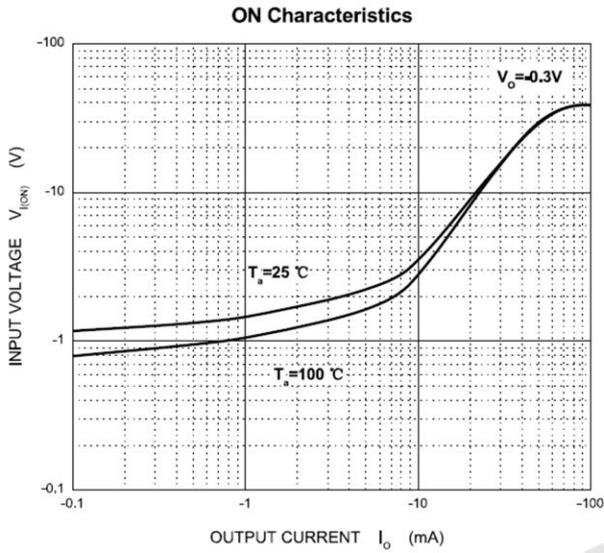
ITEM	SYMBOL	UNIT	CONDITIONS	VALUE
Supply Voltage	VCC	V		-50
Input Voltage	VIN	V		-40 to +10
Output Current	IO	mA		-100
Power Dissipation	PD	mW		100
Junction Temperature	TJ	°C		150
Storage Temperature	TSTG	°C		-55 to +150

### ELECTRICAL CHARACTERISTICS

$T = 25^\circ\text{C}$  unless otherwise noted

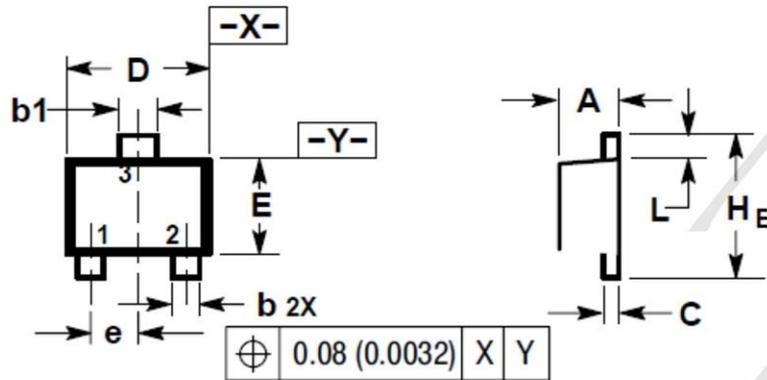
ITEM	SYMBOL	UNIT	CONDITIONS	MIN	TYP	MAX
Input voltage	$V_{I(off)}$	V	$V_{CC} = -5V, I_c = -100\mu\text{A}$ $V_0 = -0.3V, I_c = -2\text{mA}$	-0.5		
	$V_{I(on)}$	V				-3
Output voltage	$V_{O(on)}$	V	$I_o / I_i = -10\text{mA} / -0.5\text{mA}$			-0.3
Input current	I <sub>I</sub>	mA	$V_I = -5V$			-0.18
Output current	$I_{O(off)}$	uA	$V_{CC} = -50V, V_i = 0$			-0.5
DC current gain	G <sub>I</sub>		$V_o = -5V, I_o = -5\text{mA}$	68		
Input resistance	$R_1$	kΩ		32.9	47	61.1
Resistance ratio	$R_2/R_1$			0.8	1	1.2
Transition frequency	f <sub>T</sub>	MHz	$V_{CE} = -10V, I_E = -5\text{mA}$		250	

TYPICAL CHARACTERISTICS

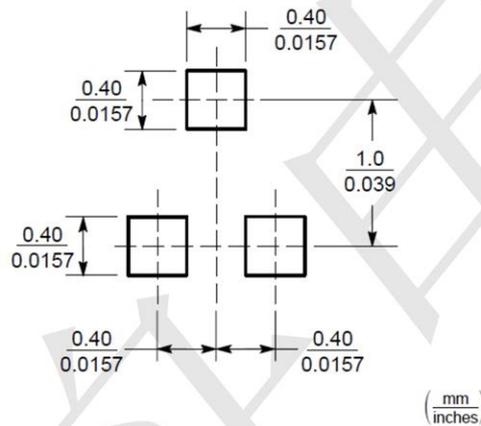


PACKAGE INFORMATION

Dimension in SOT-723 (Unit: mm)



SOLDERING FOOTPRINT



DIM	MILLIMETERS		INCHES	
	MIN	MAX	MIN	MAX
A	0.45	0.55	0.018	0.022
b	0.15	0.27	0.0059	0.0106
b1	0.25	0.35	0.010	0.014
C	0.07	0.17	0.0028	0.0067
D	1.15	1.25	0.045	0.049
E	0.75	0.85	0.03	0.034
e	0.40 BSC		0.016 BSC	
HE	1.15	1.25	0.045	0.049
L	0.15	0.25	0.0059	0.0098