

Description

The HSH200N02 is the highest performance trench N-ch MOSFETs with extreme high cell density, which provide excellent RDSON and gate charge for most of the synchronous buck converter applications.

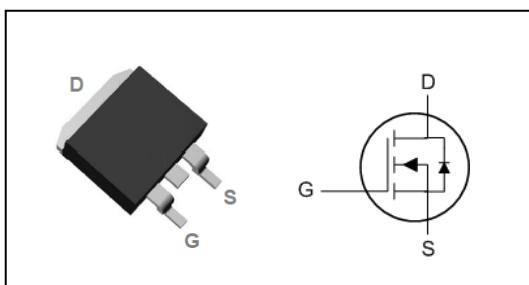
The HSH200N02 meet the RoHS and Green Product requirement, 100% EAS guaranteed with full function reliability approved.

- Power Switching application
- Green Device Available
- Excellent CdV/dt effect decline
- Advanced high cell density Trench technology

Product Summary

V _{DS}	200	V
R _{DSON,typ}	27	mΩ
I _D	70	A

TO263 Pin Configuration



Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
V _{DS}	Drain-Source Voltage	200	V
V _{Gs}	Gate-Source Voltage	±20	V
I _D @T _C =25°C	Continuous Drain Current, V _{Gs} @ 10V ¹	70	A
I _D @T _C =100°C	Continuous Drain Current, V _{Gs} @ 10V ¹	46	A
I _{DM}	Pulsed Drain Current ²	252	A
EAS	Single Pulse Avalanche Energy ³	580	mJ
P _D @T _C =25°C	Total Power Dissipation ³	200	W
T _{STG}	Storage Temperature Range	-55 to 150	°C
T _J	Operating Junction Temperature Range	-55 to 150	°C

Thermal Data

Symbol	Parameter	Typ.	Max.	Unit
R _{θJA}	Thermal Resistance Junction-ambient ¹	---	60	°C/W
R _{θJC}	Thermal Resistance Junction-Case ¹	---	0.55	°C/W

Electrical Characteristics ($T_J=25^\circ\text{C}$, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV_{DSS}	Drain-Source Breakdown Voltage	$\text{V}_{\text{GS}}=0\text{V}$, $\text{I}_D=250\mu\text{A}$	200	---	---	V
$\text{R}_{\text{DS(ON)}}$	Static Drain-Source On-Resistance ²	$\text{V}_{\text{GS}}=10\text{V}$, $\text{I}_D=30\text{A}$	---	27	33	$\text{m}\Omega$
$\text{V}_{\text{GS(th)}}$	Gate Threshold Voltage	$\text{V}_{\text{GS}}=\text{V}_{\text{DS}}$, $\text{I}_D=250\mu\text{A}$	2	---	4	V
I_{DSS}	Drain-Source Leakage Current	$\text{V}_{\text{DS}}=160\text{V}$, $\text{V}_{\text{GS}}=0\text{V}$, $\text{T}_J=25^\circ\text{C}$	---	---	1	μA
		$\text{V}_{\text{DS}}=160\text{V}$, $\text{V}_{\text{GS}}=0\text{V}$, $\text{T}_J=55^\circ\text{C}$	---	---	5	
I_{GSS}	Gate-Source Leakage Current	$\text{V}_{\text{GS}}=\pm 20\text{V}$, $\text{V}_{\text{DS}}=0\text{V}$	---	---	± 100	nA
R_g	Gate Resistance	$\text{V}_{\text{DS}}=0\text{V}$, $\text{V}_{\text{GS}}=0\text{V}$, $f=1\text{MHz}$	---	3	---	Ω
Q_g	Total Gate Charge (10V)	$\text{V}_{\text{DS}}=100\text{V}$, $\text{V}_{\text{GS}}=10\text{V}$, $\text{I}_D=30\text{A}$	---	110	---	nC
Q_{gs}	Gate-Source Charge		---	32	---	
Q_{gd}	Gate-Drain Charge		---	38	---	
$\text{T}_{\text{d(on)}}$	Turn-On Delay Time	$\text{V}_{\text{DD}}=100\text{V}$, $\text{V}_{\text{GS}}=10\text{V}$, $\text{R}_g=2.5\Omega$	---	30	---	ns
T_r	Rise Time		---	18	---	
$\text{T}_{\text{d(off)}}$	Turn-Off Delay Time		---	22	---	
T_f	Fall Time		---	33	---	
C_{iss}	Input Capacitance	$\text{V}_{\text{DS}}=25\text{V}$, $\text{V}_{\text{GS}}=0\text{V}$, $f=1\text{MHz}$	---	5082	---	pF
C_{oss}	Output Capacitance		---	343	---	
C_{rss}	Reverse Transfer Capacitance		---	129	---	

Diode Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I_s	Continuous Source Current ^{1,5}	$\text{V}_G=\text{V}_D=0\text{V}$, Force Current	---	---	70	A
I_{SM}	Pulsed Source Current ^{2,5}		---	---	252	A
V_{SD}	Diode Forward Voltage ²	$\text{V}_{\text{GS}}=0\text{V}$, $\text{I}_s=30\text{A}$, $\text{T}_J=25^\circ\text{C}$	---	---	1.2	V
t_{rr}	Reverse Recovery Time	$\text{I}_F=30\text{A}$, $d\text{I}/dt=100\text{A}/\mu\text{s}$, $\text{T}_J=25^\circ\text{C}$	---	47	---	nS
Q_{rr}	Reverse Recovery Charge		---	81	---	nC

Note :

- 1.The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper.
- 2.The data tested by pulsed , pulse width $\leq 300\mu\text{s}$, duty cycle $\leq 2\%$
- 3.The EAS data shows Max. rating . The test condition is $\text{V}_{\text{DD}}=25\text{V}, \text{V}_{\text{GS}}=10\text{V}, \text{L}=0.3\text{mH}$
- 4.The power dissipation is limited by 150°C junction temperature
- 5.The data is theoretically the same as I_D and I_{DM} , in real applications , should be limited by total power dissipation.



Typical Characteristics

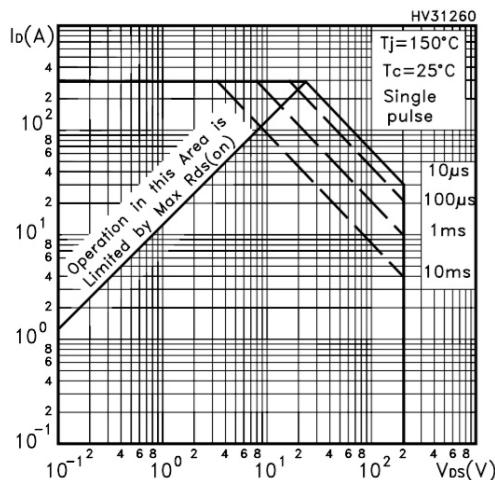


Fig.1 Safe operating area for TO-220

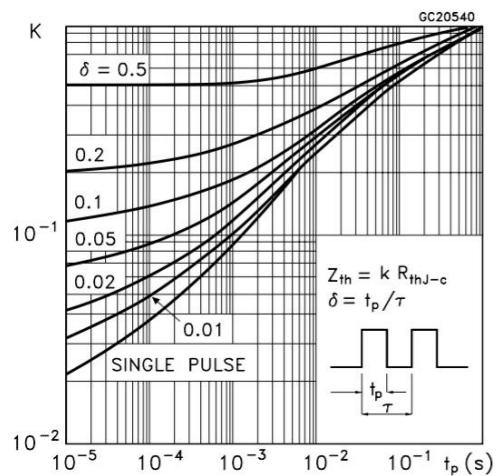


Fig.2 Thermal impedance for TO-220

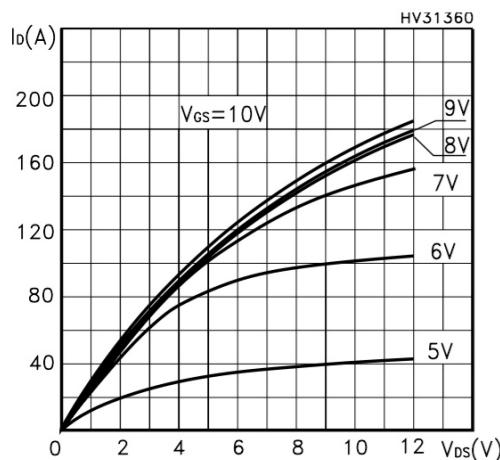


Fig.3 Output characteristics

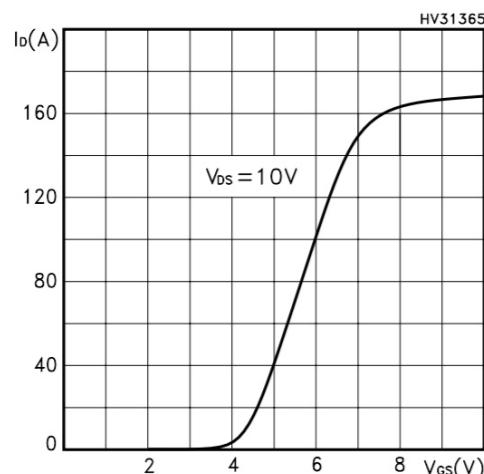


Fig.4 Transfer characteristics

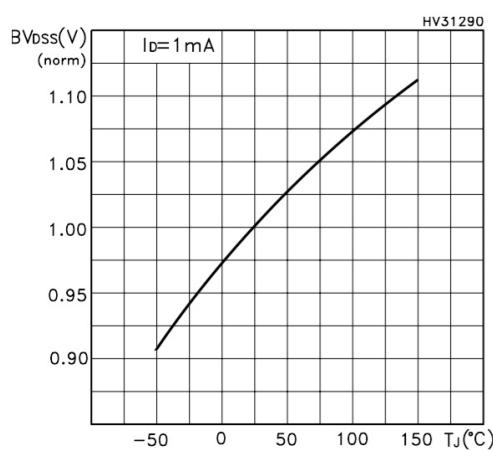


Fig.5 Normalized BV_{DSS} vs temperature

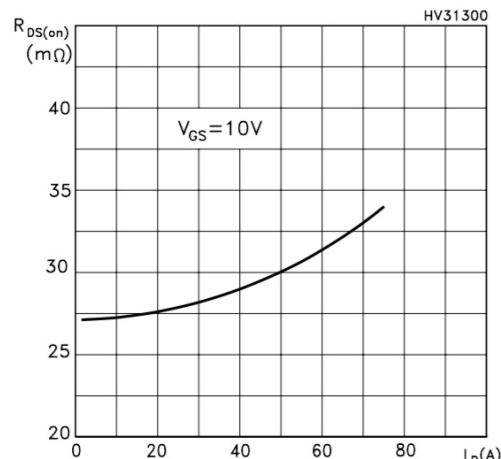


Fig.6 Static drain-source on resistance

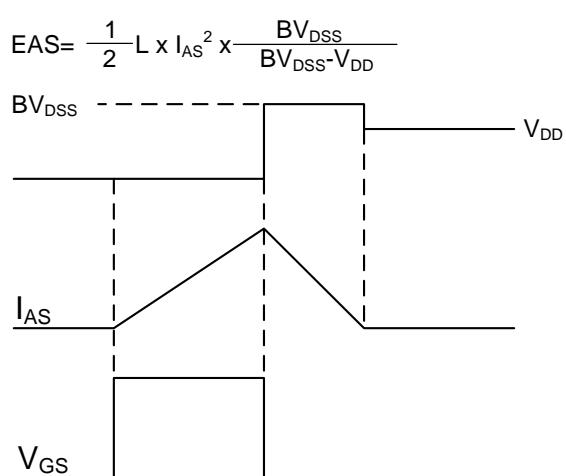
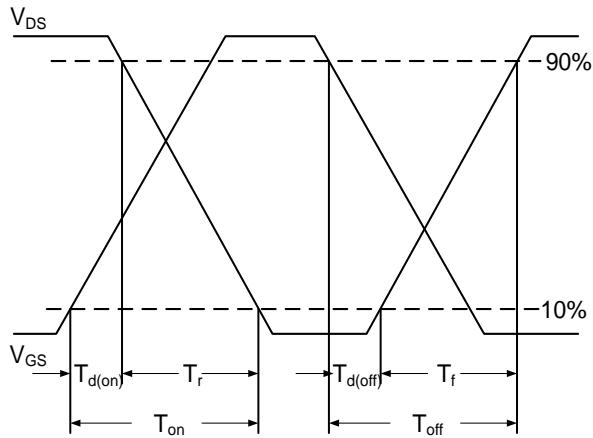
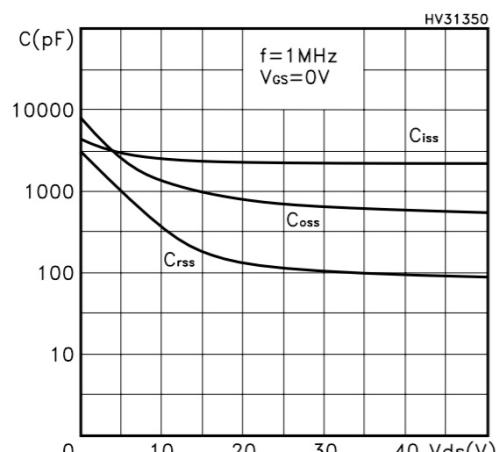
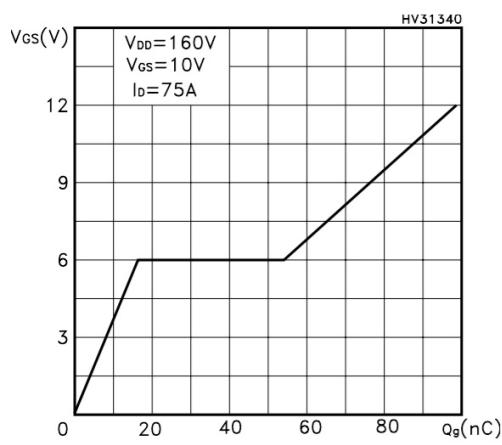
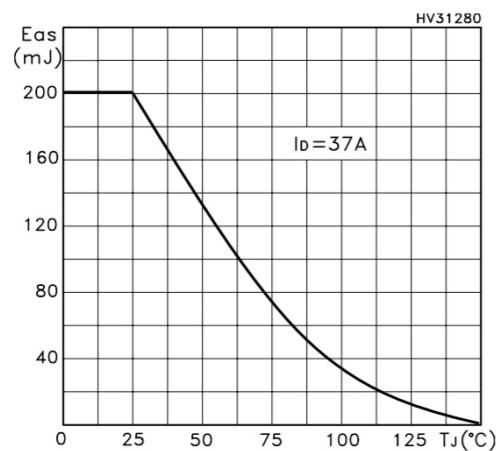
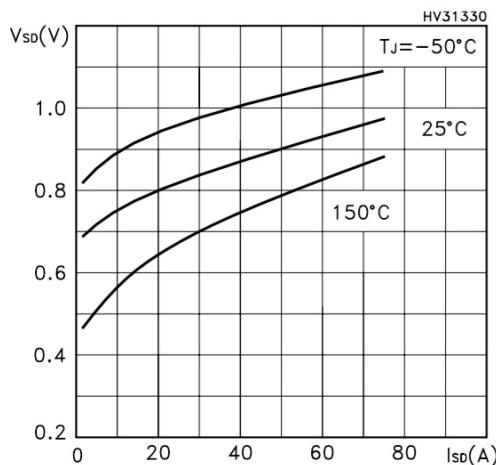
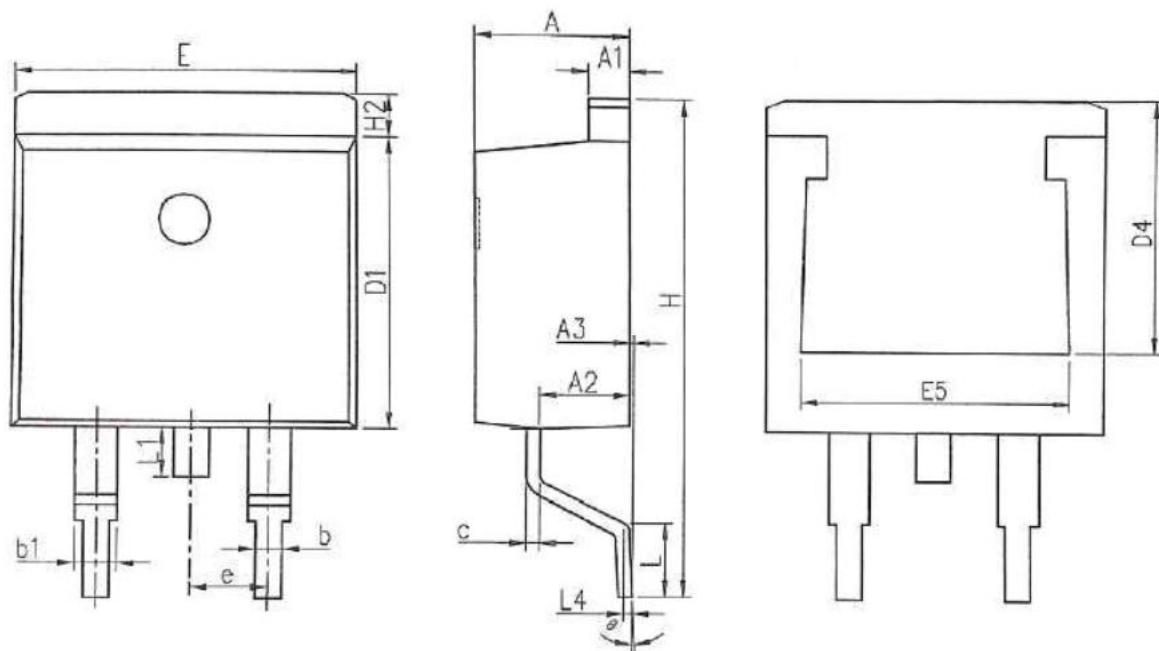
N-Ch 200V Fast Switching MOSFETs


Fig.12 Unclamped Inductive Switching



SYMBOLS	MILLIMETERS		INCHES	
	MIN	MAX	MIN	MAX
A	4.370	4.770	0.172	0.188
A1	1.220	1.420	0.048	0.056
A2	2.200	2.890	0.087	0.114
A3	0.000	0.250	0.000	0.010
b	0.700	0.960	0.028	0.038
b1	1.170	1.470	0.046	0.058
c	0.300	0.530	0.012	0.021
D1	8.500	9.300	0.335	0.366
D4	6.600	-	0.260	-
E	9.860	10.36	0.388	0.408
E5	7.060	-	0.278	-
e	2.540 BSC		0.100 BSC	
H	14.70	15.70	0.579	0.618
H2	1.070	1.470	0.042	0.058
L	2.000	2.600	0.079	0.102
L1	1.400	1.750	0.055	0.069
L4	0.250 BSC		0.010 BSC	
Θ	0°	9°	0°	9°