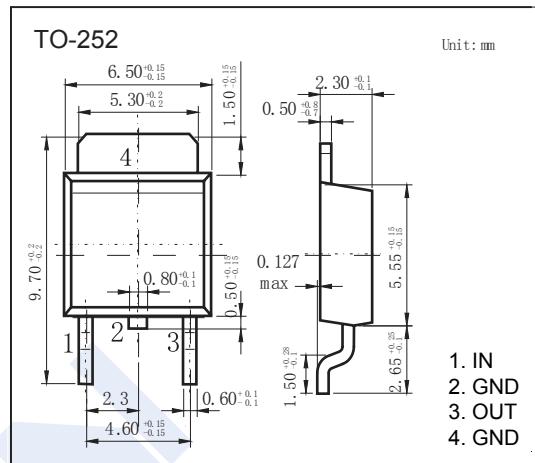


## Three Terminal Positive Voltage Regulator

### 78M08

#### ■ Features

- Maximum Output Current  $I_o = 500\text{mA}$
- Output Voltage  $V_o = 8\text{V}$
- Internal Thermal Overload Protection
- Internal Short Circuit Current Limiting
- Output Transistor Safe Operating Area Protection



#### ■ Absolute Maximum Ratings Over Operating Temperature Range(unless otherwise noted)

Parameter	Symbol	Rating	Unit
Input Voltage	$V_i$	35	V
Maximum Output Current	$I_o$	0.5	A
Operating Virtual Junction Temperature	$T_j$	0 to 125	°C
Storage Temperature Range	$T_{stg}$	-65 to 150	
Lead Temperature 1.6mm(1/16 inch) from case for 10 seconds		260	

#### ■ Electrical Characteristics at Specified Virtual Junction Temperature, $V_i=14\text{V}$ , $I_o=350\text{mA}$ (unless otherwise noted)

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Output Voltage	$V_o$	25°C	7.7	8.0	8.3	V
		10.5V≤ $V_i$ ≤23V, $I_o = 5.0\text{mA} \sim 350\text{mA}$	7.6	8.0	8.4	
Line Regulation	$\Delta V_o$	10.5V≤ $V_i$ ≤25V, $I_o=200\text{mA}$	25°C		150	mV
		11V≤ $V_i$ ≤25V, $I_o=200\text{mA}$			75	
Load Regulation	$\Delta V_o$	$I_o = 5.0\text{mA} \sim 500\text{mA}$	25°C		160	
		$I_o = 5.0\text{mA} \sim 200\text{mA}$			80	
Quiescent Current	$I_q$	25°C			8.0	mA
		125°C			7.5	
Quiescent Current Change	$\Delta I_q$	10.5V≤ $V_i$ ≤25V	0 to 125°C		1.0	
		5mA≤ $I_o$ ≤350mA			0.5	
Output Noise Voltage	$V_N$	10Hz≤ $f$ ≤100kHz, $T_a=25\text{°C}$	25°C		52	uV
Ripple Rejection	$R_R$	11.5V≤ $V_i$ ≤21.5V, $f=120\text{Hz}$	25°C	62	80	dB
Dropout Voltage	$V_d$		25°C		2	V

\* Pulse testing techniques are used to maintain the junction temperature as close to the ambient temperature as possible.

Thermal effects must be taken into account separately. All characteristics are measured with a 0.33μF capacitor across the input and a 0.1μF capacitor across the output.

\*\* This specification applies only for dc power dissipation permitted by absolute maximum ratings.