

**General Description**

The CMP4668 uses advanced process technology and design to provide excellent RDS(ON).

These devices are well suited for high efficient switched mode power supplies and active power factor correction.

**Features**

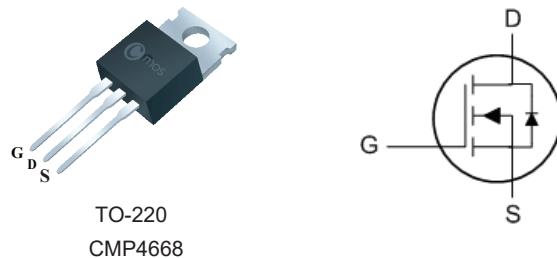
- Lower On-resistance
- 100% avalanche tested
- RoHS Compliant

**Product Summary**

BVDSS	RDS(ON)	ID
200V	15mΩ	130A

**Applications**

- DC-AC converters
- Motor Control
- UPS (Uninterruptible Power Supply)

**TO-220 Pin Configuration****Absolute Maximum Ratings**

Symbol	Parameter	Rating	Units
$V_{DS}$	Drain-Source Voltage	200	V
$V_{GS}$	Gate-Source Voltage	$\pm 20$	V
$I_D @ T_c = 25^\circ C$	Continuous Drain Current	130	A
$I_D @ T_c = 100^\circ C$	Continuous Drain Current	91	A
$I_{DM}$	Pulsed Drain Current	520	A
EAS	Single Pulse Avalanche Energy <sup>1</sup>	1500	mJ
$P_D @ T_c = 25^\circ C$	Total Power Dissipation	430	W
$T_{STG}$	Storage Temperature Range	-55 to 175	°C
$T_J$	Operating Junction Temperature Range	-55 to 175	°C

**Thermal Data**

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction-ambient	---	60	°C/W
$R_{\theta JC}$	Thermal Resistance Junction-case	---	0.35	°C/W

Electrical Characteristics ( $T_J=25^\circ\text{C}$  , unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$\text{BV}_{\text{DSS}}$	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}$ , $I_D=250\mu\text{A}$	200	---	---	V
$R_{\text{DS}(\text{ON})}$	Static Drain-Source On-Resistance	$V_{\text{GS}}=10\text{V}$ , $I_D=18\text{A}$	---	11	15	$\text{m}\Omega$
$V_{\text{GS}(\text{th})}$	Gate Threshold Voltage	$V_{\text{GS}}=V_{\text{DS}}$ , $I_D=250\mu\text{A}$	3	3.75	4.5	V
$I_{\text{DSS}}$	Drain-Source Leakage Current	$V_{\text{DS}}=200\text{V}$ , $V_{\text{GS}}=0\text{V}$	---	---	1	$\mu\text{A}$
$I_{\text{GSS}}$	Gate-Source Leakage Current	$V_{\text{GS}}=\pm 20\text{V}$ , $V_{\text{DS}}=0\text{V}$	---	---	$\pm 100$	nA
$g_{\text{fs}}$	Forward Transconductance	$V_{\text{DS}}=10\text{V}$ , $I_D=18\text{A}$	---	37	---	S
$R_g$	Gate Resistance	$V_{\text{DS}}=0\text{V}$ , $V_{\text{GS}}=0\text{V}$ , $f=1\text{MHz}$	---	3	---	$\Omega$
$Q_g$	Total Gate Charge	$I_D=20\text{A}$	---	55	---	nC
$Q_{\text{gs}}$	Gate-Source Charge	$V_{\text{DD}}=100\text{V}$	---	18	---	
$Q_{\text{gd}}$	Gate-Drain Charge	$V_{\text{GS}}=10\text{V}$	---	5	---	
$T_{\text{d}(\text{on})}$	Turn-On Delay Time	$V_{\text{DD}}=100\text{V}$	---	15	---	ns
$T_r$	Rise Time	$V_{\text{GS}}=10\text{V}$	---	22	---	
$T_{\text{d}(\text{off})}$	Turn-Off Delay Time	$I_D=20\text{A}$	---	40	---	
$T_f$	Fall Time	$R_g=10\Omega$	---	10	---	
$C_{\text{iss}}$	Input Capacitance	$V_{\text{DS}}=25\text{V}$ , $V_{\text{GS}}=0\text{V}$ , $f=1\text{MHz}$	---	7000	---	pF
$C_{\text{oss}}$	Output Capacitance		---	2800	---	
$C_{\text{rss}}$	Reverse Transfer Capacitance		---	180	---	

## Diode Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$I_S$	Continuous Source Current	$V_G=V_D=0\text{V}$ , Force Current	---	---	130	A
$I_{\text{SM}}$	Pulsed Source Current		---	---	390	A
$V_{\text{SD}}$	Diode Forward Voltage	$V_{\text{GS}}=0\text{V}$ , $I_S=18\text{A}$ , $T_J=25^\circ\text{C}$	---	---	1.2	V

Note :

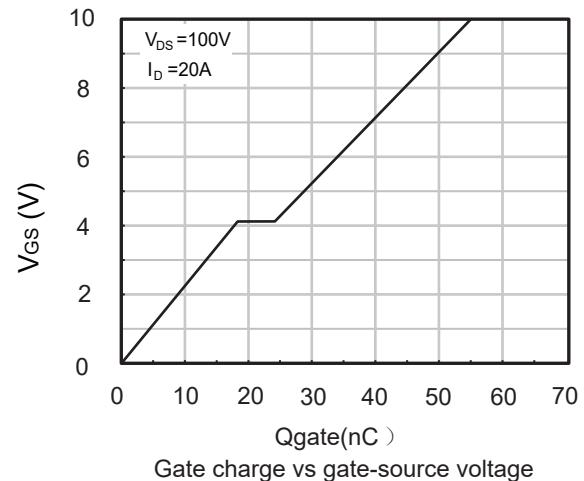
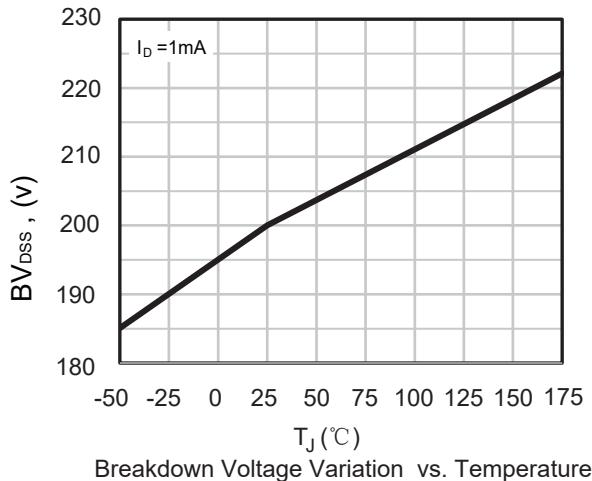
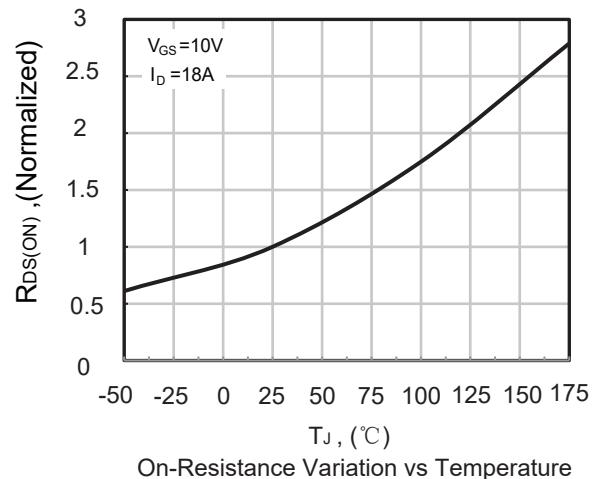
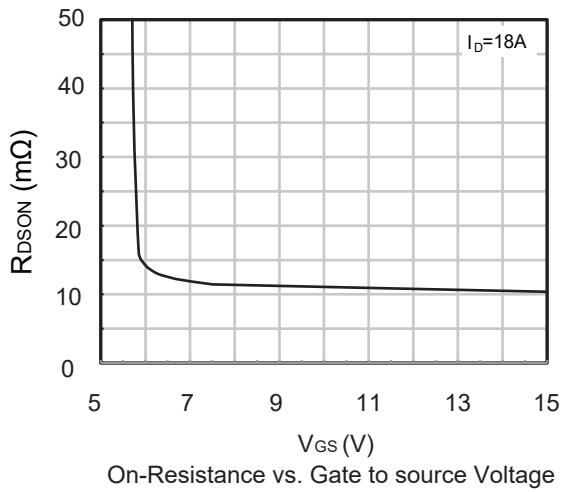
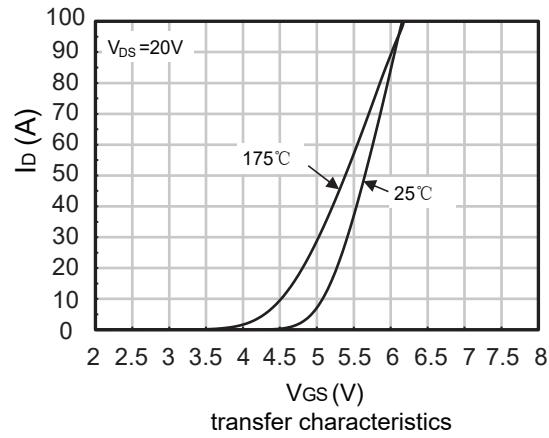
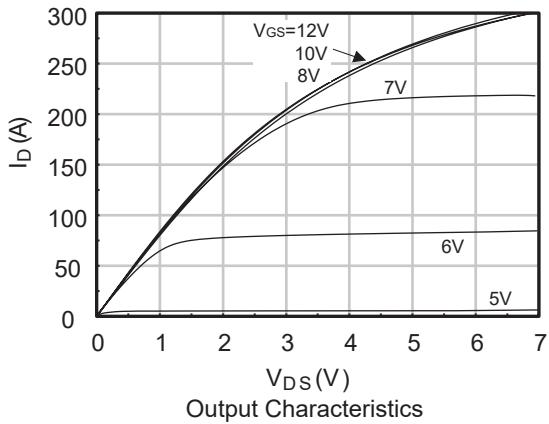
1.The EAS data shows Max. rating . The test condition is  $V_{\text{DD}}=50\text{V}$ , $V_{\text{GS}}=10\text{V}$ , $L=1\text{mH}$ , $I_{\text{AS}}=55\text{A}$ .

This product has been designed and qualified for the consumer market.

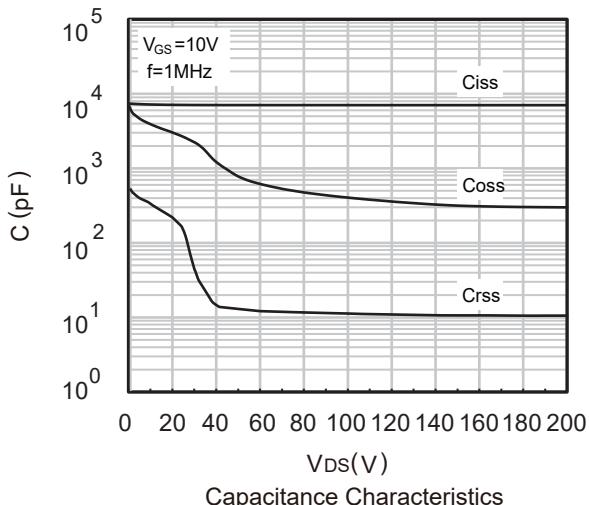
Cmos assumes no liability for customers' product design or applications.

Cmos reserves the right to improve product design ,functions and reliability without notice.

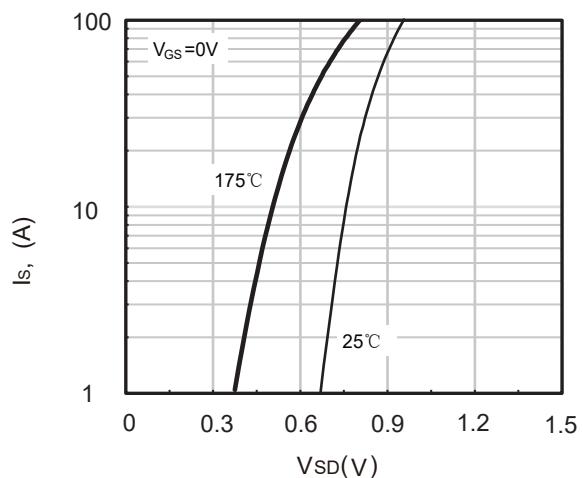
## Typical Characteristics



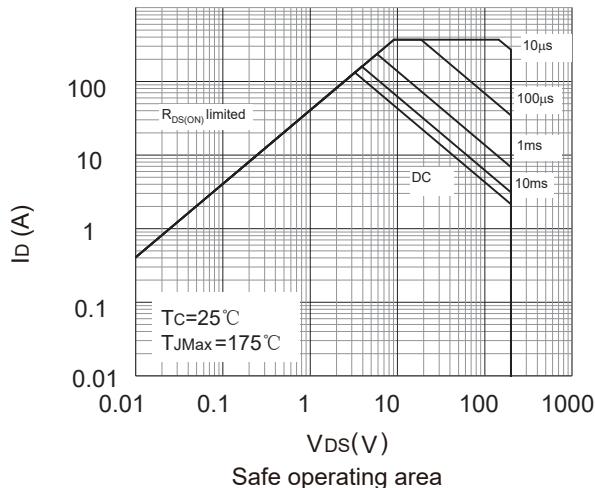
## Typical Characteristics



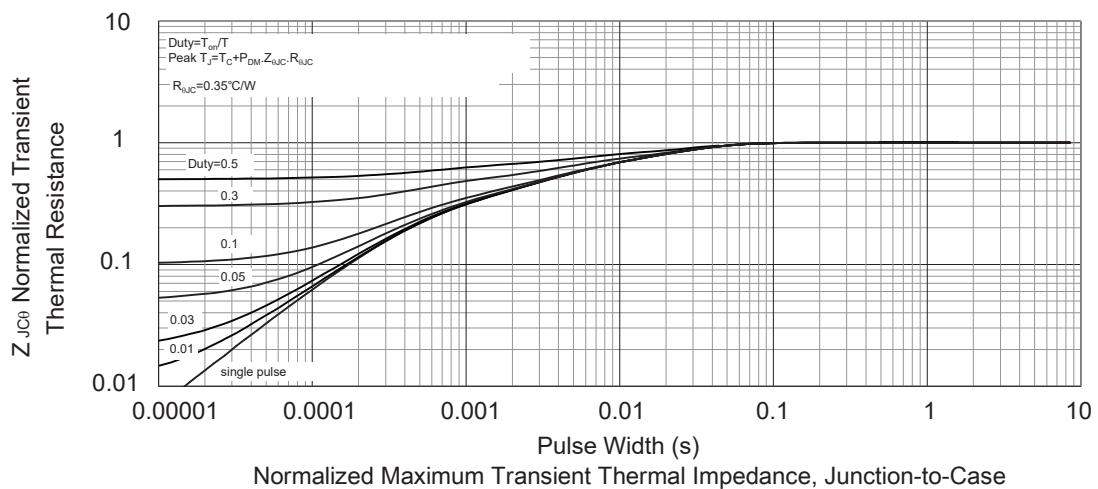
Capacitance Characteristics



Body-Diode Characteristics



Safe operating area



Normalized Maximum Transient Thermal Impedance, Junction-to-Case