CMP18N60/CMB18N60/CMI18N60/CMF18N60



600V N-Channel MOSFET

General Description

The 18N60 have been fabricated using an advanced high voltage MOSFET process that is designed to deliver high levels of performance and robustness in popular AC-DC applications. These parts can be adopted quickly into new and existing offline power supply designs.

Product Summary

BVDSS	RDSON	ID
600V	0.45Ω	18A

Applications

- Charger
- Adaptor
- Power Supply

TO-220/263/262/220F Pin Configuration

Features

- Fast switching
- 100% avalanche tested
- RoHS Compliant

TO-220 TO-263 TO-262 TO-220F (CMP18N60) (CMB18N60) (CMI18N60)

Absolute Maximum Ratings

Symbol	Parameter		220/263/262	220F	Units	
V _{DS}	Drain-Source Voltage		600		V	
V_{GS}	Gate-Source Voltage		±30		V	
I _D @T _C =25℃	Continuous Drain Current		18	18*	Α	
I _D @T _C =100℃	Continuous Drain Current		11.4	11.4*	Α	
I _{DM}	Pulsed Drain Current (Note 1)		72	72*	А	
EAS	Single Pulse Avalanche Energy (No	te 2)	810		mJ	
P _D @T _C =25℃	Total Power Dissipation		300	40	W	
T _{STG}	Storage Temperature Range		-55 to 150		℃	
T_J	Operating Junction Temperature Range		-55 to 150		$^{\circ}$	

^{*} Drain current limited by maximum junction temperature.

Thermal Data

Symbol	Parameter		220/263/262	220F	Unit
$R_{ heta JA}$	Thermal Resistance Junction-ambient	(Note 3,4)	65	62.5	°C/W
R _{θJC}	Thermal Resistance Junction-case		0.3	3.13	°C/W

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Electrical Characteristics ($T_J=25^{\circ}$ C , unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
BV _{DSS}	Drain-Source Breakdown Voltage	V_{GS} =0V , I_D =250uA	600			V
R _{DS(ON)}	Static Drain-Source On-Resistance	V _{GS} =10V , I _D =8A			0.45	Ω
V _{GS(th)}	Gate Threshold Voltage	V _{GS} =V _{DS} , I _D =250uA	3		5	V
	Drain Sauras Laglaga Current	V _{DS} =600V , V _{GS} =0V			1	uA
I _{DSS}	Drain-Source Leakage Current	V _{DS} =600V , T _C =125℃			100	
I _{GSS}	Gate-Source Leakage Current	V _{GS} =± 30V , V _{DS} =0V			±100	nA
gfs	Forward Transconductance	V _{DS} =10V, I _D =15A		30		S
R_g	Gate Resistance	V_{DS} =0V , V_{GS} =0V , f=1MHz		2.5		Ω
Q_g	Total Gate Charge	I _D =18A		55		
Q_gs	Gate-Source Charge	V _{DS} =480V		22		nC
Q_{gd}	Gate-Drain Charge	V _{GS} =10V		13		
$T_{d(on)}$	Turn-On Delay Time	V = 200V		200		
Tr	Rise Time	V _{DS} =300V		150		no
$T_{d(off)}$	Turn-Off Delay Time	I_D =18A R_G =25 Ω		460		ns
T _f	Fall Time			80		
C _{iss}	Input Capacitance			3900		
Coss	Output Capacitance	V _{DS} =25V , V _{GS} =0V , f=1MHz		260		pF
C _{rss}	Reverse Transfer Capacitance			24		

Diode Characteristics

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
Is	Continuous Source Current	V _G =V _D =0V , Force Current			18	Α
I _{SM}	Pulsed Source Current	VG-VD-OV, Porce Current			72	Α
V _{SD}	Diode Forward Voltage	V _{GS} =0V , I _S =16A			1.2	V

Notes:

- 1. Repetitive Rating: Pulse width limited by maximum junction temperature.
- 2. L=5mH, I $\,$ ID =18A, VDD=50V, RG=25 Ω , Starting TJ=25 $^{\circ}$ C.
- 3. The value of Reja is measured with the device in a still air environment with Ta=25 $^{\circ}$ C.
- 4. The Reja is the sum of the thermal impedance from junction to case Rejc and case to ambient.

This product has been designed and qualified for the counsumer market.

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