

Product Summary

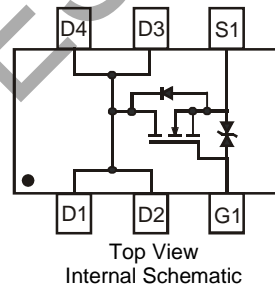
BV _{DSS}	R _{DS(ON)} Max	I _D T _A = +25°C
30V	60mΩ @ V _{GS} = 4.5V	3.2A
	80mΩ @ V _{GS} = 2.5V	2.7A
	130mΩ @ V _{GS} = 1.5V	2.1A

Description

This new generation MOSFET has been designed to minimize the on-state resistance (R_{DS(ON)}) yet maintain superior switching performance, making it ideal for high-efficiency power-management applications.

Applications

- General-purpose interfacing switches
- Power-management functions
- Analog switches



Features

- Low On-Resistance
- Very Low Gate Threshold Voltage
- Low Input Capacitance
- ESD Protected Gate
- Fast Switching Speed
- **Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)**
- **Halogen and Antimony Free. "Green" Device (Note 3)**
- **The DIODES™ DMN3115UDMQ is suitable for automotive applications requiring specific change control; this part is AEC-Q101 qualified, PPAP capable, and manufactured in IATF 16949 certified facilities.**
<https://www.diodes.com/quality/product-definitions/>

Mechanical Data

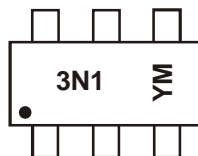
- Package: SOT26
- Package Material – Molded Plastic, "Green" Molding Compound. UL Flammability Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish - Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 (E3)
- Terminal Connections: See Diagram
- Weight: 0.015 grams (Approximate)

Ordering Information (Note 4)

Part Number	Package	Packing	
		Qty.	Carrier
DMN3115UDM-7	SOT26	3,000	Tape & Reel
DMN3115UDMQ-7	SOT26	3,000	Tape & Reel
DMN3115UDM-13	SOT26	10,000	Tape & Reel
DMN3115UDMQ-13	SOT26	10,000	Tape & Reel

- Notes:
1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
 2. See <https://www.diodes.com/quality/lead-free/> for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
 4. For packaging details, go to our website at <https://www.diodes.com/design/support/packaging/diodes-packaging/>.

Marking Information



3N1 = Product Type Marking Code
 YM = Date Code Marking
 Y = Year (ex: K = 2023)
 M = Month (ex: 1 = January)

Date Code Key

Year	2007	...	2023	2024	2025	2026	2027	2028	2029	2030	2031	2032
Code	U	...	K	L	M	N	O	P	R	S	T	U

Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	O	N	D

Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Units
Drain-Source Voltage	V _{DSS}	30	V
Gate-Source Voltage	V _{GSS}	±8	V
Drain Current (Note 5)	I _D	3.2	A
Pulsed Drain Current (Note 5)	I _{DM}	12.8	A

Thermal Characteristics

Characteristic	Symbol	Value	Units
Total Power Dissipation (Note 5)	P _D	900	mW
Thermal Resistance, Junction to Ambient	R _{θJA}	139	°C/W
Operating and Storage Temperature Range	T _J , T _{STG}	-55 to +150	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 6)						
Drain-Source Breakdown Voltage	BV _{DSS}	30	—	—	V	V _{GS} = 0V, I _D = 100μA
Zero Gate Voltage Drain Current	I _{DSS}	—	—	1	μA	V _{DS} = 30V, V _{GS} = 0V
Gate-Source Leakage	I _{GSS}	—	—	±5	μA	V _{GS} = ±8V, V _{DS} = 0V
ON CHARACTERISTICS (Note 6)						
Gate Threshold Voltage	V _{GS(TH)}	0.5	—	1.0	V	V _{DS} = V _{GS} , I _D = 250μA
Static Drain-Source On-Resistance	R _{DS(ON)}	—	40	60	mΩ	V _{GS} = 4.5V, I _D = 6A
		—	50	80		V _{GS} = 2.5V, I _D = 2A
		—	76	130		V _{GS} = 1.5V, I _D = 1.0A
Forward Transfer Admittance	Y _{fs}	—	8	—	S	V _{DS} = 10V, I _D = 6A
Diode Forward Voltage (Note 6)	V _{SD}	—	0.7	1.1	V	V _{GS} = 0V, I _S = 2A
DYNAMIC CHARACTERISTICS						
Input Capacitance	C _{iss}	—	476	—	pF	V _{DS} = 15V, V _{GS} = 0V, f = 1.0MHz
Output Capacitance	C _{oss}	—	77	—	pF	
Reverse Transfer Capacitance	C _{rss}	—	59	—	pF	

Notes: 5. Device mounted on FR-4 PCB, minimum recommended pad layout on 2oz. Copper pads.
6. Short duration pulse test used to minimize self-heating effect.

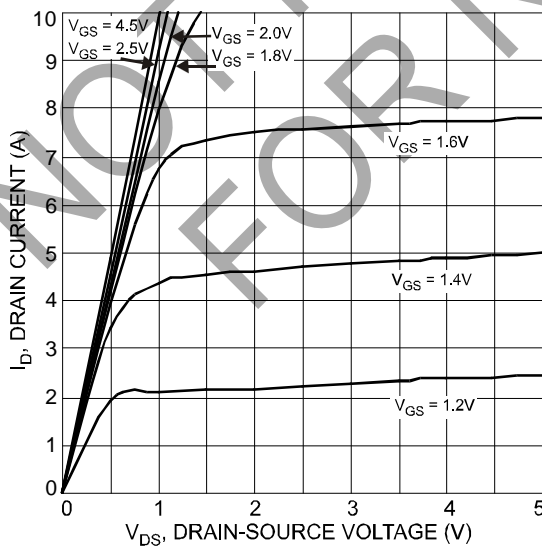


Fig.1 Typical Output Characteristic

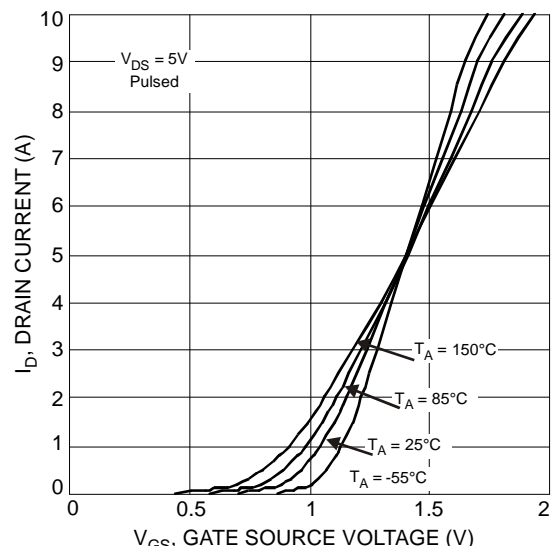


Fig. 2 Typical Transfer Characteristics

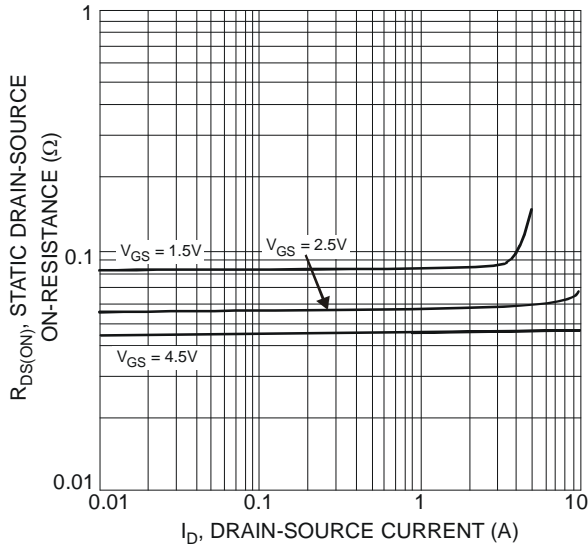


Fig. 3 On-Resistance vs. Drain Current & Gate Voltage

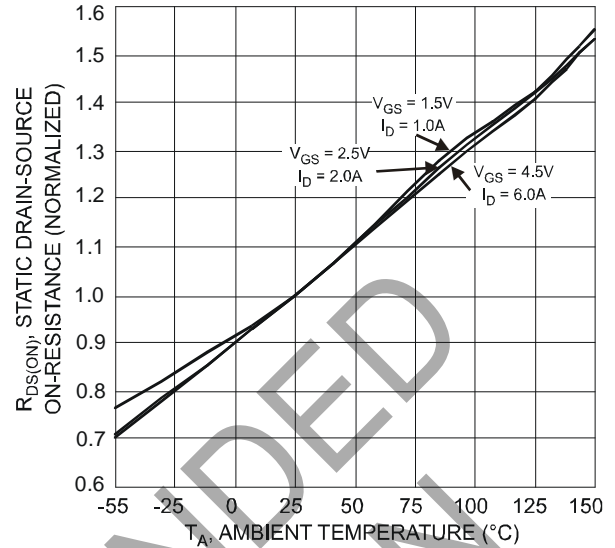


Fig. 4 Normalized Static Drain-Source On-Resistance vs. Ambient Temperature

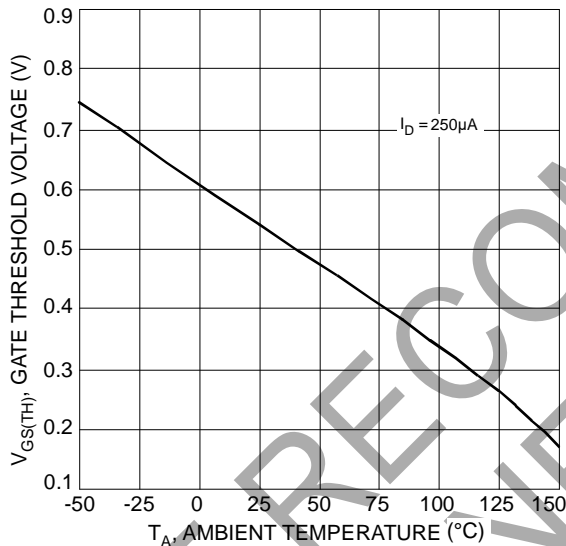


Fig. 5 Gate Threshold Variation vs. Ambient Temperature

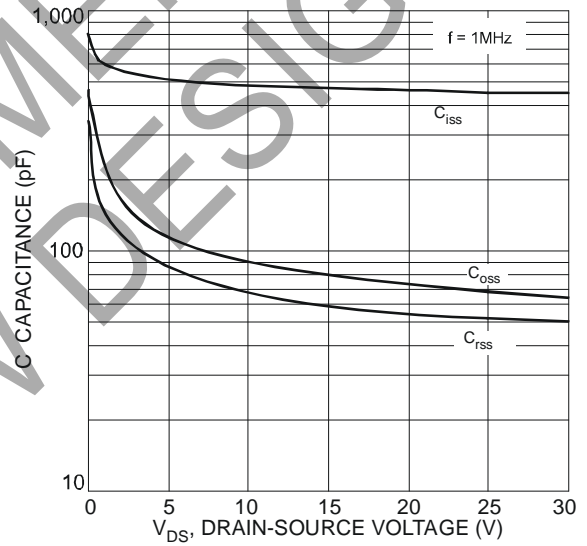


Fig. 6 Typical Total Capacitance

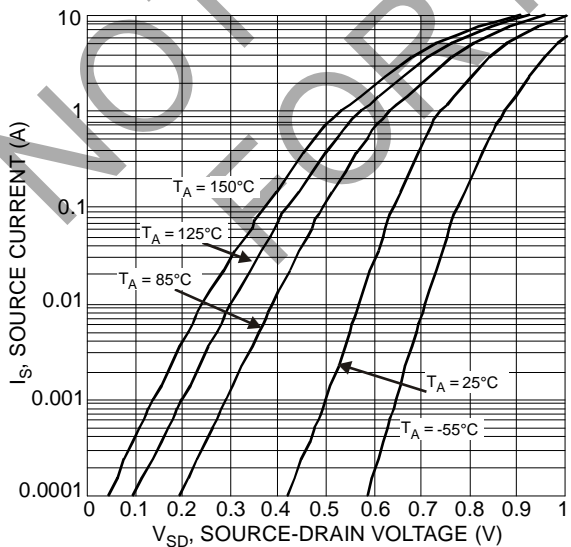
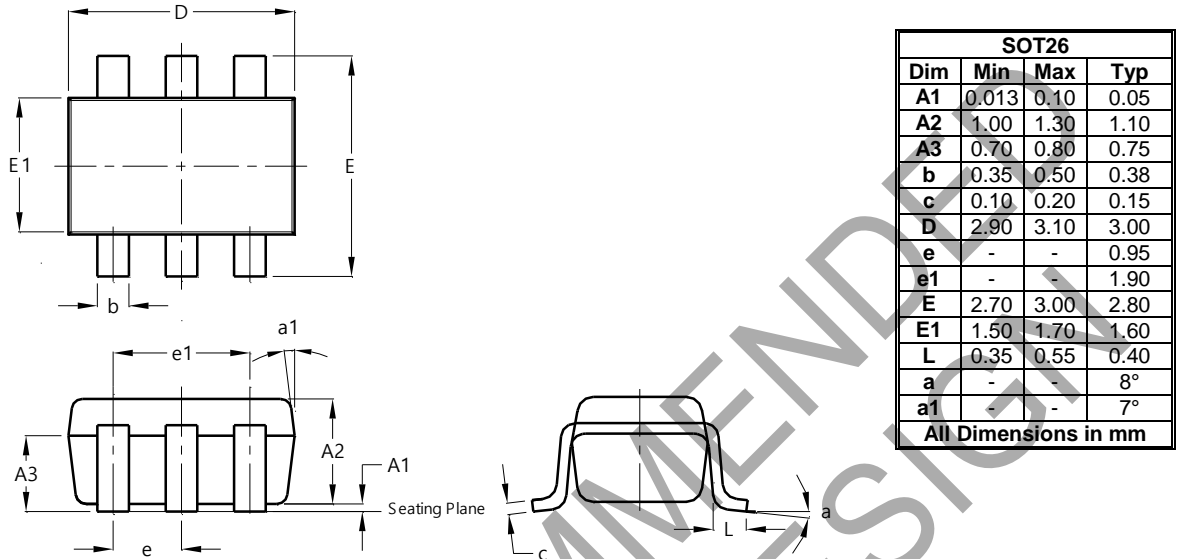


Fig. 7 Reverse Drain Current vs. Source-Drain Voltage

Package Outline Dimensions

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

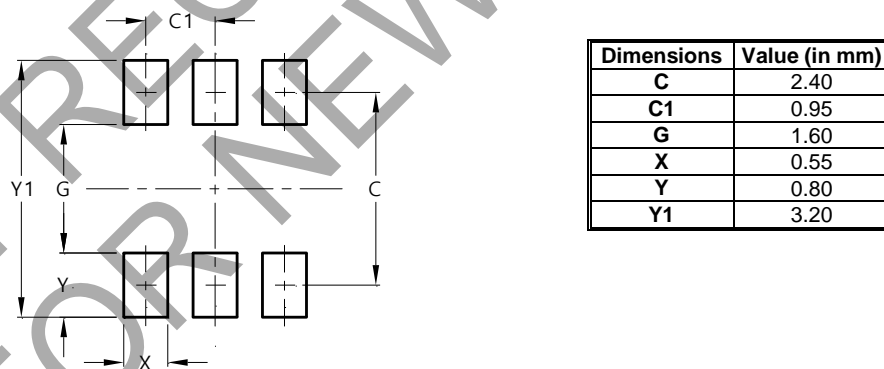
SOT26



Suggested Pad Layout

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

SOT26



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