

## Product Summary

Symbol	Value	Unit
$I_{T(RMS)}$	12	A
$V_{DRM} V_{RRM}$	600 / 800	V
$V_{TM}$	1.55	V

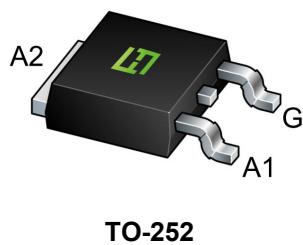
## Feature

With high ability to withstand the shock loading of large current, With high commutation performances, 4 quadrants products especially recommended for use on inductive load.

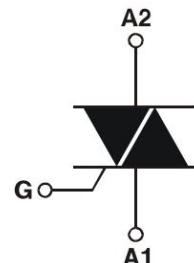
## Application

Washing machine, vacuums, massager, solid state relay, AC Motor speed regulation and so on.

## Package



## Circuit diagram



## Marking



**Absolute maximum ratings (Ta=25°C unless otherwise noted)**

Parameter	Symbol	Value		Unit
Repetitive peak off-state voltage	V <sub>DRM</sub>	600 / 800		V
Repetitive peak reverse voltage	V <sub>RRM</sub>	600 / 800		V
RMS on-state current	I <sub>T(RMS)</sub>	12		A
Non repetitive surge peak on-state current (full cycle, F=50Hz)	I <sub>TSM</sub>	95		A
I <sup>2</sup> t value for fusing (tp=10ms)	I <sup>2</sup> t	45		A <sup>2</sup> s
Critical rate of rise of on-state current (I <sub>G</sub> =2×I <sub>GT</sub> )	dI <sub>T</sub> /dt	I - II - III IV	50 10	A/μs
Peak gate current	I <sub>GM</sub>	2		A
Average gate power dissipation	P <sub>G(AV)</sub>	0.5		W
Junction Temperature	T <sub>J</sub>	-40 ~ +125		°C
Storage Temperature	T <sub>STG</sub>	-40 ~ +150		°C

**Electrical characteristics (T<sub>A</sub>=25 °C, unless otherwise noted)**

Parameter	Symbol	Test Condition		Value		Unit
Gate trigger current	I <sub>GT</sub>	V <sub>D</sub> =12V I <sub>T</sub> =0.1A T <sub>j</sub> =25°C	I - II - III	MAX.	10	mA
Gate trigger voltage	V <sub>GT</sub>		IV		25	
Gate non-trigger voltage	V <sub>GD</sub>	V <sub>D</sub> =V <sub>DRM</sub> T <sub>j</sub> =125°C		MIN.	0.2	V
latching current	I <sub>L</sub>	V <sub>D</sub> =12V I <sub>GT</sub> =0.1A T <sub>j</sub> =25°C	I - III - IV	MAX.	30	mA
Holding current	I <sub>H</sub>		II		40	
Critical-rate of rise of commutation voltage	dV <sub>D</sub> /dt	V <sub>D</sub> =2/3V <sub>DRM</sub> Gate Open T <sub>j</sub> =125°C		MIN.	20	V/μs
<b>STATIC CHARACTERISTICS</b>						
Forward "on" voltage	V <sub>TM</sub>	I <sub>TM</sub> =15A tp=380μs		MAX.	1.55	V
Repetitive Peak Off-State Current	I <sub>DRM</sub>	V <sub>D</sub> =V <sub>DRM</sub> V <sub>R</sub> =V <sub>RRM</sub>	T <sub>j</sub> =25°C	MAX.	10	μA
Repetitive Peak Reverse Current	I <sub>RRM</sub>		T <sub>j</sub> =125°C	MAX.	1	mA
<b>THERMAL RESISTANCES</b>						
Thermal resistance	R <sub>th(j-c)</sub>	Junction to case(AC)		TYP.	1.4	°C/W
	R <sub>th(j-a)</sub>	Junction to ambient		TYP.	70	°C/W

## Typical Characteristics

FIG.1: Maximum power dissipation versus RMS on-state current (full cycle)

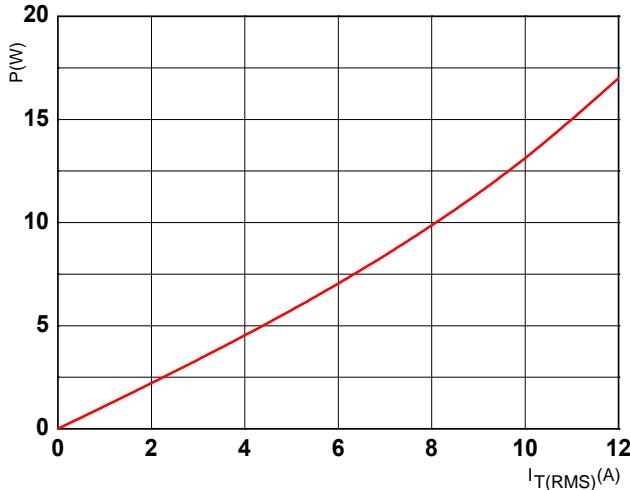


FIG.2: RMS on-state current versus case temperature (full cycle)

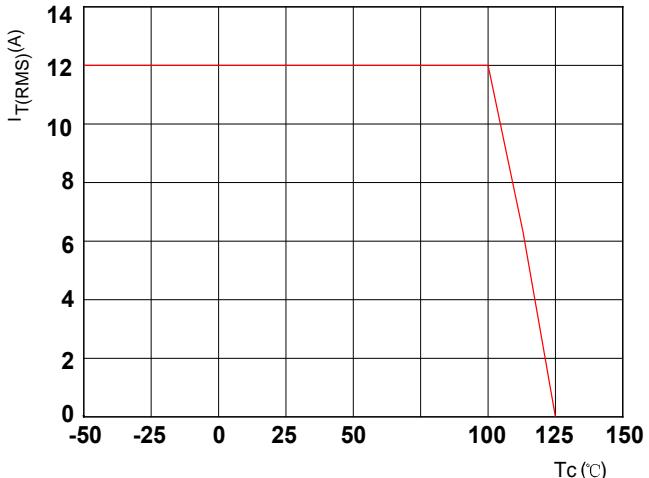


FIG.3: Surge peak on-state current versus number of cycles

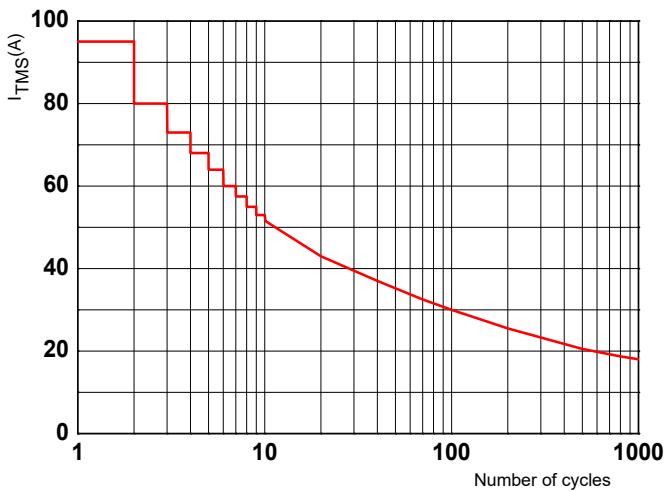


FIG.5: Non-repetitive surge peak on-state current for a sinusoidal pulse with width  $t_p < 10\text{ms}$

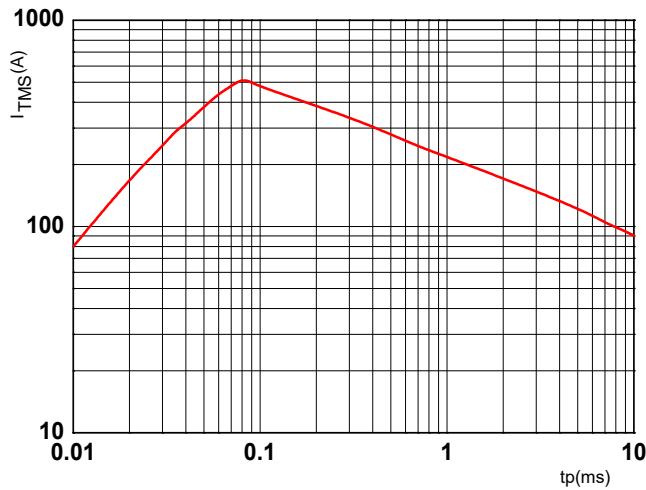


FIG.4: On-state characteristics (maximum values)

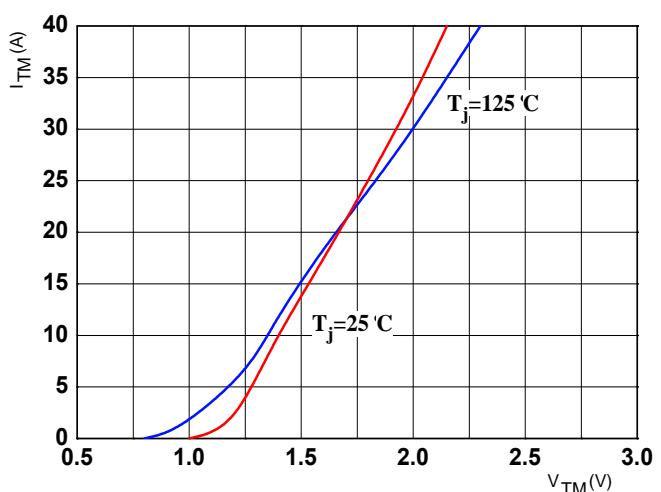
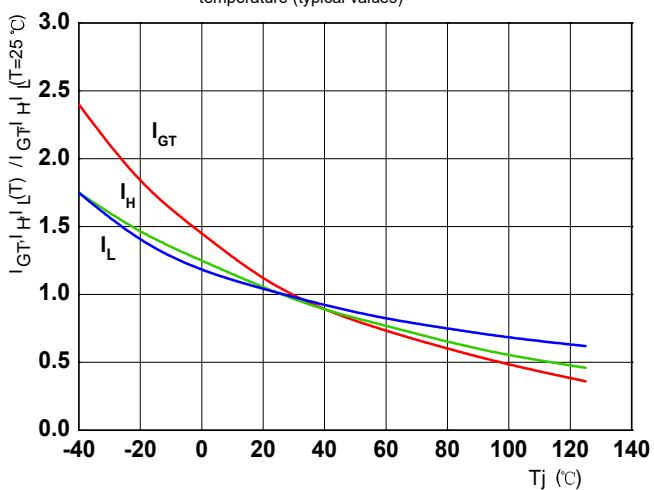
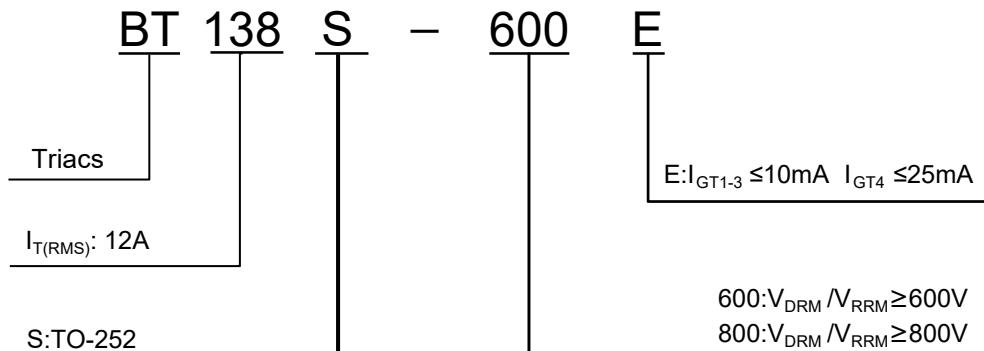


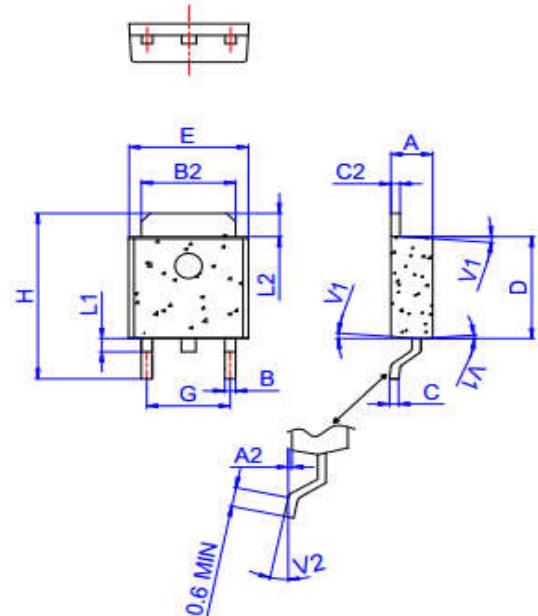
FIG.6: Relative variations of gate trigger current, holding current and latching current versus junction temperature (typical values)



### Ordering Information



### TO-252 Package Information



Ref.	Dimensions					
	Millimeters			Inches		
	Min.	Typ.	Max.	Min.	Typ.	Max.
A	2.20		2.40	0.086		0.095
A2	0.03		0.23	0.001		0.009
B	0.55		0.65	0.022		0.026
B2	5.10		5.40	0.200		0.213
C	0.45		0.62	0.018		0.024
C2	0.48		0.85	0.019		0.034
D	5.30		6.20	0.208		0.244
E	6.40		6.70	0.252		0.264
G	4.40		4.70	0.173		0.185
H	9.35		10.6	0.368		0.417
L1	1.30		1.70	0.051		0.067
L2	1.37		1.50	0.054		0.059
V1		4°			4°	
V2	0°		8°	0°		8°