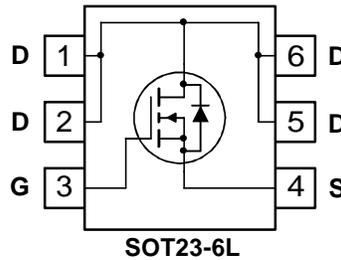


Features

- 2.6 A, 100 V $R_{DS(ON)} = 90\text{ m}\Omega @ V_{GS} = 10\text{ V}$
 $R_{DS(ON)} = 85\text{ m}\Omega @ V_{GS} = 4.5\text{ V}$
- High performance trench technology for extremely low $R_{DS(ON)}$
- Low gate charge (14nC typ)
- High power and current handling capability
- Fast switching speed

Applications

- DC/DC converter

Pin Configuration

Absolute Maximum Ratings $T_A=25^\circ\text{C}$ unless otherwise noted

Symbol	Parameter	Ratings	Units
V_{DSS}	Drain-Source Voltage	100	V
V_{GSS}	Gate-Source Voltage	± 20	V
I_D	Drain Current – Continuous (Note 1a)	2.6	A
	– Pulsed	20	
P_D	Maximum Power Dissipation (Note 1a) (Note 1b)	1.6	W
		0.8	
T_J, T_{STG}	Operating and Storage Junction Temperature Range	-55 to +125	$^\circ\text{C}$

Thermal Characteristics

$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient (Note 1a)	78	$^\circ\text{C/W}$
$R_{\theta JC}$	Thermal Resistance, Junction-to-Case (Note 1)	30	$^\circ\text{C/W}$

Package Marking and Ordering Information

Device Marking	Device	Reel Size	Tape width	Quantity
JY10220	JY10220	7"	8mm	3000 units

Electrical Characteristics						
<small>T_A = 25°C unless otherwise noted</small>						
Symbol	Parameter	Test Conditions	Min	Typ	Max	Units
Drain-Source Avalanche Ratings (Note 2)						
W _{DSS}	Drain-Source Avalanche Energy	Single Pulse, V _{DD} = 50 V, I _D = 2.6 A			90	mJ
I _{AR}	Drain-Source Avalanche Current				2.6	A
Off Characteristics						
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} = 0 V, I _D = 250 μA	100			V
$\frac{\Delta BV_{DSS}}{\Delta T_J}$	Breakdown Voltage Temperature Coefficient	I _D = 250 μA, Referenced to 25°C		99		mV/°C
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} = 80 V, V _{GS} = 0 V			10	μA
I _{GSSF}	Gate-Body Leakage, Forward	V _{GS} = 20 V, V _{DS} = 0 V			100	nA
I _{GSSR}	Gate-Body Leakage, Reverse	V _{GS} = -20 V, V _{DS} = 0 V			-100	nA
On Characteristics (Note 2)						
V _{GS(th)}	Gate Threshold Voltage	V _{DS} = V _{GS} , I _D = 250 μA	1	2.3	3	V
$\frac{\Delta V_{GS(th)}}{\Delta T_J}$	Gate Threshold Voltage Temperature Coefficient	I _D = 250 μA, Referenced to 25°C		-6		mV/°C
R _{DS(on)}	Static Drain-Source On Resistance	V _{gs} = 10 V, I _d = 1 A V _{gs} = 4.5 V, I _d = 1 A		85 90	110 125	mΩ
I _{D(on)}	On-State Drain Current	V _{GS} = 10 V, V _{DS} = 5 V	10			A
g _{FS}	Forward Transconductance	V _{DS} = 10 V, I _D = 2.6 A		10		S
Dynamic Characteristics						
C _{iss}	Input Capacitance	V _{DS} = 50 V, V _{GS} = 0 V, f = 1.0 MHz		660		pF
C _{oss}	Output Capacitance			55		pF
C _{rss}	Reverse Transfer Capacitance			40		pF
Switching Characteristics (Note 2)						
t _{d(on)}	Turn-On Delay Time	V _{DD} = 50 V, I _D = 1 A, V _{GS} = 10 V, R _{GEN} = 6 Ω		6	11	ns
t _r	Turn-On Rise Time			3.5	7	ns
t _{d(off)}	Turn-Off Delay Time			23	37	ns
t _f	Turn-Off Fall Time			3.7	7.4	ns
Q _g	Total Gate Charge	V _{DS} = 50 V, I _D = 2.6 A, V _{GS} = 10 V		14	20	nC
Q _{gs}	Gate-Source Charge			2.3		nC
Q _{gd}	Gate-Drain Charge			3.6		nC
Drain-Source Diode Characteristics and Maximum Ratings						
I _S	Maximum Continuous Drain-Source Diode Forward Current				1.3	A
V _{SD}	Drain-Source Diode Forward Voltage	V _{GS} = 0 V, I _S = 1.3 A (Note 2)		0.76	1.2	V
t _{rr}	Diode Reverse Recovery Time	I _F = 2.6 A		31		nS
Q _{rr}	Diode Reverse Recovery Charge	d _{IF} /d _t = 100 A/μs (Note 2)		56		nC

Notes:

- R_{θJA} is the sum of the junction-to-case and case-to-ambient resistance where the case thermal reference is defined as the solder mounting surface of the drain pins. R_{θJC} is guaranteed by design while R_{θCA} is determined by the user's board design.
 - 78°C/W when mounted on a 1in² pad of 2oz copper on FR-4 board.
 - 156°C/W when mounted on a minimum pad.
- Pulse Test: Pulse Width ≤ 300 μs, Duty Cycle

Typical Characteristics

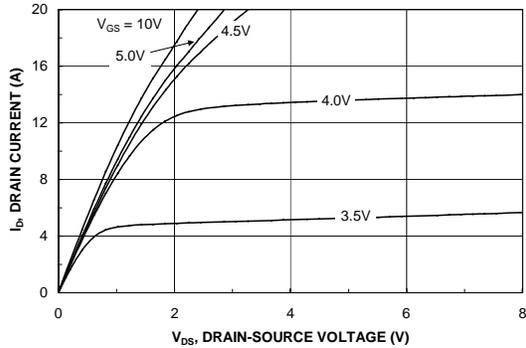


Figure 1. On-Region Characteristics.

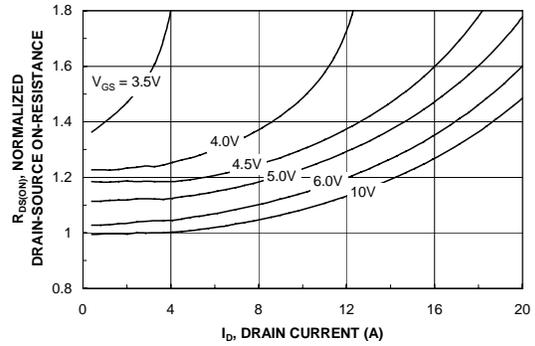


Figure 2. On-Resistance Variation with Drain Current and Gate Voltage.

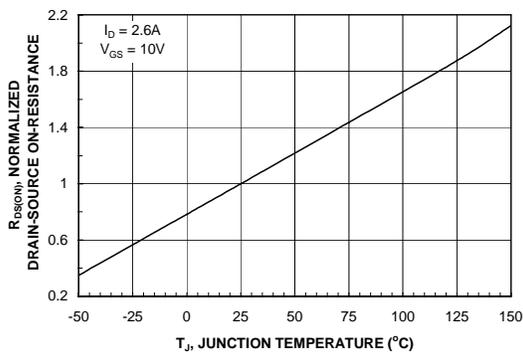


Figure 3. On-Resistance Variation with Temperature.

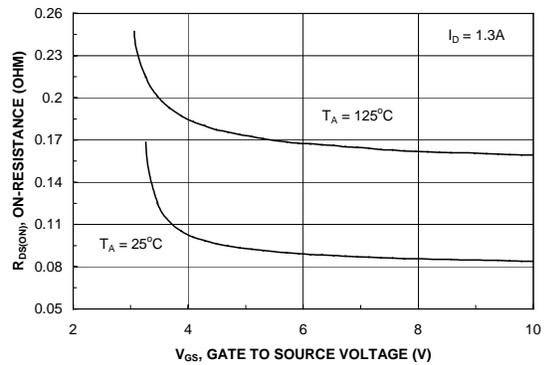


Figure 4. On-Resistance Variation with Gate-to-Source Voltage.

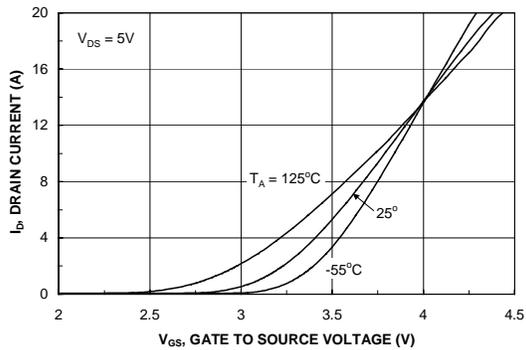


Figure 5. Transfer Characteristics.

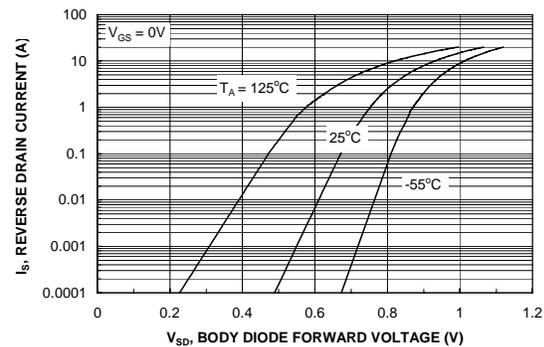
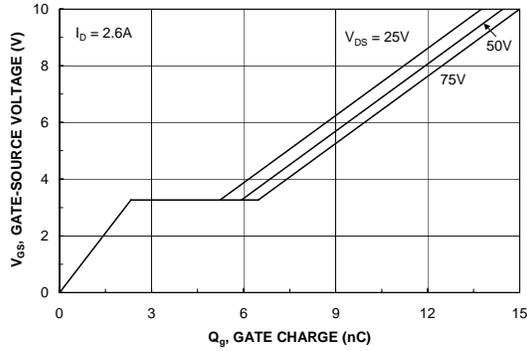
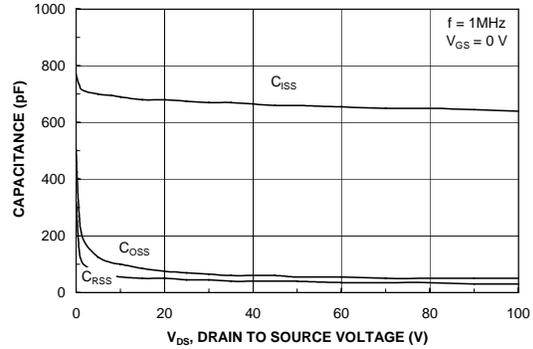
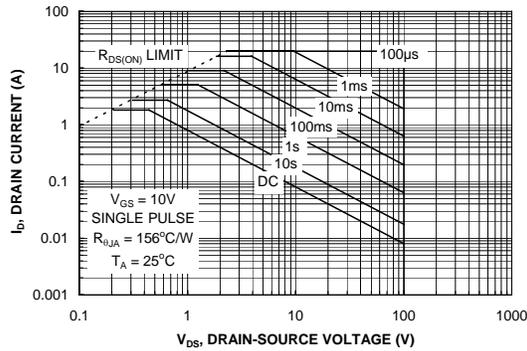
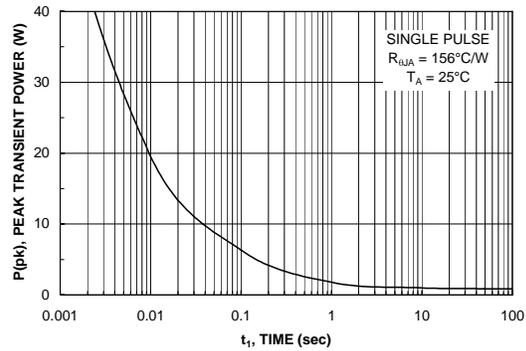
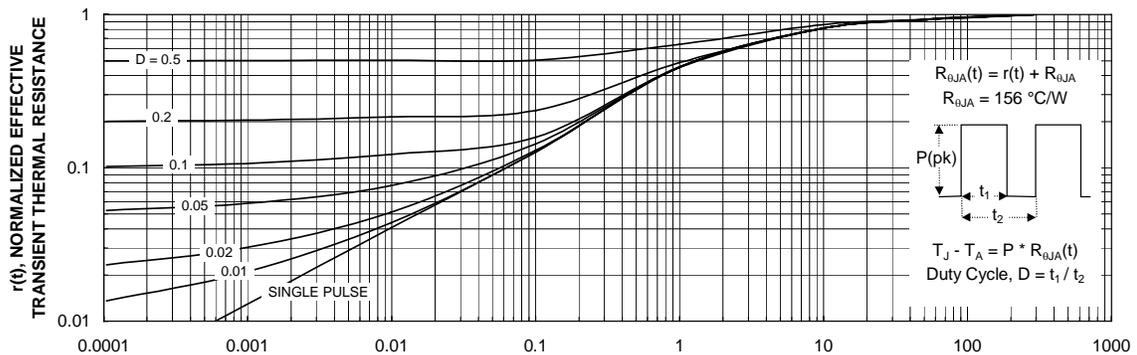


Figure 6. Body Diode Forward Voltage Variation with Source Current and Temperature.

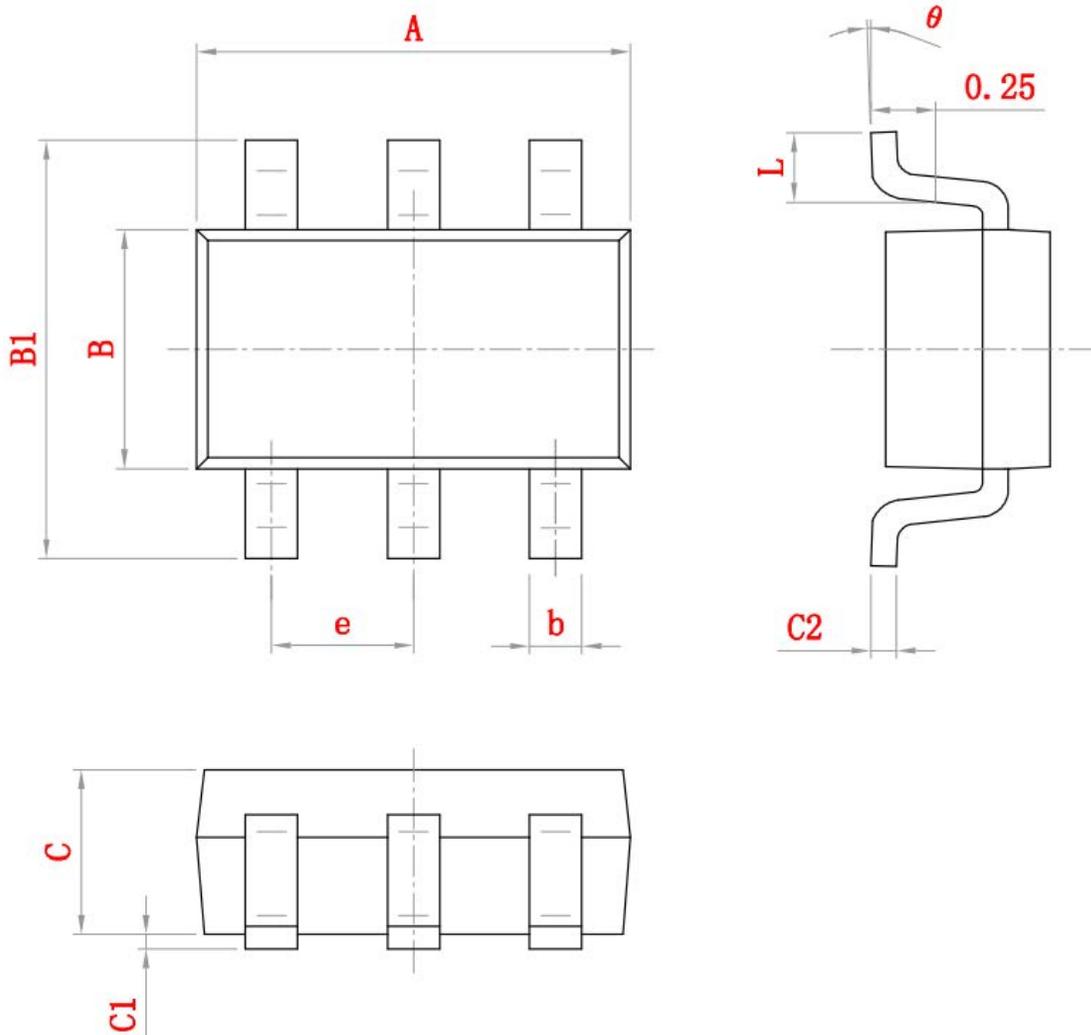
Typical Characteristics

Figure 7. Gate Charge Characteristics.

Figure 8. Capacitance Characteristics.

Figure 9. Maximum Safe Operating Area.

Figure 10. Single Pulse Maximum Power Dissipation.

Figure 11. Transient Thermal Response Curve.

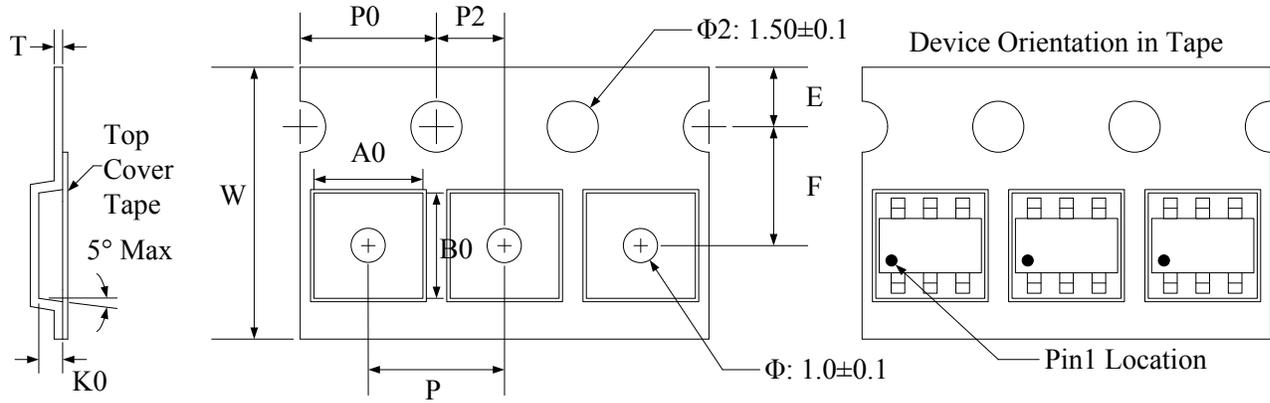
Thermal characterization performed using the conditions described in Note 1b.
 Transient thermal response will change depending on the circuit board design.

Package Outline

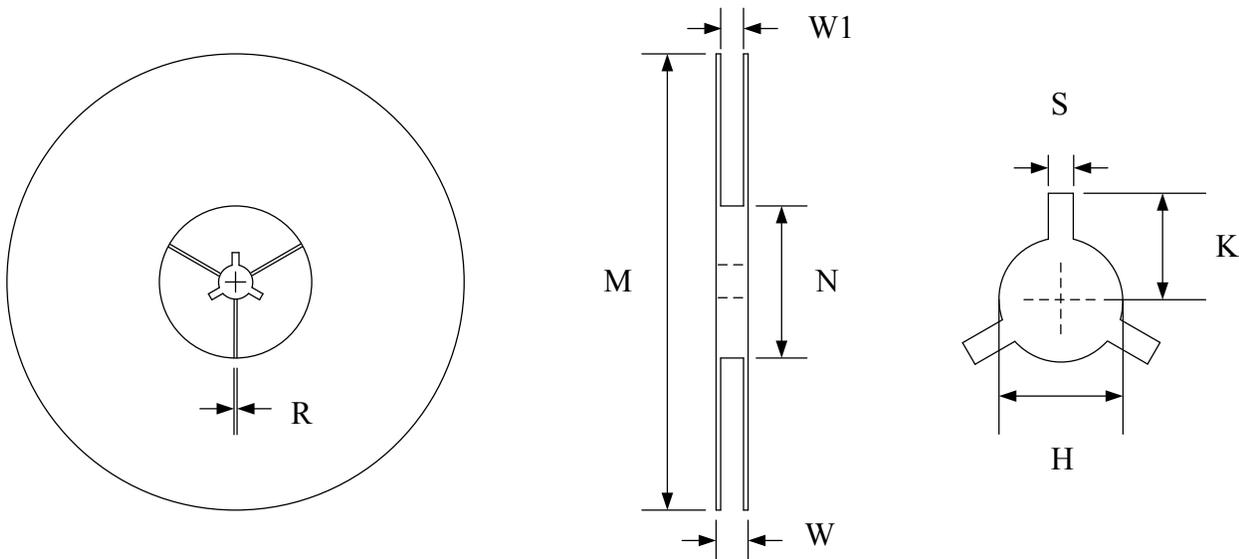
□ SOT23-6L package

尺寸 标注	最小 (mm)	最大 (mm)	尺寸 标注	最小 (mm)	最大 (mm)
A	2.82	3.02	C	1.05	1.15
e	0.95 (BSC)		C1	0.03	0.15
b	0.28	0.45	C2	0.12	0.23
B	1.50	1.70	L	0.35	0.55
B1	2.60	3.00	θ	0°	8°



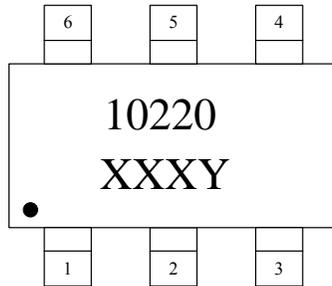
Tape and Reel Specification


Symbol	W	A0	B0	K0	E	F	P	P0	P2	T
Dimensions (mm)	8.00 ± 0.3 -0.1	3.23 ± 0.05	3.17 ± 0.05	1.37 ± 0.05	1.75 ± 0.1	3.5 ± 0.05	4.0 ± 0.1	4.0 ± 0.1	2.0 ± 0.05	0.25 ± 0.02



Symbol	Reel Size	M	N	W	W1	H	S	K	R
Dimensions (mm)	$\Phi 178$	178.0 ± 1.0	60.0 ± 1.0	11.5 ± 0.5	9.0 ± 0.5	13.0 ± 0.5	2.0 ± 0.1	11.0 ± 0.2	1.0 ± 0.05

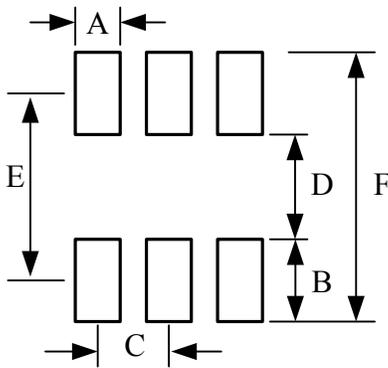
Marking Codes



Note:

- (1) “10220” is the part number, fixed.
- (2) “XXX” is the last 3 characters of the wafer's Lot No.,
“Y” is the internal code.

Footprint: SOT23-6L



Symbol	Dimensions	
	Millimeters	Inches
A	0.60	0.024
B	1.10	0.043
C	0.95	0.037
D	1.40	0.055
E	2.50	0.098
F	3.60	0.141