

N-Channel Enhancement Mode Field Effect Transistor

General Description

The 200N08A is a N-channel Power MOSFET. It has specifically been designed to minimize input capacitance and gate charge. The device is therefore suitable in advanced high-efficiency switching applications.

Features

- Advanced Process Technology
- Ultra Low On-Resistance
- Dynamic dv/dt Rating
- 175°C Operating Temperature
- Fast Switching
- Fully Avalanche Rated
- Lead-Free

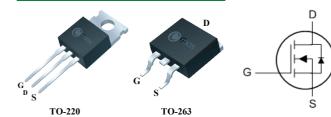
Product Summary

BVDSS	RDSON	ID
80V	$3.5 \text{m}\Omega$	140A

Applications

- LED power controller
- DC-DC & DC-AC converters
- High current, High speed switching
- Solenoid and relay drivers
- Motor control, Audio amplifiers

TO-220/263 Pin Configuration



Туре	Package	Marking
CMP200N08A	TO-220	CMP200N08A
CMB200N08A	TO-263	CMB200N08A

Absolute Maximum Ratings

Symbol	Parameter	Rating	Units	
V_{DS}	Drain-Source Voltage	80	V	
V _{GS}	Gate-Source Voltage	±20	V	
I _D @T _C =25°C	Continuous Drain Current,VGS @ 10V	140	Α	
I _D @T _C =100°C	Continuous Drain Current,VGS @ 10V	112	А	
I _{DM}	Pulsed Drain Current	420	А	
EAS	Single Pulse Avalanche Energy	940	mJ	
P _D @T _C =25°C	Power Dissipation	210	W	
T _{STG}	Storage Temperature Range	-55 to 175	°C	
T _J	Operating Junction Temperature Range	-55 to 175	°C	

Thermal Data

Symbol	Parameter	Тур.	Max.	Unit	
$R_{\theta JA}$	Junction-to-Ambient (PCB mount)		65	°C/W	
$R_{ heta JC}$	Junction-to-Case		0.70	°C/W	

CMP200N08A/CMB200N08A



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Electrical Characteristics (T_J=25 ℃, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V , I _D =250uA	80			V
R _{DS(ON)}	Static Drain-Source On-Resistance	V_{GS} =10V , I_D =50A			3.5	mΩ
V _{GS(th)}	Gate Threshold Voltage	V _{GS} =V _{DS} , I _D =250uA	2		4	V
I _{DSS}	Drain-Source Leakage Current	V _{DS} =80V, V _{GS} =0V			1	uA
I _{GSS}	Gate-Source Leakage Current	$V_{GS} = \pm 20V$, $V_{DS} = 0V$			±100	nA
gfs	Forward Transconductance	V_{DS} =10V , I_{D} =30A		33		S
R_g	Gate Resistance	V_{DS} =0 V , V_{GS} =0 V , f=1 MHz		1.8		Ω
Q_g	Total Gate Charge	I _D =75A		95		
Q_gs	Gate-Source Charge	V _{DD} =40V		30		nC
Q_{gd}	Gate-Drain Charge	V _{GS} = 10 V		22		
$T_{d(on)}$	Turn-On Delay Time	V _{DD} =40V		18		
T _r	Rise Time	I _D =75A		11		ns
$T_{d(off)}$	Turn-Off Delay Time	$R_G=3\Omega$		43		115
T _f	Fall Time	V _{GS} =10V		10		
C _{iss}	Input Capacitance			4700		
Coss	Output Capacitance	V _{DS} =40V , V _{GS} =0V , f=1MHz		980		pF
C _{rss}	Reverse Transfer Capacitance			63		

Diode Characteristics

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
Is	Continuous Source Current	V _G =V _D =0V , Force Current			140	Α
I _{SM}	Pulsed Source Current				420	Α
V _{SD}	Diode Forward Voltage	V _{GS} =0V , I _S =30A , T _J =25℃			1.2	V

Note:

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