

#### **N-Channel Enhancement Mode MOSET**

## **General Description**

Advanced Power MOSFETs from
Cmos provide the designer with the
best combination of fast switching
and low on-resistance. This device
is well suited for Power Management
and load switching applications
common in Notebook Computers and
Portable Battery Packs.

#### **Features**

- N-Channel MOSFET
- Low ON-resistance
- Surface Mount Package
- RoHS Compliant

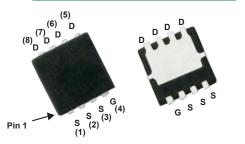
## **Product Summary**

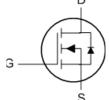
BVDSS	RDSON	ID
30V	5mΩ	90A

## **Applications**

- High side in DC DC Buck Converters
- Notebook battery power management
- Load switch in Notebook

# **DFN-8 3.3x3.3 Pin Configuration**





DFN-8 3.3x3.3

Туре	Package	Marking		
CMSC3006	DFN-8 3.3*3.3	3006		

## **Absolute Maximum Ratings**

Symbol	Parameter	Rating	Units
$V_{DS}$	Drain-Source Voltage	30	V
V <sub>GS</sub>	Gate-Source Voltage	±20	V
I <sub>D</sub> @T <sub>C</sub> =25℃	Continuous Drain Current	90	Α
I <sub>D</sub> @T <sub>C</sub> =100℃	Continuous Drain Current	58	А
I <sub>DM</sub>	Pulsed Drain Current	360	А
EAS	Single Pulse Avalanche Energy <sup>1</sup>	196	mJ
P <sub>D</sub> @T <sub>C</sub> =25℃	Total Power Dissipation	45	W
T <sub>STG</sub>	Storage Temperature Range	-55 to 150	$^{\circ}$
TJ	Operating Junction Temperature Range	-55 to 150	$^{\circ}$

#### **Thermal Data**

Symbol	Parameter	Тур.	Max.	Unit
R <sub>θJA</sub>	Thermal Resistance, Junction-to-Ambient		75	°C/W



#### **N-Channel Enhancement Mode MOSFET**

# Electrical Characteristics (T<sub>J</sub>=25℃, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	$V_{GS}$ =0V , $I_D$ =250uA	30			V
D	Static Drain-Source On-Resistance	$V_{GS}$ =10V , $I_D$ =28A		4.5	5	mΩ
R <sub>DS(ON)</sub>	Static Dialii-Source Off-Resistance	$V_{GS}$ =4.5V , $I_D$ =20A		6	7.5	11122
VGS(th)	Gate Threshold Voltage	$V_{GS}$ = $V_{DS}$ , $I_D$ = 250 $\mu$ A	1		2.5	V
I <sub>DSS</sub>	Drain-Source Leakage Current	V <sub>DS</sub> =30V , V <sub>GS</sub> =0V			1	uA
I <sub>GSS</sub>	Gate-Source Leakage Current	V <sub>GS</sub> = ±20V			±100	nA
gfs	Forward Transconductance	$V_{DS}$ =5V , $I_D$ =20A		29		S
Qg	Total Gate Charge	V <sub>DS</sub> =15V , I <sub>D</sub> =15A 		20		
$Q_gs$	Gate-Source Charge			7.5		nC
$Q_gd$	Gate-Drain Charge			7		
$T_{d(on)}$	Turn-On Delay Time			10		
T <sub>r</sub>	Rise Time	$V_{DD}$ =15V , $V_{GS}$ =10V , $R_{G}$ =3.3 $\Omega$		15		, no
T <sub>d(off)</sub>	Turn-Off Delay Time			40		ns
$T_f$	Fall Time			12		
C <sub>iss</sub>	Input Capacitance			2400		
Coss	Output Capacitance	V <sub>DS</sub> = 25V, V <sub>GS</sub> =0V, f=1MHz		250		pF
C <sub>rss</sub>	Reverse Transfer Capacitance			230		

## **Diode Characteristics**

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
Is	Diode continuous forward current	- V <sub>G</sub> =V <sub>D</sub> =0V , Force Current			90	Α
I <sub>S,pulse</sub>	Diode pulse current				360	Α
$V_{SD}$	Diode Forward Voltage	V <sub>GS</sub> =0V , I <sub>F</sub> =25A , Tj=25℃			1.2	V

#### Notes:

<sup>1.</sup>The EAS data shows Max. rating . The test condition is VDD=-20V,VGS=-10V,L=0.5mH,IAS=-28A

This product has been designed and qualified for the counsumer market.

Cmos assumes no liability for customers' product design or applications.

 $<sup>\</sup>label{lem:constraint} \mbox{Cmos reserver the right to improve product design , functions and reliability without notice.}$