TAI-TECH **KBM01-230300734** P2.

# High Current Ferrite Chip Bead(Lead Free)

HCB2012MF-050T60

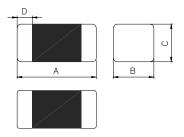
Certificate

Green Partner

## 1.Features

- 1. Monolithic inorganic material construction.
- 2. Closed magnetic circuit avoids crosstalk.
- 3. Suitable for reflow soldering.
- 4. Shapes and dimensions follow E.I.A. spec.
- 5. Available in various sizes.
- 6. Excellent solder ability and heat resistance.
- 7. High reliability.
- 8. 100% Lead(Pb) & Halogen-Free and RoHS compliant.
- 9. Low DC resistance structure of electrode to prevent wasteful electric power consumption.
- 10. Operating Temperature: --55~+125°C (Including self-temperature rise)

## 2. Dimensions



Chip Size						
Α	2.00±0.20					
В	1.25±0.20					
С	0.85±0.20					
D	0.50±0.30					

Units: mm

# 3.Part Numbering



A: Series

**B**: Dimension

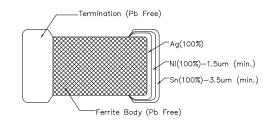
C: Material Lead Free Material

D: Impedance

E: Packaging T=Taping and Reel, B=Bulk(Bags)

LxW

F: Rated Current

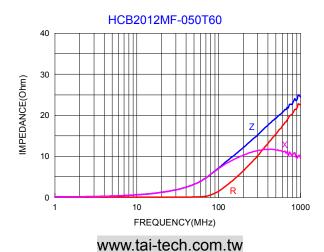


# 4. Specification

Tai-Tech Part Number	Impedance ( $\Omega$ )	Test Frequency (Hz)	DC Resistance $(\Omega)$ max.	Rated Current (mA) max.
HCB2012MF-050T60	5±25%	60mV/100M	0.01	6000

- Rated current: based on temperature rise test
- In compliance with EIA 595

## Impedance-Frequency Characteristics



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# 5. Reliability and Test Condition

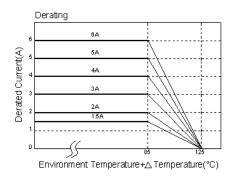
Item			ا	Performanc	е			Te	st Con	dition		
Series No.	FCB	FCM	1	HCB	GHB	FCA						
Operating Temperature		(	Includin	-55~+125℃ g self-tempera								
Transportation Storage Temperature				-55~+125℃ (on board)				ng storaç ation Noti		ons, please	see the	
Impedance (Z)	Defer to etc.	ndord cloatri	ioal aba	racteristics list			Agilent Agilent Agilent Agilent	E4991 4287				
DC Resistance	_ Relei to stai	ndard electri	icai cria	racteristics list			Agilent					
Rated Current										ements, the	ere will be	
Temperature Rise Test		tated Current < 1A ΔT 20℃Max tated Current ≧ 1A ΔT 40℃Max							llowed DC measured	current. by digital su	urface	
Life test	Appearance	e: no damag	e.				times.( Reflow Tempe Applied Duratio	IPC/JED Profiles) rature: 12 d current: on: 1000± red at ro	PEC J-STE 25±2°C rated curr :12hrs.	ough reflo 0-020E Class rent. erature afte	ssification	
Load Humidity		Impedance: within±15%of initial value.  RDC: within ±15% of initial value and shall not exceed the specification value					times.( Reflow Humid Tempe Duratio	IPC/JED Profiles) ty: 85±29 rature: 85 n:1000h Inductand red at ro	DEC J-STE %R.H. 5±2℃. rsMin.Bea ce: with	ough reflo 0-020E Clas d:with100%i 10% rated erature afte	ssification ratedcurr I current	
Thermal shock	Appearance: Impedance: RDC: within	within±15%	of initial		ot exceed the s	pecification va	Precor times.( Reflow Condit Step1: Step2: Step3: Number	Inditioning IPC/JED Profiles) Ipcon for 1 of the control of the co	DEC J-STE cycle 30±5 ≤ 0.9 30±5mi es: 500	5min	ssification	
Vibration	Appearance Impedance RDC: within	: within±15%	% of initi		ot exceed the s	pecification va	times.( Reflow Oscilla for 20 Equipr Total A Testing	IPC/JED Profiles) tion Fred minutes nent : V mplitude:	DEC J-STE quency: 1 ibration ch 10g 2 hours(2	ough reflo 0-020E Clas 0Hz ~ 2KHz ecker 0 minutes,	ssification z ∼ 10Hz	
Bending	Appearance Impedance RDC : within	: within±109	% of initi		ot exceed the s	pecification va	followin >=080 <0805 Bendir >=080 <0805	Shall be mounted on a FR4 substrate of the following dimensions: >=0805inch(2012mm):40x100x1.2mm <0805inch(2012mm):40x100x0.8mm Bending depth: >=0805inch(2012mm):1.2mm <0805inch(2012mm):0.8mm Duration of 10 sec for a min.				
							Test o	ondition	1:			
Shock	Appearance Impedance	: within±10%	% of initi		-1 -u- : 1 "		Туре	Peak Value (g's)	Normal duration (D) (ms)	Wave form	Velocity change (Vi)ft/sec	
	RDC : Within	n ±15% of in	ııtıaı valı	ue and shall n	ot exceed the s	pecification va		50	11	Half-sine	11.3	
										@155°C 0		
Solderability	More than 95% of the terminal electrode should be covered with solder.					b. Met ± 15 m	nod D ca in)@ 260	tegory 3.	+0/-0.5 seconomics (steam aginomics)			

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Item	Performance	Те	st Con	dition	
		Number of heat	cycles: 1		
Resistance to Soldering	Appearance : No damage.		Temperature (°C)	Time (s)	Temperature ramp/immersion and emersion rate
Heat	Impedance: within±15% of initial value RDC: within ±15% of initial value and shall not	260 ±5 (solder temp)	10 ±1	25mm/s ±6 mm/s	
		Depth: completely cover the termination			
Terminal strength	Appearance: No damage. Impedance: within±15% of initial value RDC: within ±15% of initial value and shall not exceed the specification value	Reflow Profiles) Component moi >0805inch(2012 <=0805inch(201 to the side of a shall be applied	unted on a 2mm):1kg 2mm):0.5 device bei d for 60 + oplied grad	0-020E Classification a PCB apply a force kg ing tested. This force 1 seconds. Also the dually as not to shock	

## \*\*Derating Curve

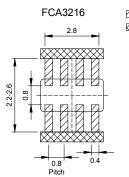
For the ferrite chip bead which withstanding current over 1.5A, as the operating temperature over  $85^{\circ}\mathbb{C}$ , the derating current information is necessary to consider with. For the detail derating of current, please refer to the Derated Current vs. Operating Temperature curve.



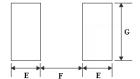
# 6. Soldering and Mounting

#### 6-1. Recommended PC Board Pattern

	Refle	w Sold	s For ering					
Series T	Гуре	A(mm)	B(mm)	C(mm)	D(mm)	E(mm)	F(mm)	G(mm)
FCB	1005	1.0±0.10	0.50±0.10	0.50±0.10	0.25±0.10	0.50	0.40	0.60
FCM	1608	1.6±0.15	0.80±0.15	0.80±0.15	0.30±0.20	0.80	0.85	0.95
HCB	2012	<mark>2.0±0.20</mark>	<mark>1.25±0.20</mark>	<mark>0.85±0.20</mark>	0.50±0.30	1.05	1.00	1.45
GHB	2012	2.0±0.20	1.25±0.20	1.25±0.20	0.50±0.30	1.00	1.00	1.45
FCI ;	3216	3.2±0.20	1.60±0.20	1.10±0.20	0.50±0.30	1.05	2.20	1.80
FHI ;	3225	3.2±0.20	2.50±0.20	1.30±0.20	0.50±0.30	1.05	2.20	2.70
FCH ,	4516	4.5±0.20	1.60±0.20	1.60±0.20	0.50±0.30	1.05	3.30	1.80
HCI 4	4532	4.5±0.20	3.20±0.20	1.50±0.20	0.50±0.30	1.05	3.30	3.40



Land
Solder Resist



PC board should be designed so that products can prevent damage from mechanical stress when warping the board.

## 6-2. Soldering

Mildly activated rosin fluxes are preferred. TAI-TECH terminations are suitable for re-flow soldering systems. If hand soldering cannot be avoided, the preferred technique is the utilization of hot air soldering tools.

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#### 6-2.1 Soldering Reflow:

Recommended temperature profiles for lead free re-flow soldering in Figure 1. Table 1.1&1.2 (J-STD-020E)

#### 6-2.2 Soldering Iron:

Products attachment with a soldering iron is discouraged due to the inherent process control limitations. In the event that a soldering iron must be employed the following precautions are recommended. (Figure 2.)

- Preheat circuit and products to  $150^{\circ}$ C  $350^{\circ}$ C tip temperature (max)
- Never contact the ceramic with the iron tip1.0mm tip diameter (max)
- Use a 20 watt soldering iron with tip diameter of 1.0mm
- · Limit soldering time to 4~5sec.

Fig.1 Soldering Reflow

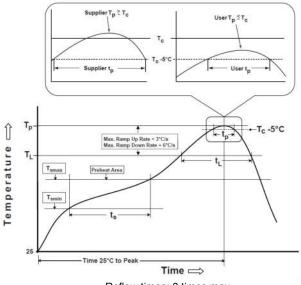
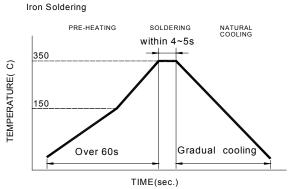


Fig.2 Iron soldering temperature profiles



Iron Soldering times: 1 times max

Reflow times: 3 times max

Table (1.1): Reflow Profiles

Profile Type:	Pb-Free Assembly
$eq:total_continuous_cont$	150°C 200°C 60-120seconds
Ramp-up rate(T <sub>L</sub> to T <sub>p</sub> )	3℃/second max.
Liquidus temperature(T <sub>L</sub> ) Time(t <sub>L</sub> )maintained above T <sub>L</sub>	217℃ 60-150 seconds
Classification temperature(T <sub>c</sub> )	See Table (1.2)
$Time(t_p)$ at Tc- $5^{\circ}\mathbb{C}$ (Tp should be equal to or less than Tc.)	< 30 seconds
Ramp-down rate(Tp to TL)	6°C /second max.
Time 25℃ to peak temperature	8 minutes max.

Tp: maximum peak package body temperature, Tc: the classification temperature.

For user (customer)  ${\bf Tp}$  should be equal to or less than  ${\bf Tc.}$ 

Table (1.2) Package Thickness/Volume and Classification Temperature (Tc)

	Package Thickness	Volume mm <sup>3</sup> <350	Volume mm <sup>3</sup> 350-2000	Volume mm <sup>3</sup> >2000
	<1.6mm	260°C	260°C	260°C
PB-Free Assembly	1.6-2.5mm	260°C	250°C	245°C
	≥2.5mm	250°C	245°C	245°C

Reflow is referred to standard IPC/JEDEC J-STD-020E  $\,^{\circ}$ 

#### 6-2.3 Solder Volume:

Accordingly increasing the solder volume, the mechanical stress to product is also increased. Exceeding solder volume may cause the failure of mechanical or electrical performance. Solder shall be used not to be exceed as shown in right side:

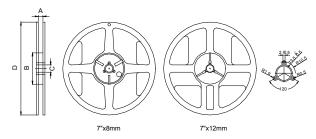
Minimum fillet height = soldering thickness + 25% product height

Upper limit Recommendable t

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# 7.Packaging Information

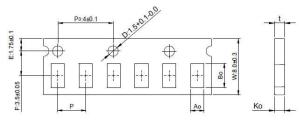
# 7-1. Reel Dimension



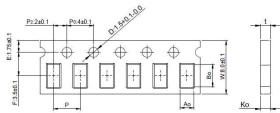
Туре	Type A(mm)		C(mm)	D(mm)	
<mark>7"x8mm</mark>	9.0±0.5	60±2	13.5±0.5	178±2	
7"x12mm	13.5±0.5	60±2	13.5±0.5	178±2	

#### 7-2.1 Tape Dimension / 8mm

## ■Material of taping is paper

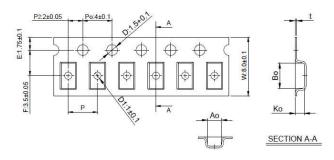


Size	Bo(mm)	Ao(mm)	Ko(mm)	P(mm)	t(mm)
100505	1.12±0.03	0.62±0.03	0.60±0.03	2.0±0.05	0.60±0.03



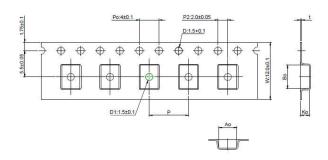
Size	Bo(mm)	Ao(mm)	Ko(mm)	P(mm)	t(mm)
160808	1.80±0.05	0.96+0.05/-0.03	0.95±0.05	4.0±0.10	0.95±0.05
<mark>201209</mark>	<mark>2.10±0.05</mark>	1.30±0.05	<mark>0.95±0.05</mark>	4.0±0.10	<mark>0.95±0.05</mark>

## ■Material of taping is plastic



Size	Bo(mm)	Ao(mm)	Ko(mm)	P(mm)	t(mm)	D1(mm)
201212	2.10±0.10	1.28±0.10	1.28±0.10	4.0±0.10	0.22±0.05	1.0±0.10
321611	3.35±0.10	1.75±0.10	1.25±0.10	4.0±0.10	0.23±0.05	1.0±0.10
322513	3.42±0.10	2.77±0.10	1.55±0.10	4.0±0.10	0.22±0.05	1.0±0.10
321609	3.40±0.10	1.77±0.10	1.04±0.10	4.0±0.10	0.22±0.05	1.0±0.10

# 7-2.2 Tape Dimension / 12mm



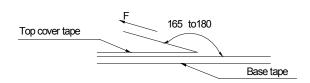
Size	Bo(mm)	Ao(mm)	Ko(mm)	P(mm)	t(mm)	D1(mm)
451616	4.70±0.10	1.75±0.10	1.75±0.10	4.0±0.10	0.24±0.05	1.5±0.10
453215	4.70±0.10	3.45±0.10	1.60±0.10	8.0±0.10	0.24±0.05	1.5±0.10

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## 7-3. Packaging Quantity

Chip Size	453215	451616	322513	321611	321609	201212	<mark>201209</mark>	160808	100505
Chip / Reel	1000	2000	2500	3000	3000	2000	<mark>4000</mark>	4000	10000
Inner box	4000	8000	12500	15000	15000	10000	20000	20000	50000
Middle box	20000	40000	62500	75000	75000	50000	100000	100000	250000
Carton	40000	80000	125000	150000	150000	100000	200000	200000	500000

## 7-4. Tearing Off Force



The force for tearing off cover tape is 15 to 60 grams in the arrow direction under the following conditions.

Room Temp.	Room Humidity	Room atm	Tearing Speed
(℃)	(%)	(hPa)	mm/min
5~35	45~85	860~1060	300

## **Application Notice**

Storage Conditions(component level)

To maintain the solder ability of terminal electrodes:

- 1. TAI-TECH products meet IPC/JEDEC J-STD-020E standard-MSL, level 1.
- 3. Recommended products should be used within 12 months from the time of delivery.
- 4. The packaging material should be kept where no chlorine or sulfur exists in the air.
- Transportation
  - 1. Products should be handled with care to avoid damage or contamination from perspiration and skin oils.
  - 2. The use of tweezers or vacuum pick up is strongly recommended for individual components.
  - 3. Bulk handling should ensure that abrasion and mechanical shock are minimized.