Winding Type Chip Inductor

SWF2012RF-4R7K-P

1. Features

- 1. Ferrite core wire wound construction.
- 2. High Reliability due to wire wound type construction.
- 3. Small footprint as well as low profile.
- 5. 100% Lead(Pb) & Halogen-Free and RoHS compliant.
- 6. Operating temperature -40~+125°C (Including self temperature rise)
- 7. These products provide low DC resistance and high current.
- 8. Precision inductance tolerance is available.
- 9. Application for DC power line.

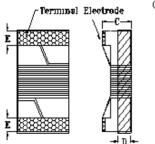
Digital camera and other electronic equipment

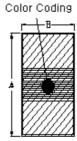
Personal computers, Hard disk drives

Mobile Device / Handheld Device / LowProfile Device / Panel

xDSL modem and Cable modem

2. Dimensions

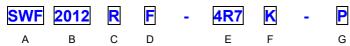




| Size | Α | В | С | D | E |
|---------|----------|----------|----------|----------|-----------|
| SWF2012 | 2.40 Max | 1.65 Max | 1.40 Max | 0.65ref. | 0.44 ref. |

Unit:mm

3. Part Numbering



A: Series

B: Dimension L x W

C: Control S/N

D: Lead free type

E: Inductance 4R7=4.7 uH
F: Inductance Tolerance K=±10%

G: Point

4. Specification

| TAI-TECH Part Number | Inductance (uH) | Tolerance | Test Frequency (Hz) | Q/MHz Typ. | SRF (MHz) Typ. | DCR (Ω) ±30%. | IDC (mA) Typ. | Irms (mA) Typ. | Color Coding |
|----------------------|--------------------|-----------|------------------------|---------------|-------------------|------------------|------------------|-------------------|--------------|
| SWF2012RF-4R7K-P | 4.7 | K | 0.5V/7.9M | 14/7.9 | 51 | 0.43 | 520 | 840 | Yellow |

Note:

Isat : Based on inductance change $\ \ (\,\triangle L/L0 : \leqq 10\%\,)$ @ ambient temp. 25%

Irms : Based on temperature rise $\ (\triangle T:25^{\circ}C.)\ Typ$

Measurement board data

Material : FR4

Board dimensions: 100 X 50 X 1.6t mm

Pattern dimensions: 45 X 30 mm (Double side board)

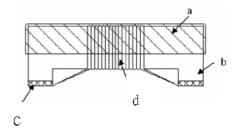
Pattern thickness $: 50 \ \mu m$





5. Materials

| No. | Description | Specification |
|-----|-------------|----------------------|
| a. | Upper Plate | UV Glue |
| b. | Core | Ferrite Core |
| С | Termination | Ag/Ni/Sn |
| d | Wire | Enameled Copper Wire |



6. Reliability and Test Condition

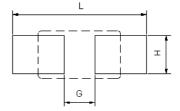
| Item | Performance | Test Condition |
|------------------------|--|--|
| Operating temperature | -40~+125℃ (Including self - temperature rise) | |
| Storage temperature | -40~+125℃ (on board) | |
| Electrical Performance | Test | |
| Inductance L | | Agilent-4291, Agilent-4287 , Agilent-E4991A |
| Q | | Agilent-4192, Agilent-4285 |
| SRF | Refer to standard electrical characteristic list | Agilent-4291 , Agilent-E4991A Agilent-4192 |
| DC Resistance | | Agilent-34420A |
| IDC | ∆L≤10% | Applied the current to coils, the inductance change shall be less than 10% to initial value. |
| Irms | ∆T≦25°C | Heat Rated Current (Irms) will cause the coil temperature rise △T(°C) without core loss. 1.Applied the allowed DC current. 2.Temperature measured by digital surface thermometer |
| Reliability Test | | |
| Life Test | | Preconditioning: Run through IR reflow for 3 times.(IPC/JEDEC J-STD-020E Classification Reflow Profiles) Temperature: 125±2°C Applied current: rated current Duration: 1000±12hrs Measured at room temperature after placing for 24±2 hrs |
| Load Humidity | | Preconditioning: Run through IR reflow for 3 times.(IPC/JEDEC J-STD-020E Classification Reflow Profiles Humidity: 85±2% R.H., Temperature: 85℃±2℃ Duration: 1000hrs Min. Bead: with 100% rated current Inductance: with 10% rated current Measured at room temperature after placing for 24±2 hrs |
| Moisture Resistance | Appearance: No damage. Inductance: within±10% of initial value Q: Shall not exceed the specification value. RDC: within ±15% of initial value and shall not exceed the specification value | Preconditioning: Run through IR reflow for 3 times.(IPC/JEDEC J-STD-020E Classification Reflow Profiles 1. Baked at50°C for 25hrs, measured at room temperature after placing for 4 hrs. 2. Raise temperature to 65±2°C 90-100%RH in 2.5hrs, and keep 3 hours, cool down to 25°C in 2.5hrs. 3. Raise temperature to 65±2°C 90-100%RH in 2.5hrs, and keep 3 hours, cool down to 25°C in 2.5hrs, keep 3 hours, cool down to 25°C in 2.5hrs, keep at 25°C for 2 hrs then keep at -10°C for 3 hrs 4. Keep at 25°C 80-100%RH for 15min and vibrate at the frequency of 10 to 55 Hz to 10 Hz, measure at room temperature after placing for 1-2 hrs. |
| Thermal shock | | Preconditioning: Run through IR reflow for 3 times.(IPC/JEDEC J-STD-020E Classification Reflow Profiles Condition for 1 cycle Step1: -40±2°C 30±5min Step2: 25±2°C ≤0.5min Step3: 125±2°C 30±5min Number of cycles: 500 Measured at room temperature after placing for 24±2 hrs |
| Vibration | | Oscillation Frequency: 10Hz~2KHz~10Hz for 20 minute Equipment: Vibration checker Total Amplitude:10g Testing Time: 12 hours(20 minutes, 12 cycles each of 3 orientations) |

| Item | Performance | Test Condition | | | | |
|------------------------------|--|---|--|--|--|--|
| Bending | Appearance : No damage. Inductance : within±10% of initial value | Shall be mounted on a FR4 substrate of the following dimensions: >=0805 inch(2012mm):40x100x1.2mm <0805 inch(2012mm):40x100x0.8mm Bending depth: >=0805 inch(2012mm):1.2mm <0805 inch(2012mm):0.8mm duration of 10 sec. | | | | |
| | Q : Shall not exceed the specification value. RDC : within ±15% of initial value and shall not exceed the specification value | Type Value duration (D) Wave form (Vi)ft/sec | | | | |
| Shock | | SMD 50 11 Half-sine 11.3 | | | | |
| | | Lead 50 11 Half-sine 11.3 | | | | |
| Solder ability | More than 95% of the terminal electrode should be covered with solder。 | a. Method B, 4 hrs @155°C dry heat @235°C±5°C Testing Time :5 +0/-0.5 seconds b. Method D category 3. (8hours ± 15 min)@ 260°C±5°C Testing Time :30 +0/-0.5 seconds | | | | |
| Resistance to Soldering Heat | | Depth: completely cover the termination Temperature (°C) Time(s) Temperature ramp/immersion and emersion rate heat cycles 260 ±5 (solder temp) 10 ±1 25mm/s ±6 mm/s 1 | | | | |
| Terminal Strength | Appearance: No damage. Inductance: within±10% of initial value Q: Shall not exceed the specification value. RDC: within ±15% of initial value and shall not exceed the specification value e | Preconditioning: Run through IR reflow for 3 times.(IPC/JEDEC J-STD-020E Classification Reflow Profiles With the component mounted on a PCB with the device to be tested, apply a force(>0805:18g , <=0805:0.5kg)to the side of a device being tested. This force shall be applied for 60 +1 seconds. Also the force shall be applied gradually as not to apply a shock to the component being tested. | | | | |

7. Soldering and Mounting

7-1. Recommended PC Board Pattern

| Chip size | | | | | | | | l Pattern ow Sold | |
|-----------|------|---------|---------|---------|-----------|---------|-------|----------------------|-------|
| Series | Туре | A(mm) | B(mm) | C(mm) | D(mm) | E(mm) | L(mm) | G(mm) | H(mm) |
| SWF | 2012 | 2.40Max | 1.60Max | 1.40Max | 0.65 ref. | 0.44ref | 3.00 | 0.96 | 1.78 |



7-2. Soldering

Mildly activated rosin fluxes are preferred. TAI-TECH terminations are suitable for re-flow soldering systems. If hand soldering cannot be avoided, the preferred technique is the utilization of hot air soldering tools.

7-2.1 IR Soldering Reflow:

Recommended temperature profiles for lead free re-flow soldering in Figure 1. Table 1.1&1.2 (J-STD-020E)

7-2.2 Soldering Iron:

Products attachment with a soldering iron is discouraged due to the inherent process control limitations. In the event that a soldering iron must be employed the following precautions are recommended. (Figure 2.)

- Never contact the ceramic with the iron tip
- Use a 20 watt soldering iron with tip diameter of 1.0mm

- 350°C tip temperature (max)
- 1.0mm tip diameter (max)
- Limit soldering time to 4~5sec.

Fig.1 IR Soldering Reflow

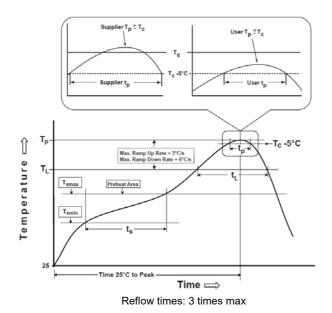
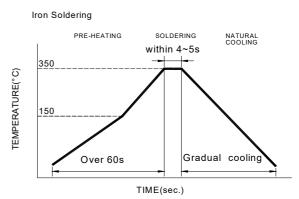


Fig.2 Iron soldering temperature profiles



Iron Soldering times: 1 times max

Table (1.1): Reflow Profiles

| Profile Type: | Pb-Free Assembly |
|---|---------------------------------|
| Preheat -Temperature Min(T _{smin}) -Temperature Max(T _{smax}) -Time(t _s)from(T _{smin} to T _{smax}) | 150°C 200°C 60-120seconds |
| Ramp-up rate(T_L to T_p) | 3°C/second max. |
| $\label{eq:Liquidus} \begin{array}{c} \text{Liquidus temperature}(T_L) \\ \text{Time}(t_L) \\ \text{maintained above } T_L \end{array}$ | 217°C 60-150 seconds |
| Classification temperature(T _c) | See Table (1.2) |
| $\label{eq:total_final} \mbox{Time}(t_p) \mbox{ at Tc-} 5^{\circ}\mbox{\mathbb{C}} (\mbox{Tp should be equal to or less than Tc.})$ | < 30 seconds |
| Ramp-down rate(T _p to T _L) | 6°C /second max. |
| Time 25°C to peak temperature | 8 minutes max. |

Tp: maximum peak package body temperature, Tc: the classification temperature.

For user (customer) Tp should be equal to or less than Tc.

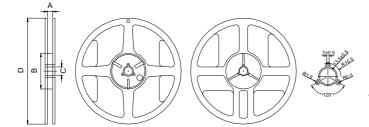
Table (1.2) Package Thickness/Volume and Classification Temperature (T_c)

| | Package | Volume mm ³ | Volume mm ³ | Volume mm ³ |
|------------------|-----------|------------------------|------------------------|------------------------|
| | Thickness | <350 | 350-2000 | >2000 |
| | <1.6mm | 260°C | 260°C | 260°C |
| PB-Free Assembly | 1.6-2.5mm | 260°C | 250°C | 245°C |
| | ≥2.5mm | 250°C | 245°C | 245°C |

Reflow is referred to standard IPC/JEDEC J-STD-020E ∘

8. Packaging Information

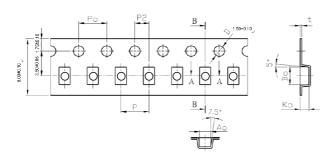
8-1. Reel Dimension



| Туре | A(mm) | B(mm) | C(mm) | D(mm) |
|--------|---------|-------|----------|-------|
| 7"x8mm | 9.0±0.5 | 60±2 | 13.5±0.5 | 178±2 |

8-2. Tape Dimension / 8mm

7"x8mm



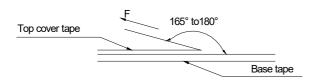
| Series | P(mm) | Po(mm) | P2(mm) | Bo(mm) | Ao(mm) | Ko(mm) | W(mm) | t(mm) |
|---------|-----------|-----------|-----------|-----------|-----------|-----------|-----------|-----------|
| SWF2012 | 4.00±0.10 | 4.00±0.10 | 2.00±0.05 | 2.50±0.10 | 1.60±0.10 | 1.55±0.10 | 8.00±0.10 | 0.22±0.05 |

7"x12mm

8-3. Packaging Quantity

| SWF | 2012 |
|-------------|--------|
| Chip / Reel | 2000 |
| Reel Size | 7"x8mm |

8-4. Tearing Off Force



The force for tearing off cover tape is 15 to 80 grams in the arrow direction under the following conditions.

| Room Temp. | Room Humidity | Room atm | Tearing Speed |
|------------|---------------|----------|---------------|
| (℃) | (%) | (hPa) | mm/min |
| 5~35 | 45~85 | 860~1060 | 300 |

Application Notice

• Storage Conditions(component level)

To maintain the solderability of terminal electrodes:

- 1.TAI-TECH products meet IPC/JEDEC J-STD-020E standard-MSL, level 1.
- 3. Recommended products should be used within 12 months form the time of delivery.
- 4. The packaging material should be kept where no chlorine or sulfur exists in the air.
- Transportation
- 1. Products should be handled with care to avoid damage or contamination from perspiration and skin oils.
- $2. \ The \ use \ of \ tweezers \ or \ vacuum \ pick \ up \ is \ strongly \ recommended \ for \ individual \ components.$
- 3. Bulk handling should ensure that abrasion and mechanical shock are minimized.