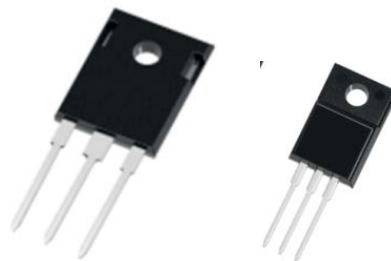


650V 20A Power MOSFET

Description

XCH Semiconductor(XCH)has series Multi-EPI Super-Junction power MOSFET platforms for voltage up 500V to 1000 volts, bothwith design service and manufacturing capability, including cell,termination design and simulation.

The GSx20N65E is a Low voltage N channel Multi-EPI Super-Junction power MOSFET sample with advanced technology to have better characteristics, such as fast switchingtime. low Ciss and Crss. low on resistance and excell entavalanche characteristics.



TO-247

TO-220F

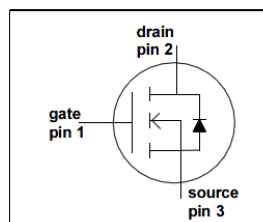
Features

$R_{DS(ON)} = 0.19 \Omega$ @ $V_{GS} = 10V$

$V_{DS} = 650V$

PKG

GSA20N65E	GSW20N65E
TO-220F	TO-247



Absolute Maximum Ratings (TC = 25°C, unless otherwise specified)

Symbol	Parameter	GSW20N65E	GSA20N65E	Unit
V_{DS}	Drain-Source Voltage	650		V
I_D	Drain Current -Continuous (TC = 25°C) -Continuous (TC = 100°C)	20* 12*		A
I_{DM}	Drain Current - Pulsed (Note 1)	45		A
V_{GSS}	Gate-Source voltage	± 30		V
E_{AS}	Single Pulsed Avalanche Energy (Note 2)	485		mJ
I_{AR}	Avalanche Current (Note 1)	3.5		A
E_{AR}	Repetitive Avalanche Energy (Note 1)	1		mJ
dv/dt	Peak Diode Recovery dv/dt (Note 3)	15		V/ns
dV_{DS}/dt	Drain Source voltage slope ($V_{DS}=480V$)	50		V/ns
P_D	Power Dissipation (TC = 25°C)	210	35	W
T_J, T_{STG}	Operating and Storage Temperature Range	-55 to +150		°C
T_L	Max. Lead Temperature for Soldering Purpose, 1/8" from Case for 5 Seconds	300		°C

650V 20A Power MOSFET

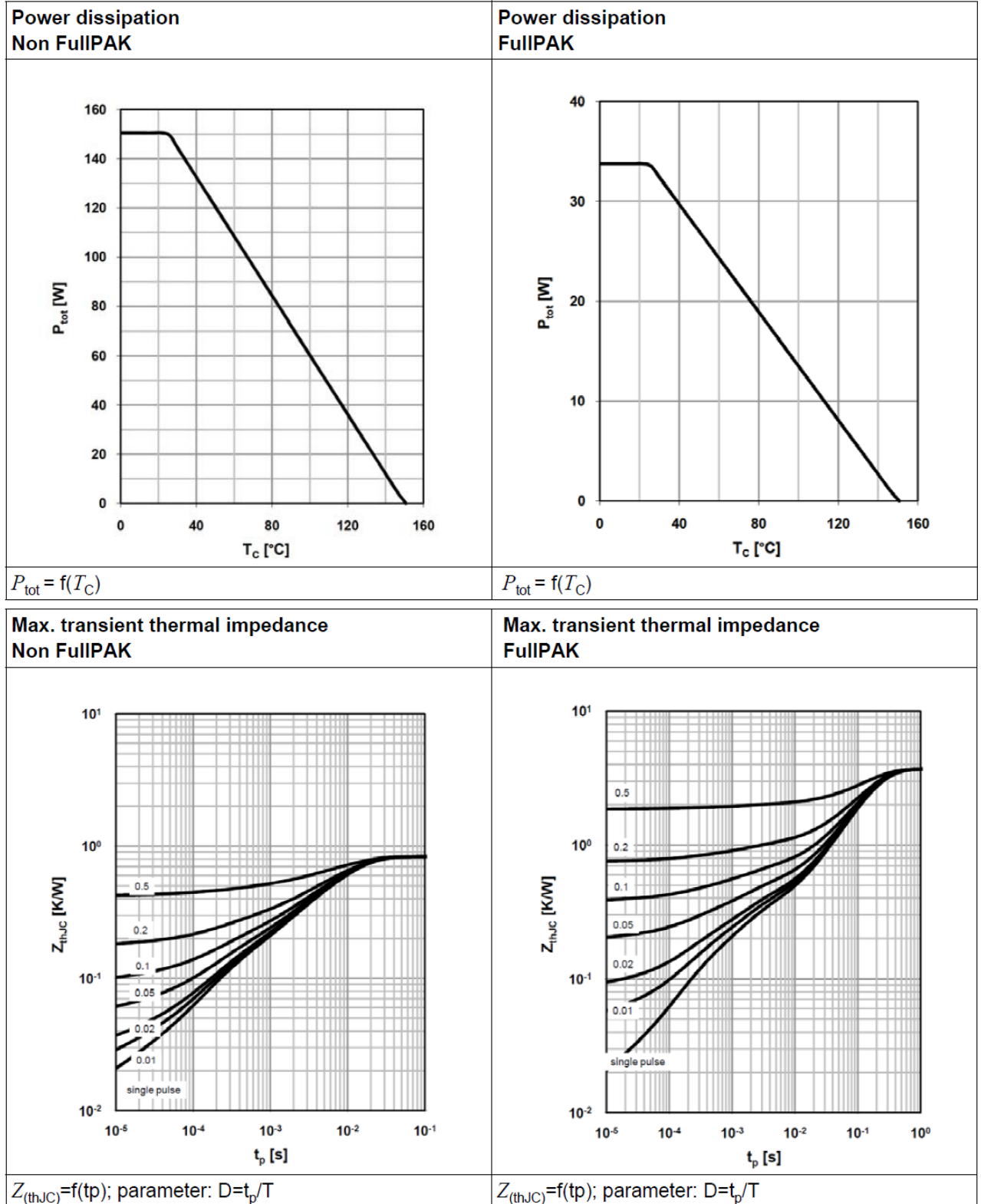
■ Electrical Characteristics (T_J=25 °C unless otherwise specified)

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
Off Characteristics						
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} = 0V, I _D = 250μA, T _J = 25°C	650	--	--	V
		V _{GS} = 0V, I _D = 250μA, T _J = 150°C	--	700	--	V
ΔBV _{DSS} /ΔT _J	Breakdown Voltage Temperature Coefficient	I _D = 250μA, Referenced to 25°C	--	0.6	--	V/°C
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} = 500V, V _{GS} = 650V -T _j =25 °C -T _J = 150°C	--	-- 10	1 -	μA μA
I _{GSSF}	Gate-Body Leakage Current, Forward	V _{GS} = 30V, V _{DS} = 0V	--	--	100	nA
I _{GSSR}	Gate-Body Leakage Current, Reverse	V _{GS} = -30V, V _{DS} = 0V	--	--	-100	nA
On Characteristics						
V _{GS(th)}	Gate Threshold Voltage	V _{DS} = V _{GS} , I _D = 250μA	2	--	4	V
R _{DS(on)}	Static Drain-Source On-Resistance	V _{GS} = 10V, I _D = 10A	--	0.17	0.19	Ω
g _{FS}	Forward Transconductance	V _{DS} = 40V, I _D = 10A	--	16	--	S
Dynamic Characteristics						
C _{iss}	Input Capacitance	V _{DS} = 25V, V _{GS} = 0V, f = 1.0MHz	--	1510	-	pF
C _{oss}	Output Capacitance		--	75	-	pF
C _{rss}	Reverse Transfer Capacitance		--	6	--	pF
Switching Characteristics						
t _{d(on)}	Turn-On Delay Time	V _{DD} = 520Ω (Note 4)	--	25	--	ns
t _r	Turn-On Rise Time		--	17	--	ns
t _{d(off)}	Turn-Off Delay Time		--	130	--	ns
t _f	Turn-Off Fall Time		--	11	--	ns
Q _g	Total Gate Charge	V _{DS} = 520V, I _D = 10A	--	90	120	nC
Q _{gs}	Gate-Source Charge	V _{GS} = 10V (Note 4)	--	8.5	--	nC
Q _{gd}	Gate-Drain Charge		--	13	--	nC
Drain-Source Diode Characteristics and Maximum Ratings						
I _S	Maximum Continuous Drain-Source Diode Forward Current		--	--	20	A
I _{SM}	Maximum Pulsed Drain-Source Diode Forward Current		--	--	60	A
V _{SD}	Drain-Source Diode Forward Voltage	V _{GS} = 0V, I _S = 10A	--	0.9	1.5	V
t _{rr}	Reverse Recovery Time	V _{GS} = 0V, I _S = 10A	--	475	--	ns
Q _{rr}	Reverse Recovery Charge	dI _F /dt =100A/μs	--	5.8	--	μC

NOTES:

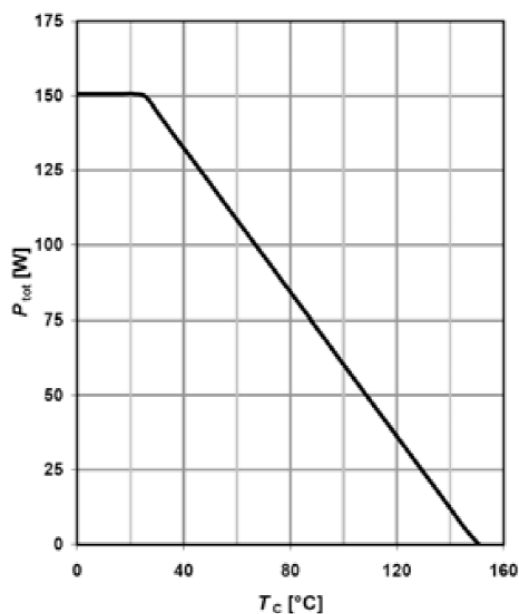
1. Repetitive Rating: Pulse width limited by maximum junction temperature
2. L=60mH, I_{AS}=3A, V_{DD}=150V, Starting T_J=25 °C
3. I_{SD}≤4.5A, di/dt ≤ 200A/μs, V_{DD} ≤ BV_{DSS}, Starting T_J = 25 °C
4. Pulse Test: Pulse width ≤ 300μs, Duty Cycle ≤ 2%
5. Essentially Independent of Operating Temperature Typical Characteristics

Typical Performance Characteristics



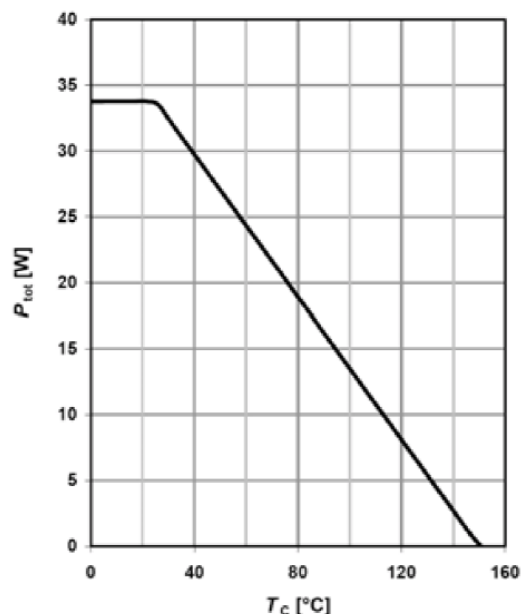
Typical Performance Characteristics

Power dissipation
TO-220, TO-247, TO-262, TO-263



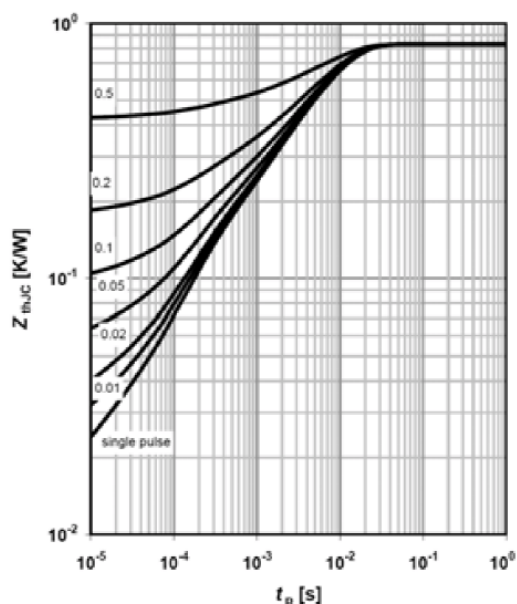
$$P_{tot} = f(T_c)$$

Power dissipation
TO-220 FullPAK



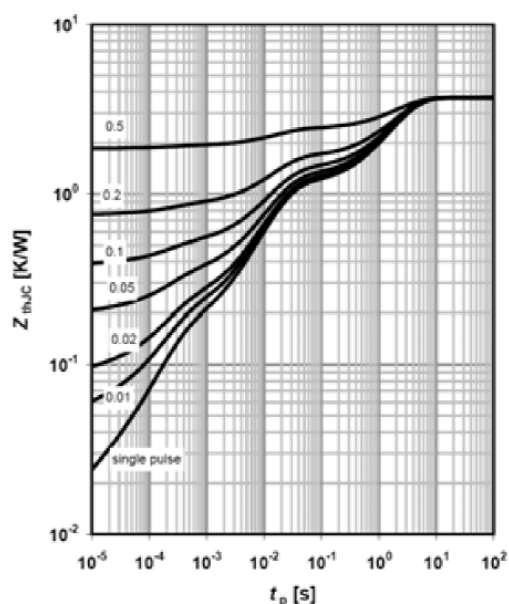
$$P_{tot} = f(T_c)$$

Max. transient thermal impedance
TO-220, TO-247, TO-262, TO-263



$$Z_{(thJC)} = f(t_p); \text{ parameter: } D = t_p / T$$

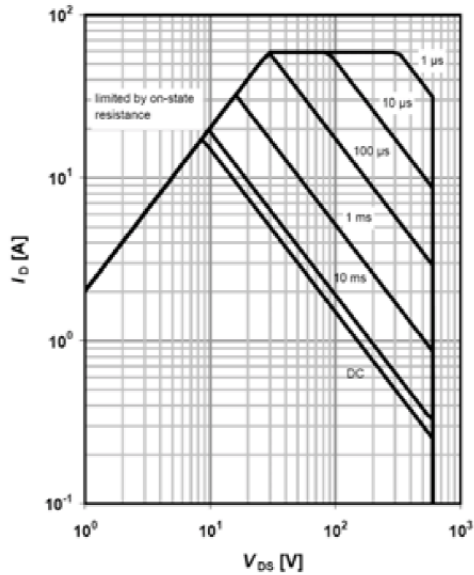
Max. transient thermal impedance
TO-220 FullPAK



$$Z_{(thJC)} = f(t_p); \text{ parameter: } D = t_p / T$$

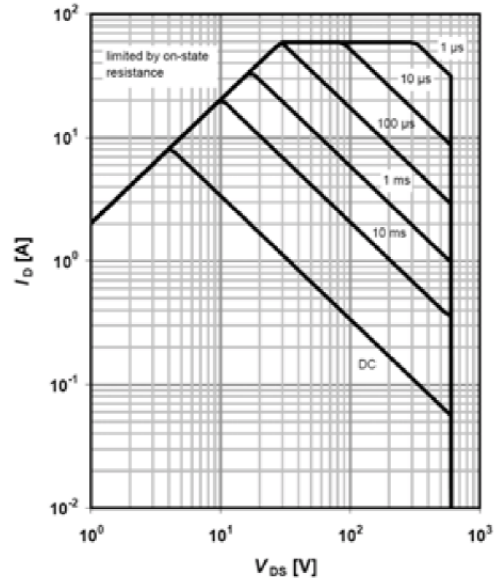
Typical Performance Characteristics

Safe operating area $T_C=25\text{ }^{\circ}\text{C}$
TO-220, TO-247, TO-262, TO-263



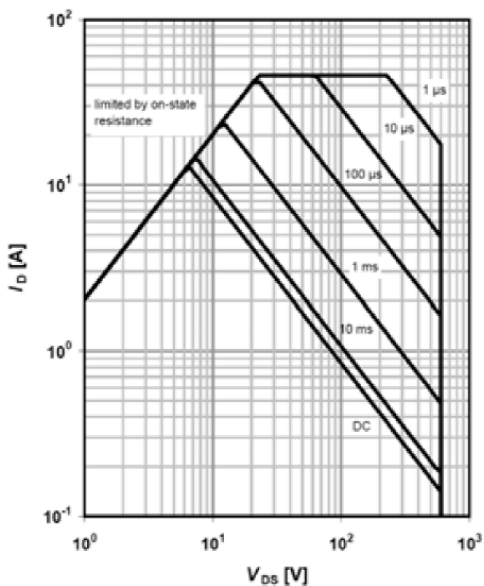
$I_D=f(V_{DS}); T_C=25\text{ }^{\circ}\text{C}; D=0; \text{parameter } t_p$

Safe operating area $T_C=25\text{ }^{\circ}\text{C}$
TO-220 FullPAK



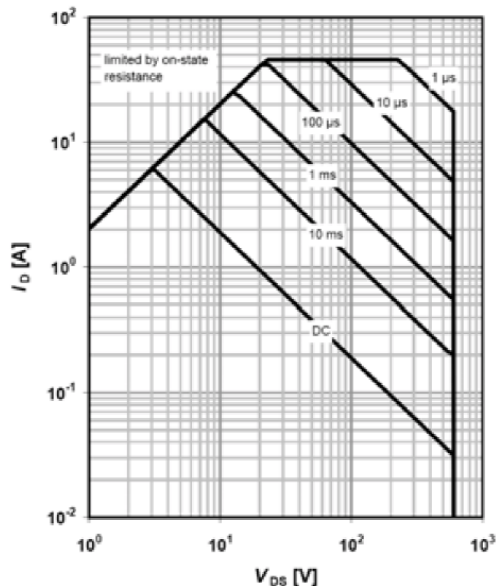
$I_D=f(V_{DS}); T_C=25\text{ }^{\circ}\text{C}; D=0; \text{parameter } t_p$

Safe operating area $T_C=80\text{ }^{\circ}\text{C}$
TO-220, TO-247, TO-262, TO-263



$I_D=f(V_{DS}); T_C=80\text{ }^{\circ}\text{C}; D=0; \text{parameter } t_p$

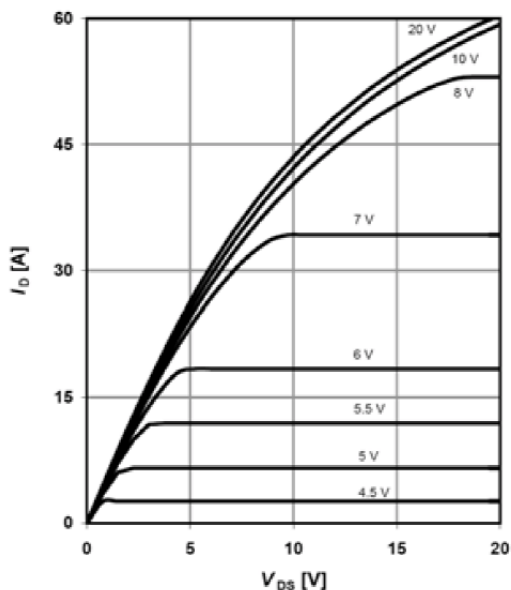
Safe operating area $T_C=80\text{ }^{\circ}\text{C}$
TO-220 FullPAK



$I_D=f(V_{DS}); T_C=80\text{ }^{\circ}\text{C}; D=0; \text{parameter } t_p$

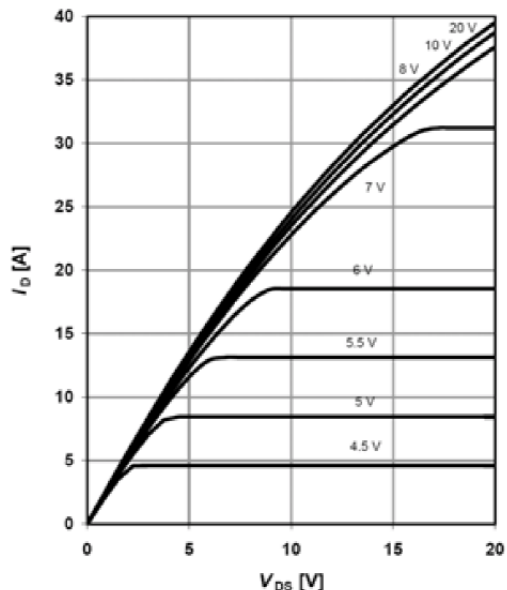
Typical Performance Characteristics

Typ. output characteristics $T_c=25^\circ\text{C}$



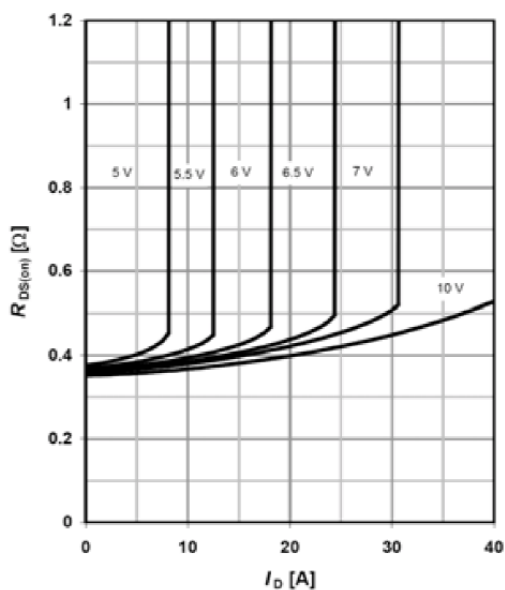
$$I_D = f(V_{DS}); T_J = 25^\circ\text{C}; \text{parameter: } V_{GS}$$

Typ. output characteristics $T_J=125^\circ\text{C}$



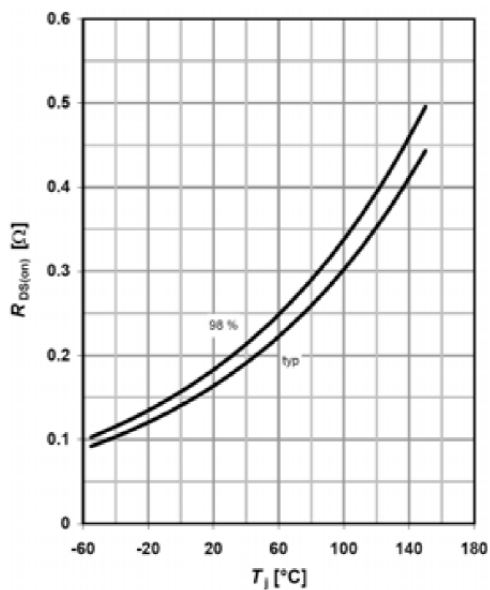
$$I_D = f(V_{DS}); T_J = 125^\circ\text{C}; \text{parameter: } V_{GS}$$

Typ. drain-source on-state resistance



$$R_{DS(on)} = f(I_D); T_J = 125^\circ\text{C}; \text{parameter: } V_{GS}$$

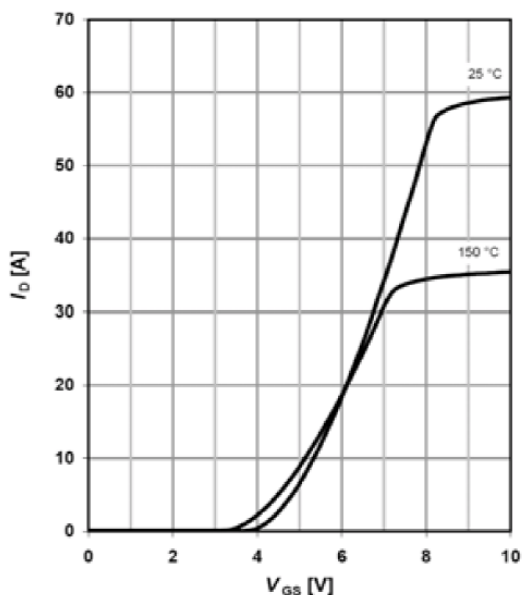
Drain-source on-state resistance



$$R_{DS(on)} = f(T_J); I_D = 9.5\text{ A}; V_{GS} = 10\text{ V}$$

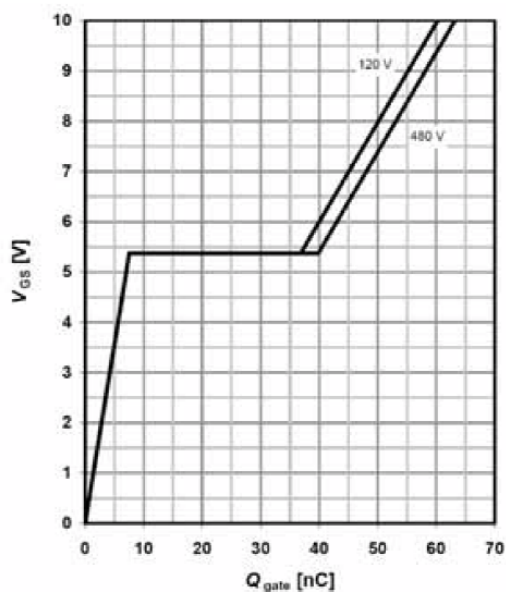
Typical Performance Characteristics

Typ. transfer characteristics



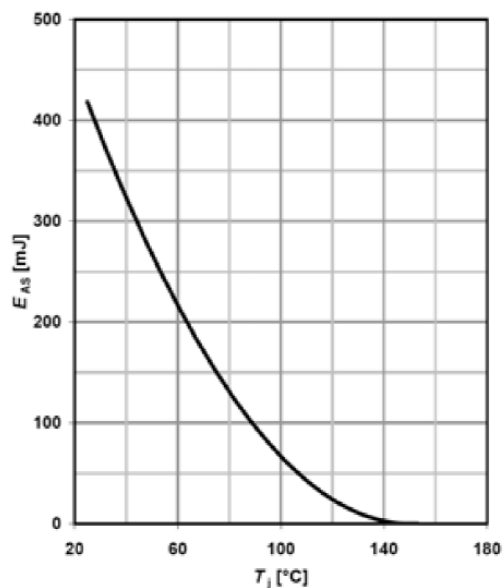
$$I_D = f(V_{GS}); V_{DS} = 20V$$

Typ. gate charge



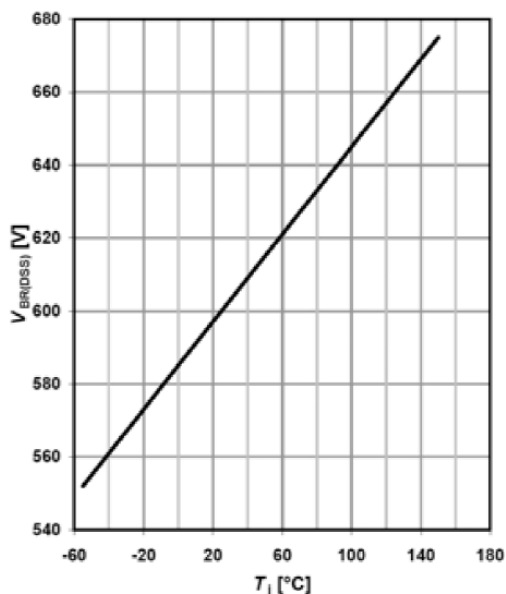
$$V_{GS} = f(Q_{gate}), I_D = 9.5A \text{ pulsed}$$

Avalanche energy



$$E_{AS} = f(T_J); I_D = 3.4A; V_{DD} = 50V$$

Drain-source breakdown voltage



$$V_{BR(DSS)} = f(T_J); I_D = 0.25mA$$

Table 20 Switching times test circuit and waveform for inductive load

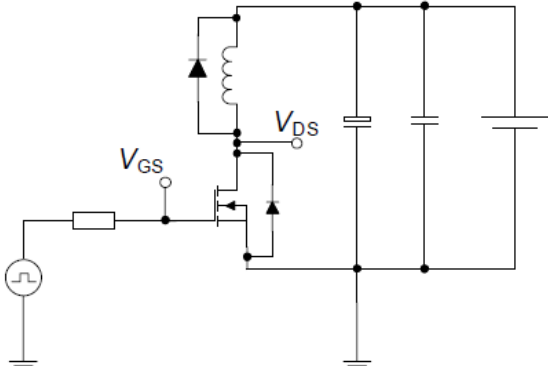
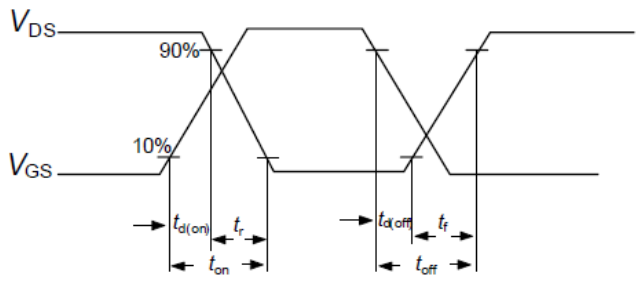
Switching times test circuit for inductive load	Switching time waveform
	

Table 21 Unclamped inductive load test circuit and waveform

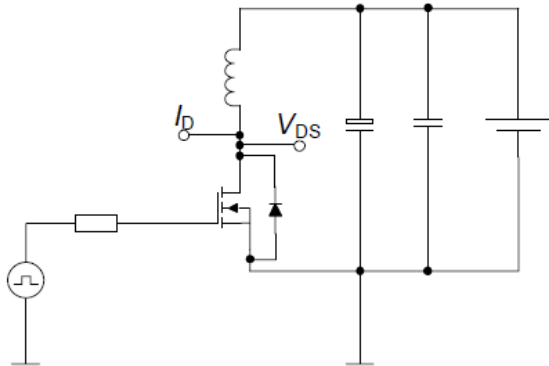
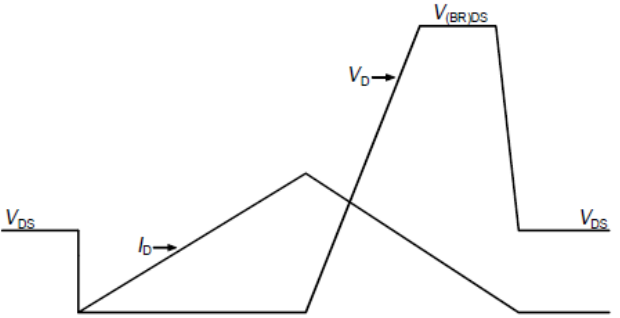
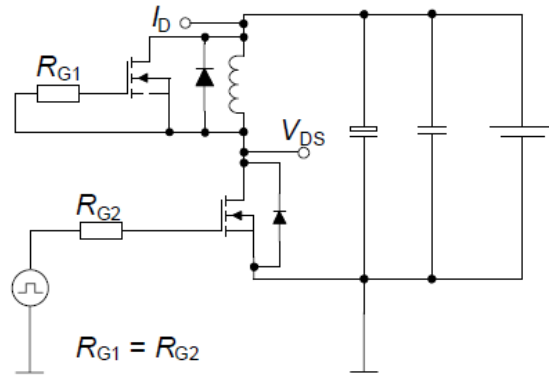
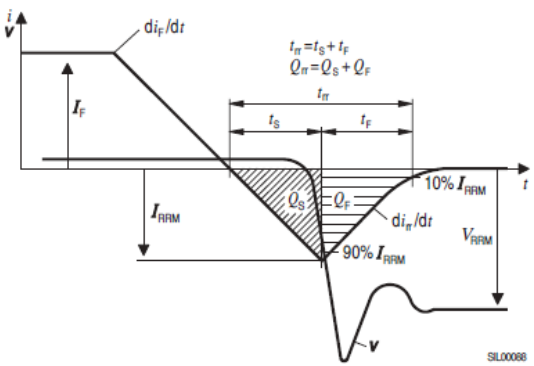
Unclamped inductive load test circuit	Unclamped inductive waveform
	

Table 22 Test circuit and waveform for diode characteristics

Test circuit for diode characteristics	Diode recovery waveform
 $R_{G1} = R_{G2}$	 $Q_n = Q_s + Q_f$ SI00008