

General Description

These P-Channel enhancement mode power field effect transistors use advanced trench technology and design to provide excellent RDS(ON) . This device is suitable for use as a load switch or in PWM applications.

Features

- Fast switching speed
- Lower On-resistance
- 100% EAS Guaranteed
- Simple Drive Requirement

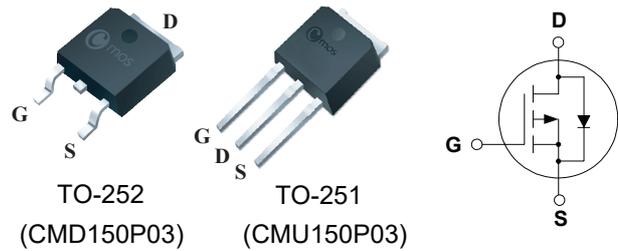
Product Summary

BVDSS	RDSON	ID
-30V	6.5mΩ	-120A

Applications

- DC-DC Converters
- LCD Display inverter
- Power Management in Note book

TO-252/251 Pin Configuration



Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	-30	V
V_{GS}	Gate-Source Voltage	±20	V
$I_D@T_C=25^\circ C$	Continuous Drain Current	-120	A
I_{DM}	Pulsed Drain Current	-360	A
EAS	Single Pulse Avalanche Energy ¹	506	mJ
$P_D@T_C=25^\circ C$	Total Power Dissipation	130	W
T_{STG}	Storage Temperature Range	-55 to 175	°C
T_J	Operating Junction Temperature Range	-55 to 175	°C

Thermal Data

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Junction-to-Ambient	---	62	°C/W
$R_{\theta JC}$	Junction-to-Case (Drain)	---	1.1	°C/W

Electrical Characteristics ($T_J=25^\circ\text{C}$, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{GS}=0V, I_D=-250\mu A$	-30	---	---	V
$R_{DS(ON)}$	Static Drain-Source On-Resistance	$V_{GS}=-10V, I_D=-28A$	---	5	6.5	m Ω
		$V_{GS}=-4.5V, I_D=-20A$	---	6	8	
$V_{GS(th)}$	Gate Threshold Voltage	$V_{GS}=V_{DS}, I_D=-250\mu A$	-1	---	-2.5	V
I_{DSS}	Drain-Source Leakage Current	$V_{DS}=-24V, V_{GS}=0V, T_J=25^\circ\text{C}$	---	---	-1	μA
I_{GSS}	Gate-Source Leakage Current	$V_{GS} = \pm 20V, V_{DS}=0V$	---	---	± 100	nA
g_{fs}	Forward Transconductance	$V_{DS}=-5V, I_D=-20A$	---	48	---	S
R_g	Gate Resistance	$V_{DS}=0V, V_{GS}=0V, f=1\text{MHz}$	---	17	---	Ω
Q_g	Total Gate Charge	$V_{DS}=-24V, I_D=-80A$ $V_{GS}=0$ to $-10V$	---	130	---	nC
Q_{gs}	Gate-Source Charge		---	20	---	
Q_{gd}	Gate-Drain Charge		---	50	---	
$T_{d(on)}$	Turn-On Delay Time	$V_{DD}=-15V, V_{GS}=-10V, R_G=6\Omega$ $I_D=-50A$	---	30	---	ns
T_r	Rise Time		---	45	---	
$T_{d(off)}$	Turn-Off Delay Time		---	200	---	
T_f	Fall Time		---	180	---	
C_{iss}	Input Capacitance	$V_{DS}=-25V, V_{GS}=0V, f=1\text{MHz}$	---	8200	---	pF
C_{oss}	Output Capacitance		---	3000	---	
C_{rss}	Reverse Transfer Capacitance		---	1100	---	

Diode Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I_S	Continuous Source Current	$V_G=V_D=0V$, Force Current	---	---	-120	A
I_{SM}	Pulsed Source Current		---	---	-360	A
V_{SD}	Diode Forward Voltage	$V_{GS}=0V, I_F=-20A$	---	---	-1.2	V

Notes:

1. The EAS data shows Max. rating . The test condition is $V_{DD}=-20V, V_{GS}=-10V, L=0.5\text{mH}, I_{AS}=-45A$

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Cmos reserves the right to improve product design ,functions and reliability without notice.