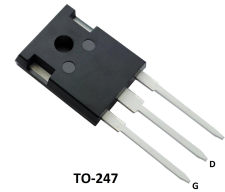


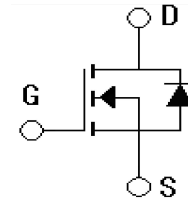
### Features

- $V_{DS}=200V, I_D=200A$   
 $R_{DS(on)}=10m\Omega$
- High density cell design for ultra low  $R_{DS(on)}$
- 100% avalanche tested
- Low gate charge



### Applications

- High Efficiency Synchronous Rectification in SMPS
- Uninterruptible Power Supply
- High Speed Power Switching
- Hard Switched And High Frequency Circuits



### Absolute Ratings ( $T_c=25^\circ C$ )

| Parameter                      | Symbol                                                 | Limit           | Unit       |
|--------------------------------|--------------------------------------------------------|-----------------|------------|
| Drain-Source Voltage           | $V_{DSS}$                                              | 200             | V          |
| Gate-Source Voltage            | $V_{GSS}$                                              | $\pm 30$        | V          |
| Drain Current-continuous       | $I_D$                                                  | 200             | A          |
| Drain Current-pulse            | $I_{DM}$                                               | 800             | A          |
| Single Pulsed Avalanche Energy | $E_{AS}$                                               | 750             | mJ         |
| Maximum Power Dissipation      | PD<br>$T_C=25^\circ C$<br>Derate above<br>$25^\circ C$ | 350             | W          |
| Operating Temperature Range    | $T_J$                                                  | $-55 \sim +175$ | $^\circ C$ |
| Storage Temperature Range      | $T_{STG}$                                              | $-55 \sim +150$ | $^\circ C$ |

### Electrical Characteristics( $T_{CASE}=25^\circ C$ unless otherwise specified)

| Parameter                       | Symbol     | Tests conditions            | Min | Typ | Max       | Units   |
|---------------------------------|------------|-----------------------------|-----|-----|-----------|---------|
| Drain-Source Voltage            | $BV_{DSS}$ | $I_D=250\mu A, V_{GS}=0V$   | 200 | -   | -         | V       |
| Zero Gate Voltage Drain Current | $I_{DSS}$  | $V_{DS}=V_{DSS}, V_{GS}=0V$ | -   | -   | 25        | $\mu A$ |
| Gate-Body Leakage Current       | $I_{GSS}$  | $V_{GS}=\pm 30V, V_{DS}=0V$ | -   | -   | $\pm 100$ | nA      |

| On-Characteristics                |              |                                   |     |      |   |            |
|-----------------------------------|--------------|-----------------------------------|-----|------|---|------------|
| Gate Threshold Voltage            | $V_{GS(th)}$ | $V_{DS}=V_{GS}, I_D=250\mu A$     | 2.5 | -    | 5 | V          |
| Static Drain-Source On-Resistance | $R_{DS(ON)}$ | $V_{GS}=10V, I_D=100A$            | -   | 10   | - | m $\Omega$ |
| Forward Transconductance          | $g_{fs}$     | $V_{DS}=50V, I_D=100A(1)$         | 150 | -    | - | S          |
| Gate Input Resistance             | $R_g$        |                                   | -   | 1    | - | $\Omega$   |
| Dynamic Characteristics           |              |                                   |     |      |   |            |
| Input capacitance                 | $C_{iss}$    | $V_{DS}=25V, V_{GS}=0V, f=1.0MHz$ | -   | 10.5 | - | nF         |
| Output capacitance                | $C_{oss}$    |                                   | -   | 760  | - | pF         |
| Reverse transfer capacitance      | $C_{rss}$    |                                   | -   | 150  | - | pF         |

**Electrical Characteristics**( $T_{CASE}=25^{\circ}C$  unless otherwise specified)

| Parameter                                              | Symbol              | Tests conditions                                                                         | Min | Typ | Max | Units |
|--------------------------------------------------------|---------------------|------------------------------------------------------------------------------------------|-----|-----|-----|-------|
| Switching-Characteristics                              |                     |                                                                                          |     |     |     |       |
| Turn-On delay time                                     | t <sub>d(on)</sub>  | V <sub>DS</sub> =100V,I <sub>D</sub> =100A,<br>V <sub>GS</sub> =10V,R <sub>g</sub> =2.7Ω | -   | 41  | -   | ns    |
| Turn-On rise time                                      | t <sub>r</sub>      |                                                                                          | -   | 105 | -   | ns    |
| Turn-Off delay time                                    | t <sub>d(Off)</sub> |                                                                                          | -   | 64  | -   | ns    |
| Turn-Off rise time                                     | t <sub>f</sub>      |                                                                                          | -   | 74  | -   | ns    |
| Total Gate Charge                                      | Q <sub>g</sub>      | V <sub>DS</sub> =100V,I <sub>D</sub> =100A,<br>V <sub>GS</sub> =10V                      | -   | 150 | -   | nC    |
| Gate-Source charge                                     | Q <sub>gs</sub>     |                                                                                          | -   | 52  | -   | nC    |
| Gate-Drain charge                                      | Q <sub>gd</sub>     |                                                                                          | -   | 51  | -   | nC    |
| Drain-Source Diode Characteristics and Maximum Ratings |                     |                                                                                          |     |     |     |       |
| Maximum Continuous Drain-Source Diode Forward Curret   | V <sub>SD</sub>     | V <sub>GS</sub> =0V,I <sub>S</sub> =100A(1)                                              | -   | -   | 1.2 | V     |
| Diode Forward Current                                  | I <sub>S</sub>      | TC=25℃                                                                                   | -   | -   | 200 | A     |
| Reverse recovery time                                  | T <sub>rr</sub>     | I <sub>S</sub> =100A,<br>dI/dT=100A/μS<br>VR=100V,VGS=0V                                 | -   | 125 | -   | nS    |
| Reverse recovery charge                                | Q <sub>rr</sub>     |                                                                                          | -   | 620 | -   | μC    |
| Reverse recovery Current                               | I <sub>rm</sub>     |                                                                                          | -   | 8.7 | -   | A     |

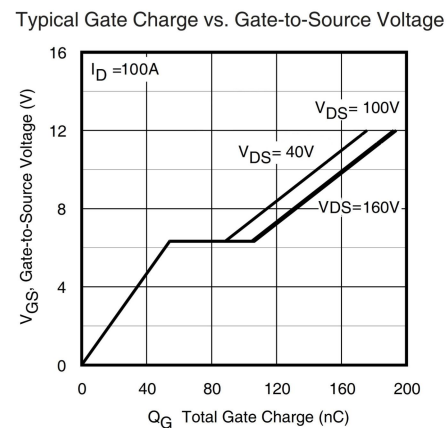
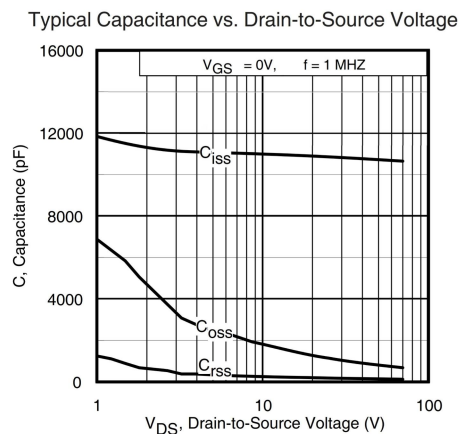
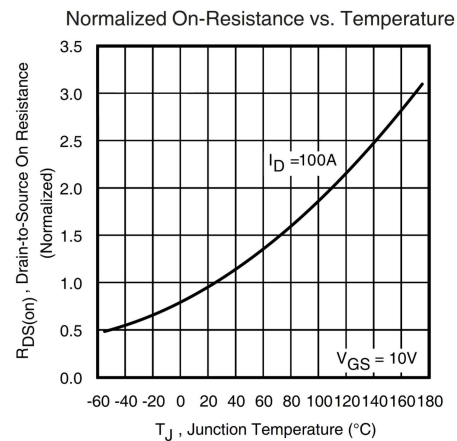
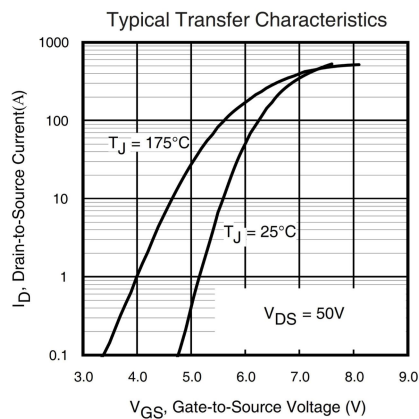
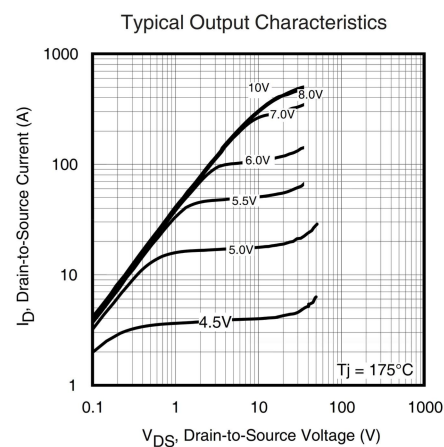
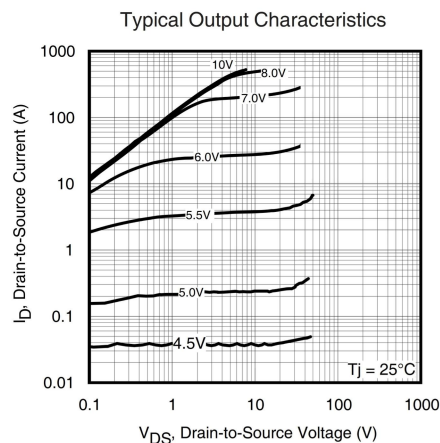
### Thermal Characteristic

| Parameter                              | Symbol        | Value | Unit                        |
|----------------------------------------|---------------|-------|-----------------------------|
| Thermal Resistance,junction to Case    | $R_{th(j-C)}$ | 0.43  | $^{\circ}\text{C}/\text{W}$ |
| Thermal Resistance,junction to Ambient | $R_{th(j-A)}$ | 40    | $^{\circ}\text{C}/\text{W}$ |

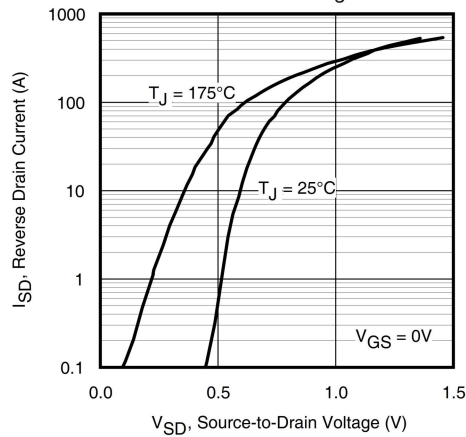
Notes:

1. Pulse Test: Pulse Width  $\leq 300\mu\text{s}$ , Duty Cycle  $\leq 2\%$

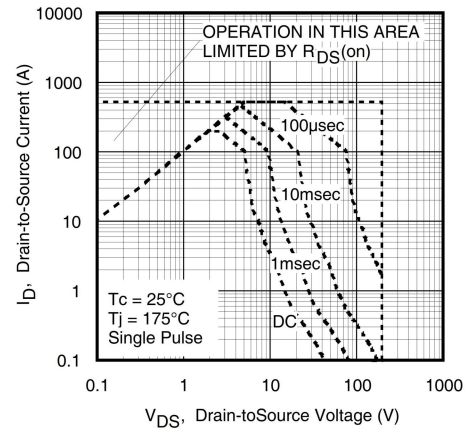
### Electrical Characteristics



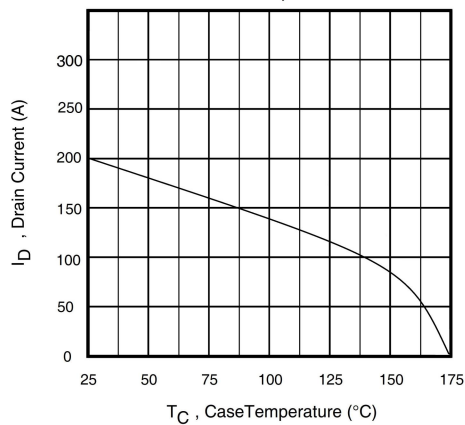
Typical Source-Drain Diode Forward Voltage



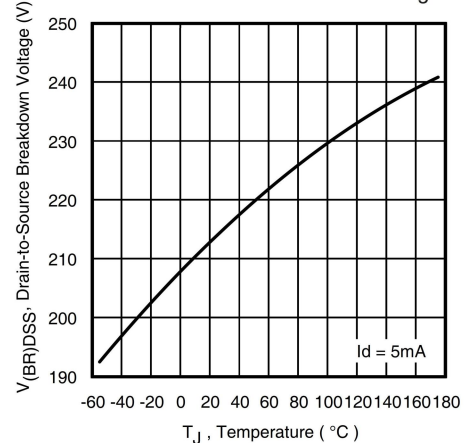
Maximum Safe Operating Area



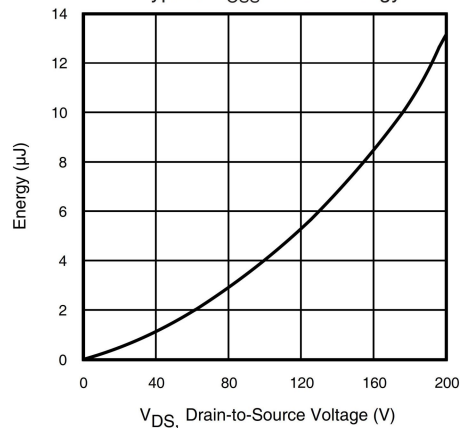
Maximum Drain Current vs. Case Temperature



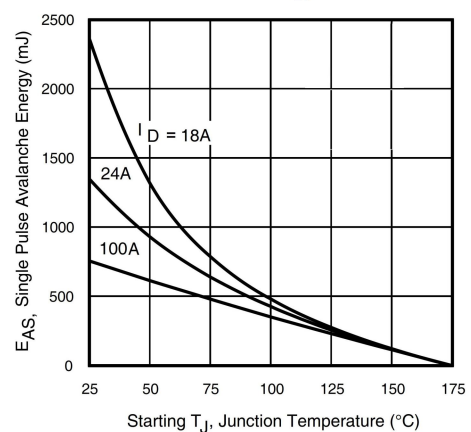
Drain-to-Source Breakdown Voltage

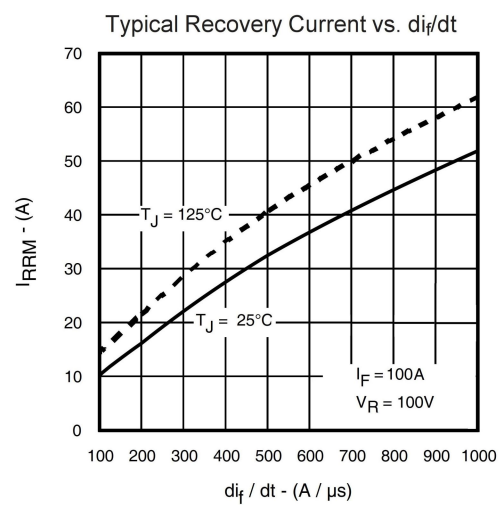
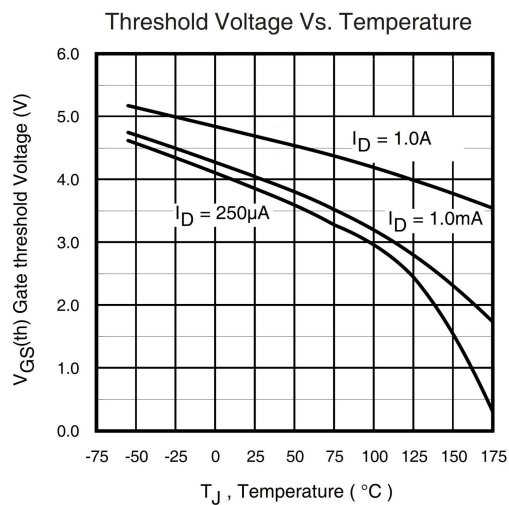
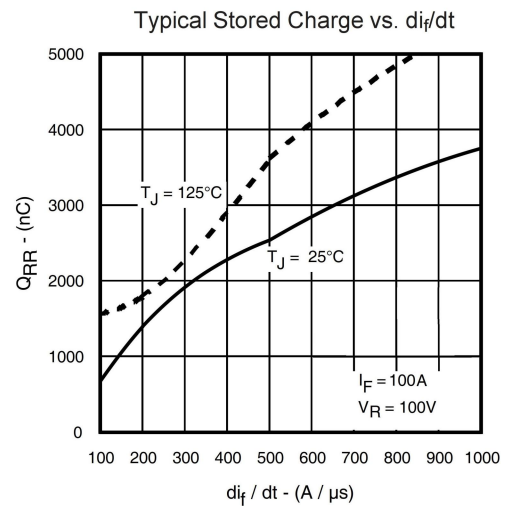
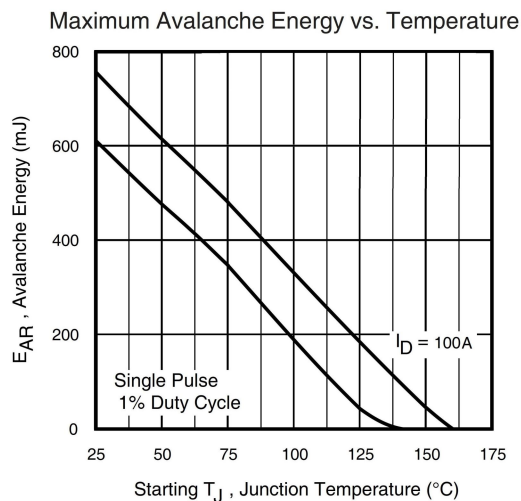
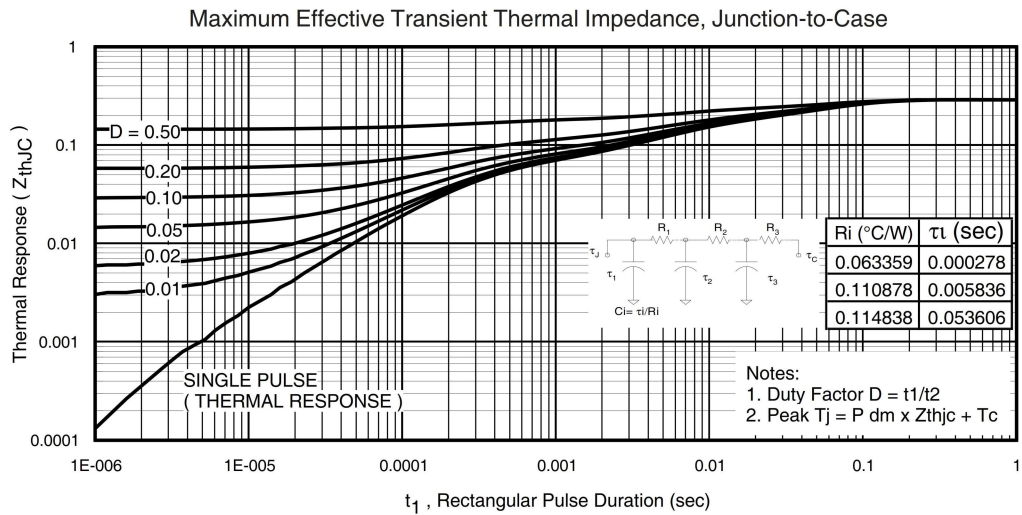


Typical  $C_{OSS}$  Stored Energy



Maximum Avalanche Energy Vs. Drain Current





## Package Mechanical DATA

