

#### **100V P-Channel MOSFET**

## **General Description**

The 12P10B uses advanced trench technology and design to provide excellent RDS(ON) with low gate charge. It can be used in a wide variety of applications.

## **Features**

- P-Channel
- Low ON-resistance.
- Fast Switching
- 100% avalanche tested

## **Product Summary**

BVDSS	RDSON	ID
-100V	0.26Ω	-12A

# **Applications**

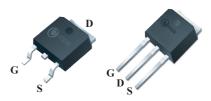
Power Switch

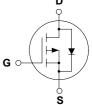
TO-252

CMU12P10B

DC / DC converter

# TO-252/251 Pin Configuration





CMU12P10B

Туре	Package	Marking			
CMD12P10B	TO-252	CMD12P10B			

TO-251

TO-251

# **Absolute Maximum Ratings**

Symbol	Parameter	Rating	Units	
$V_{DS}$	Drain-Source Voltage	-100	V	
V <sub>GS</sub>	Gate-Source Voltage	±20	V	
I <sub>D</sub> @T <sub>C</sub> =25°C	Continuous Drain Current	-12	А	
I <sub>DM</sub>	Pulsed Drain Current (Note 1)	-48	А	
EAS	Single Pulse Avalanche Energy	60	mJ	
P <sub>D</sub> @T <sub>C</sub> =25°C	Total Power Dissipation	75	W	
T <sub>STG</sub>	Storage Temperature Range	-55 to 150	°C	
T <sub>J</sub>	Operating Junction Temperature Range	-55 to 150	ů	

# **Thermal Data**

Symbol	Parameter	Тур.	Max.	Unit	
$R_{ heta JA}$	Thermal Resistance Junction-ambient		62.5	°C/W	
$R_{ heta JC}$	Thermal Resistance Junction-case		2.0	°C/W	

# CMD12P10B/CMU12P10B



#### **100V P-Channel MOSFET**

# Electrical Characteristics ( $T_J$ =25 $\,^{\circ}$ C, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V , I <sub>D</sub> =-250uA	-100			V
R <sub>DS(ON)</sub>	Static Drain-Source On-Resistance	V <sub>GS</sub> =-10V, I <sub>D</sub> =-6A			0.26	Ω
T CDS(ON)		$V_{GS}$ =-4.5 $V$ , $I_D$ =-5 $A$			0.28	
V <sub>GS(th)</sub>	Gate Threshold Voltage	$V_{GS}=V_{DS}$ , $I_D=-250uA$	-1		-3	V
1	Drain-Source Leakage Current	V <sub>DS</sub> =-100V , V <sub>GS</sub> =0V			-1	- uA
I <sub>DSS</sub>		V <sub>DS</sub> =-80V, T <sub>C</sub> =150°C			-10	
I <sub>GSS</sub>	Gate-Source Leakage Current	$V_{GS}=\pm 20V$ , $V_{DS}=0V$			±100	nA
gfs	Forward Transconductance	V <sub>DS</sub> =-10V, I <sub>D</sub> =-6A (Note 2)		8		S
$Q_g$	Total Gate Charge	I <sub>D</sub> =-11.5A		20		
$Q_gs$	Gate-Source Charge	V <sub>DS</sub> =-80V		4		nC
$Q_gd$	Gate-Drain Charge	V <sub>GS</sub> =-10V (Note 2, 3)		11		
$T_{d(on)}$	Turn-On Delay Time	V <sub>DS</sub> =-50V		14		
Tr	Rise Time	I <sub>D</sub> =-11.5A		158		ns
$T_{d(off)}$	Turn-Off Delay Time	R <sub>G</sub> =25Ω		34		115
$T_f$	Fall Time	(Note 2, 3)		58		
C <sub>iss</sub>	Input Capacitance			1400		
C <sub>oss</sub>	Output Capacitance	$V_{DS}$ =-25V, $V_{GS}$ =0V , f=1MHz		220		pF
C <sub>rss</sub>	Reverse Transfer Capacitance			65		

## **Diode Characteristics**

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
trr	Reverse Recovery Time	I <sub>S</sub> =-11.5A, V <sub>GS</sub> =0V		110		ns
Qrr	Reverse Recovery Charge	dI/dt=-100A/µs (Note 2)		0.47		μC
V <sub>SD</sub>	Diode Forward Voltage	V <sub>GS</sub> =0V , I <sub>S</sub> =-12A			-1.2	V

**Notes:**1. Repetitive Rating : Pulse width limited by maximum junction temperature 2. Pulse Test : Pulse width  $\leqslant$  300 s, Duty cycle  $\leqslant$  2% 3. Essentially independent of operating temperature

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