

## General Description

The CMSA016N10 uses advanced SGT technology to provide excellent RDS (ON), low gate charge and minimize the loss of power conversion applications. This device is suitable to be used as the low side FET in SMPS, load switching and general purpose.

## Features

- $R_{DS(ON)} < 18m\Omega$  @  $V_{GS}=10V$
- 100% avalanche tested
- Small Footprint (5x6mm) for Compact Design
- Switching losses reduced

## Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
$V_{DS}$	Drain-Source Voltage	100	V
$V_{GS}$	Gate-Source Voltage	$\pm 20$	V
$I_D@T_C=25^\circ C$	Continuous Drain Current	45	A
$I_D@T_C=85^\circ C$	Continuous Drain Current	27	A
$I_{DM}$	Pulsed Drain Current	180	A
EAS	Single Pulse Avalanche Energy <sup>1</sup>	33	mJ
$P_D@T_C=25^\circ C$	Total Power Dissipation	80	W
$T_{STG}$	Storage Temperature Range	-55 to 150	$^\circ C$
$T_J$	Operating Junction Temperature Range	-55 to 150	$^\circ C$

## Thermal Data

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient <sup>2</sup>	---	22.7	$^\circ C/W$
$R_{\theta JC}$	Thermal Resistance Junction -Case	---	1.5	$^\circ C/W$

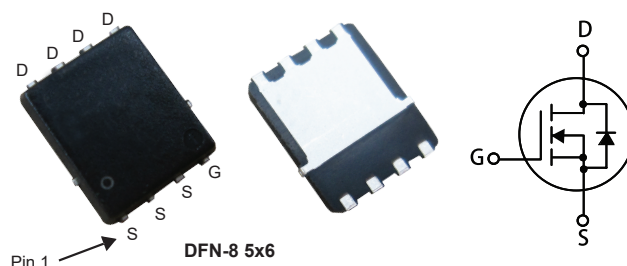
## Product Summary

BVDSS	RDSON	ID
100V	18m $\Omega$	45A

## Applications

- DC/DC Converters in Computing, Servers, and POL
- Isolated DC/DC Converters in Telecom and Industrial

## DFN-8 5x6 Pin Configuration



Type	Package	Marking
CMSA016N10	DFN-8 5*6	CMSA016N10

## N-Channel Enhancement Mode Field Effect Transistor

Electrical Characteristics ( $T_J=25^{\circ}\text{C}$  , unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$BV_{DSS}$	Drain-Source Breakdown Voltage	$V_{GS}=0V$ , $I_D=250\mu A$	100	---	---	V
$R_{DS(ON)}$	Static Drain-Source On-Resistance	$V_{GS}=10V$ , $I_D=20A$	---	15.5	18	m $\Omega$
		$V_{GS}=4.5V$ , $I_D=15A$	---	21	25	
$V_{GS(th)}$	Gate Threshold Voltage	$V_{GS}=V_{DS}$ , $I_D=250\mu A$	1	---	3	V
$I_{DSS}$	Drain-Source Leakage Current	$V_{DS}=100V$ , $V_{GS}=0V$	---	---	1	$\mu A$
$I_{GSS}$	Gate-Source Leakage Current	$V_{GS}=\pm 20V$ , $V_{DS}=0V$	---	---	$\pm 100$	nA
$g_{fs}$	Forward Transconductance	$V_{DS}=10V$ , $I_D=20A$	---	19	---	S
$R_g$	Gate Resistance	$V_{DS}=0V$ , $V_{GS}=0V$ , $f=1MHz$	---	3	---	$\Omega$
$Q_g$	Total Gate Charge	$V_{DD}=50V$ , $I_D=20A$ $V_{GS}=10V$	---	31	---	nC
$Q_{gs}$	Gate-Source Charge		---	6	---	
$Q_{gd}$	Gate-Drain Charge		---	5.1	---	
$T_{d(on)}$	Turn-On Delay Time	$V_{DD}=50V$ , $V_{GS}=10V$ , $R_G=3\Omega$ $I_D=20A$	---	8	---	ns
$T_r$	Rise Time		---	11	---	
$T_{d(off)}$	Turn-Off Delay Time		---	26	---	
$T_f$	Fall Time		---	7.5	---	
$C_{iss}$	Input Capacitance	$V_{DS}=25V$ , $V_{GS}=0V$ , $f=1MHz$	---	1000	---	pF
$C_{oss}$	Output Capacitance		---	500	---	
$C_{rss}$	Reverse Transfer Capacitance		---	75	---	

## Diode Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$I_S$	Diode continuous forward current	$V_G=V_D=0V$ , Force Current	---	---	45	A
$I_{S,pulse}$	Diode pulse current		---	---	180	A
$V_{SD}$	Diode Forward Voltage	$V_{GS}=0V$ , $I_S=20A$ , $T_J=25^{\circ}\text{C}$	---	0.9	1.2	V

## Notes:

- The EAS data shows Max. rating . The test condition is  $V_{DD}=50V$  ,  $V_{GS}=10V$  ,  $L=0.5mH$  ,  $I_{AS}=11.6A$
- Surface mounted FR-4 board by JEDEC (jesd51-7). Continuous current at  $TC=25^{\circ}\text{C}$  is silicon limited

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