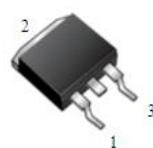
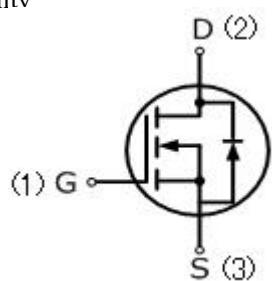


12N65(B,H)

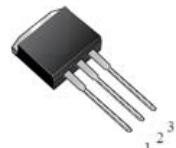
12 Amps, 650 Volts N-CHANNEL Power MOSFET

FEATURE

- 12A, 650V, $R_{DS(ON)MAX}=0.80\ \Omega$ @ $V_{GS}=10V/6A$
- Low gate charge
- Low C_{iss}
- Fast switching
- 100% avalanche tested
- Improved dv/dt capability
- Halogen free



TO-263-2L
12N65B



TO-262-3L
12N65H

Absolute Maximum Ratings ($T_c=25^\circ C$, unless otherwise noted)

Parameter	Symbol	12N65(B,H)	UNIT
Drain-Source Voltage	V_{DSS}	650	V
Gate-Source Voltage	V_{GSS}	± 30	
Continuous Drain Current	I_D	12	A
Pulsed Drain Current (Note 1)	I_{DM}	48	
Single Pulse Avalanche Energy (Note 2)	E_{AS}	550	mJ
Reverse Diode dV/dt (Note 3)	dv/dt	5	V/ns
Operating Junction and Storage Temperature Range	T_J, T_{STG}	-55 to +150	°C
Maximum lead temperature for soldering purposes, 1/8" from case for 5 seconds	T_L	260	°C

Parameter	Symbol	12N65(B,H)	Units
Thermal resistance, Channel to Case	$R_{th(ch-c)}$	0.83	°C/W
Thermal resistance, Channel to Ambient	$R_{th(ch-a)}$	62.5	°C/W
Maximum Power Dissipation	$T_c=25^\circ C$	P_D	W

Electrical Characteristics ($T_c=25^\circ\text{C}$, unless otherwise noted)						
Parameter	Symbol	Test Conditions	Min	Typ	Max	Units
Off Characteristics						
Drain-Source Breakdown Voltage	BV_{DSS}	$\text{V}_{\text{GS}}=0\text{V}, \text{I}_D=250\text{uA}$	650	—	—	V
Breakdown Temperature Coefficient	$\Delta \text{BV}_{\text{DSS}} / \Delta T_J$	Reference to 25°C , $\text{I}_D=250\text{uA}$	—	0.7	—	$\text{V}/^\circ\text{C}$
Zero Gate Voltage Drain Current	I_{DS}	$\text{V}_{\text{DS}}=650\text{V}, \text{V}_{\text{GS}}=0\text{V}$	—	—	1	uA
Gate-Body Leakage Current, Forward	I_{GSSF}	$\text{V}_{\text{GS}}=30\text{V}, \text{V}_{\text{DS}}=0\text{V}$	—	—	100	nA
Gate-Body Leakage Current, Reverse	I_{GSSR}	$\text{V}_{\text{GS}}=-30\text{V}, \text{V}_{\text{DS}}=0\text{V}$	—	—	-100	nA
On Characteristics						
Gate-Source Threshold Voltage	$\text{V}_{\text{GS(th)}}$	$\text{V}_{\text{DS}}=\text{V}_{\text{GS}}, \text{I}_D=250\text{uA}$	2	—	4	V
Drain-Source On-State Resistance	$\text{R}_{\text{DS(on)}}$	$\text{V}_{\text{GS}}=10\text{V}, \text{I}_D=6\text{A}$	—	0.66	0.80	Ω
Dynamic Characteristics						
Input Capacitance	C_{iss}	$\text{V}_{\text{DS}}=25\text{V}, \text{V}_{\text{GS}}=0\text{V},$ $f=1.0\text{MHz}$	—	1993	—	pF
Output Capacitance	C_{oss}		—	160	—	pF
Reverse Transfer Capacitance	C_{rss}		—	9.5	—	pF
Switching Characteristics						
Turn-On Delay Time	$t_{\text{d(on)}}$	$\text{V}_{\text{DD}}=325\text{V}, \text{I}_D=12\text{A},$ $\text{R}_G=10\Omega$ (Note 3,4)	—	28	—	ns
Turn-On Rise Time	t_r		—	26	—	ns
Turn-Off Delay Time	$t_{\text{d(off)}}$		—	64	—	ns
Turn-Off Fall Time	t_f		—	45	—	ns
Total Gate Charge	Q_g	$\text{V}_{\text{DS}}=520\text{V}, \text{I}_D=12\text{A},$ $\text{V}_{\text{GS}}=10\text{V}$, (Note 3,4)	—	40	—	nC
Gate-Source Charge	Q_{gs}		—	10	—	nC
Gate-Drain Charge	Q_{gd}		—	14	—	nC
Drain-Source Body Diode Characteristics and Maximum Ratings						
Continuous Diode Forward Current	I_s	$\text{I}_s=12\text{A}, \text{V}_{\text{GS}}=0\text{V}$ $\text{V}_{\text{GS}}=0\text{V}, \text{I}_s=12\text{A},$ $d\text{I}_F/dt=100\text{A/us}$, (Note 4)	—	—	12	A
Pulsed Diode Forward Current	I_{SM}		—	—	48	A
Diode Forward Voltage	V_{SD}		—	—	1.5	V
Reverse Recovery Time	t_{rr}		—	651	—	ns
Reverse Recovery Charge	Q_{rr}		—	4.297	—	uC

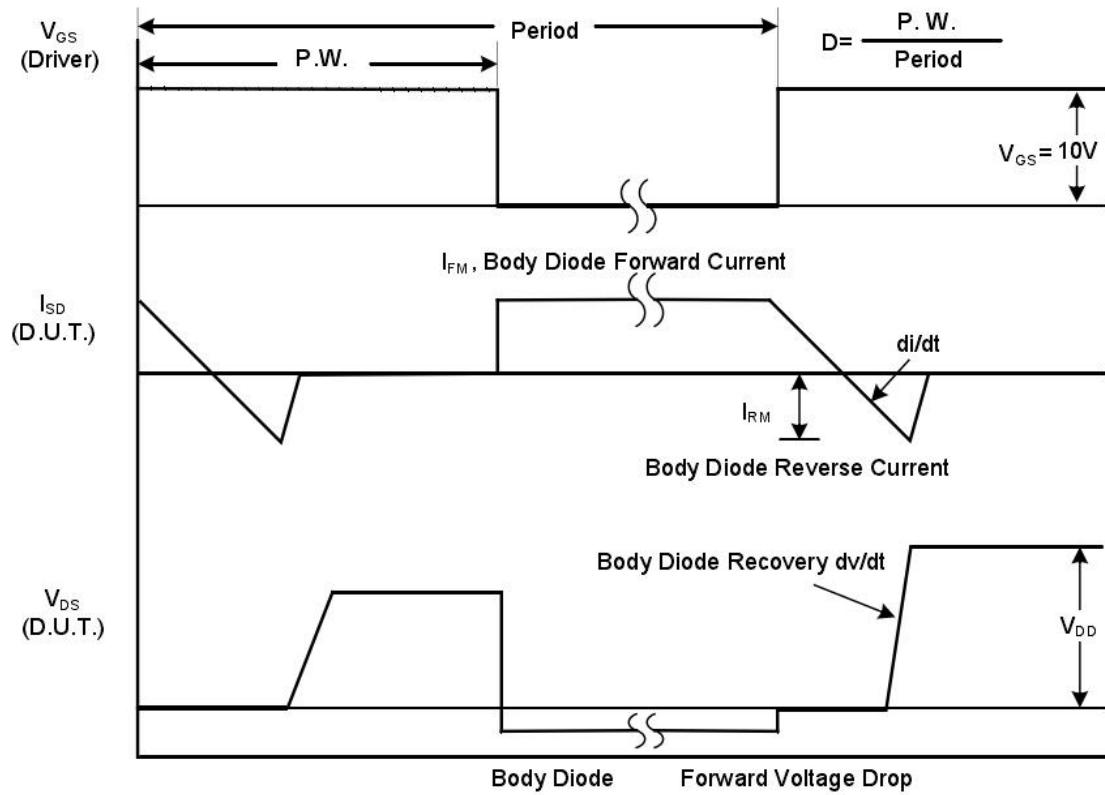
Notes

- Repetitive Rating: pulse width limited by maximum junction temperature.
- $L=10\text{mH}, I_{AS}=10.5\text{A}$, starting $T_J=25^\circ\text{C}$.
- $I_{SD}=12\text{A}, dI/dt \leq 100\text{A/us}, V_{DD} \leq \text{BV}_{\text{DSS}}$, starting $T_J=25^\circ\text{C}$, Pulse width $\leq 300\text{us}$; duty cycle $\leq 2\%$.
- Repetitive rating; pulse width limited by maximum junction temperature.

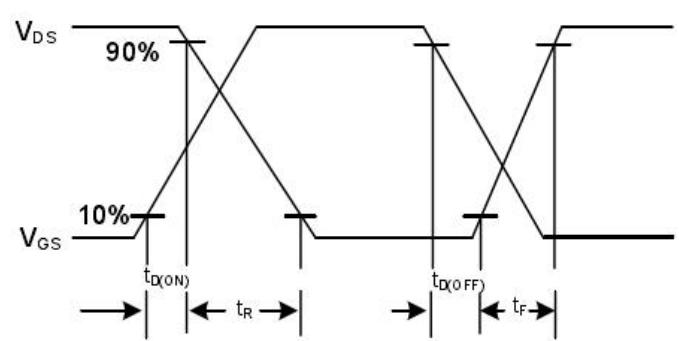
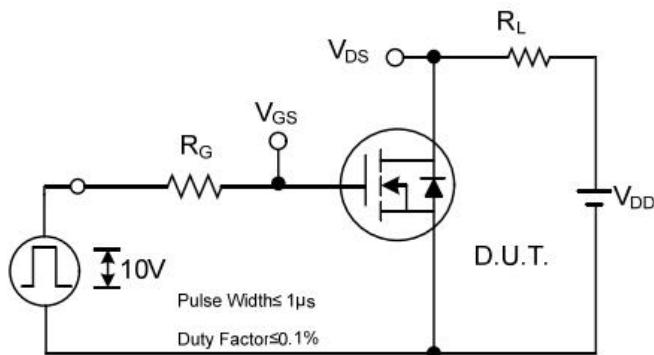
TEST CIRCUIT AND WAVEFORM



Peak Diode Recovery dv/dt Test Circuit

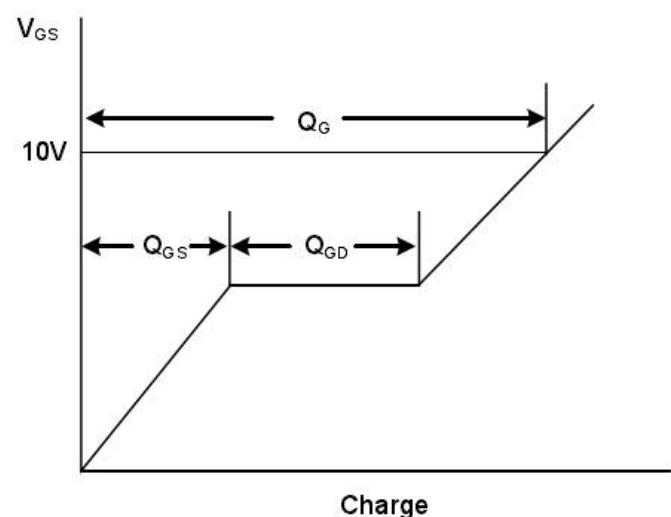
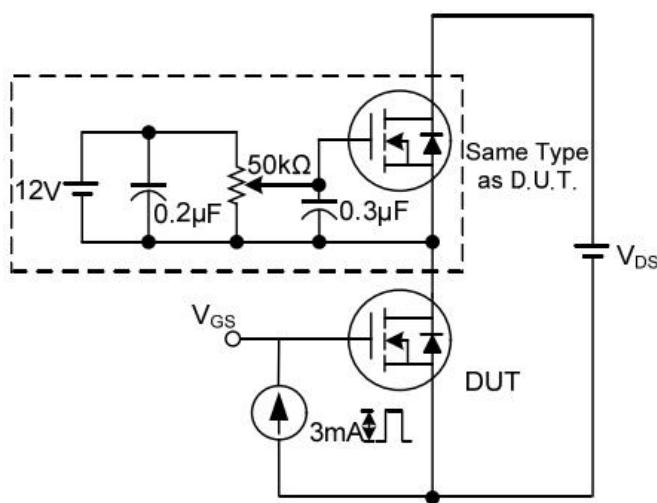


Peak Diode Recovery dv/dt Waveforms



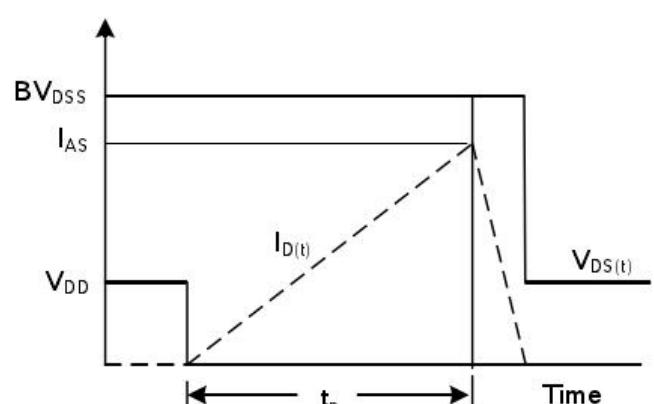
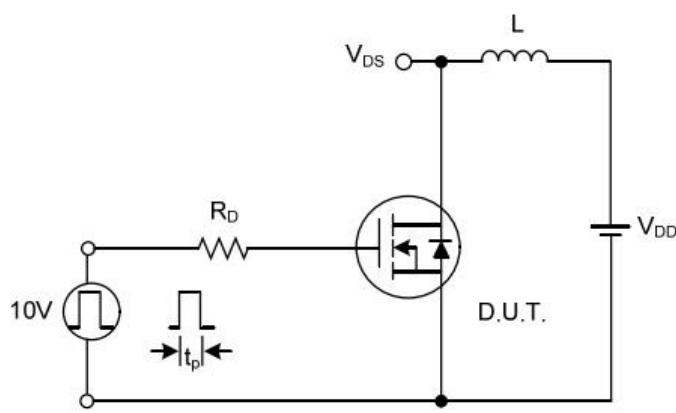
Switching Test Circuit

Switching Waveforms



Gate Charge Test Circuit

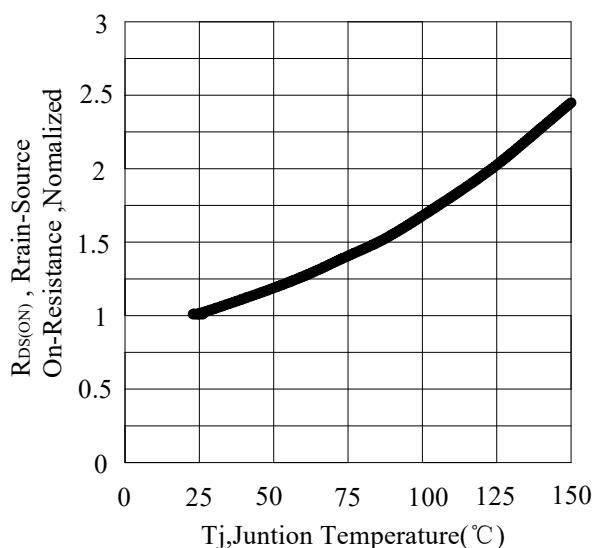
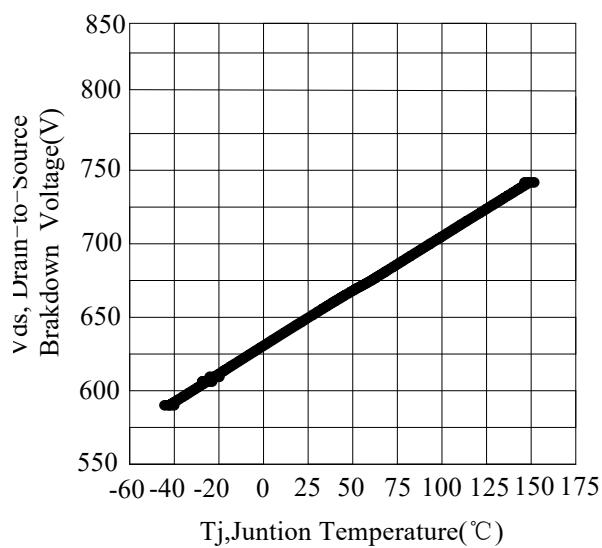
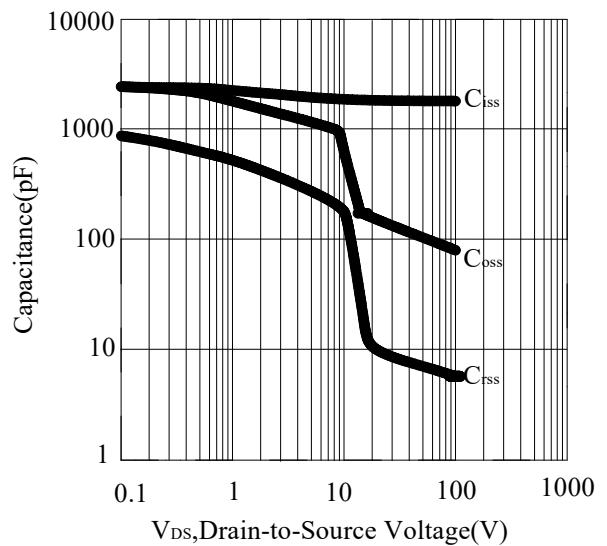
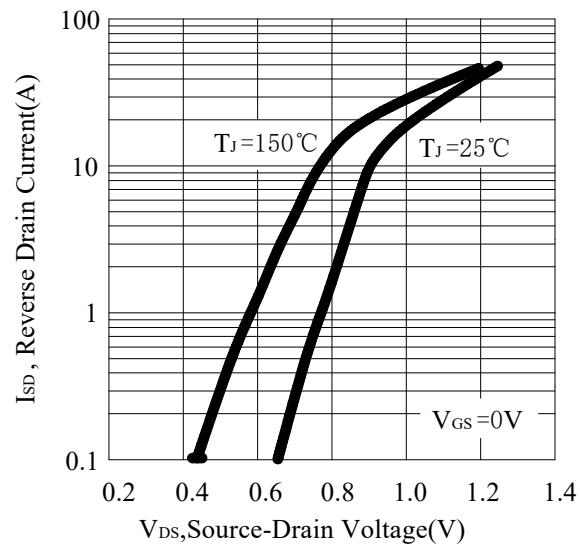
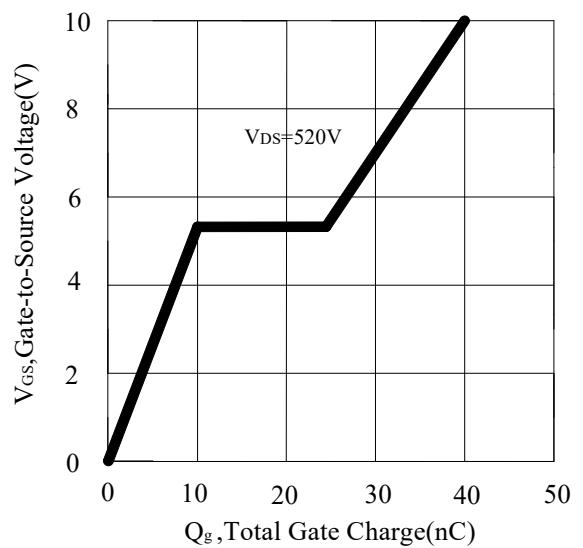
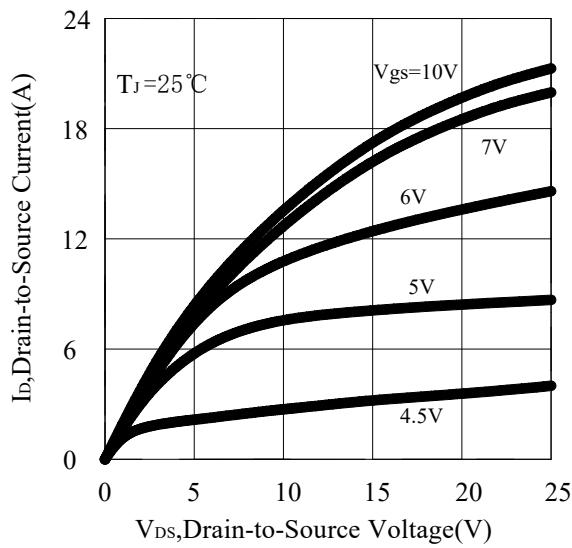
Gate Charge Waveform

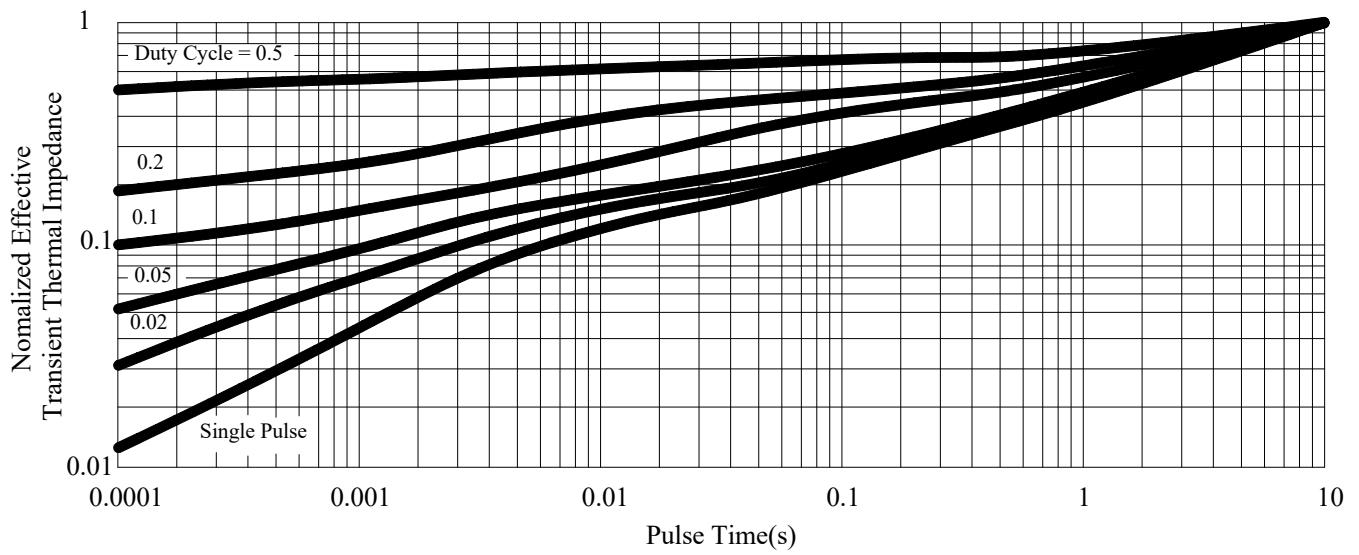
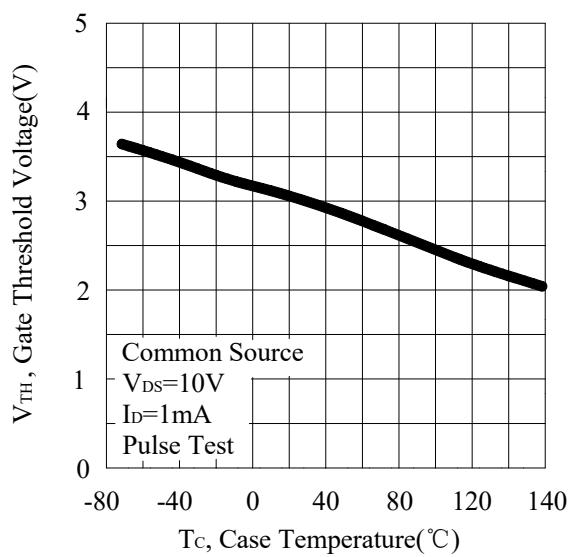
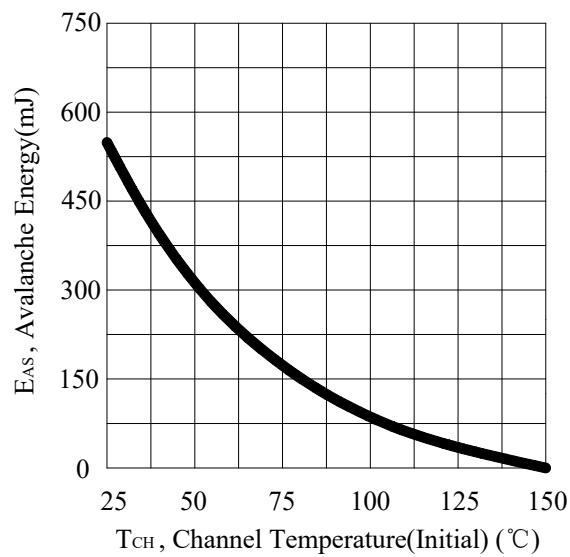
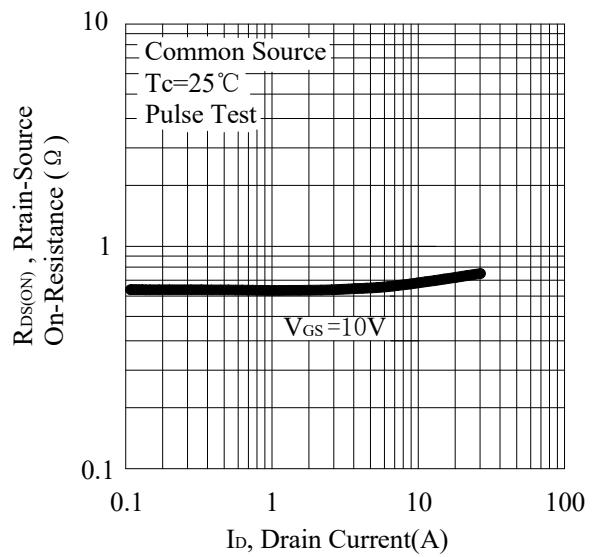
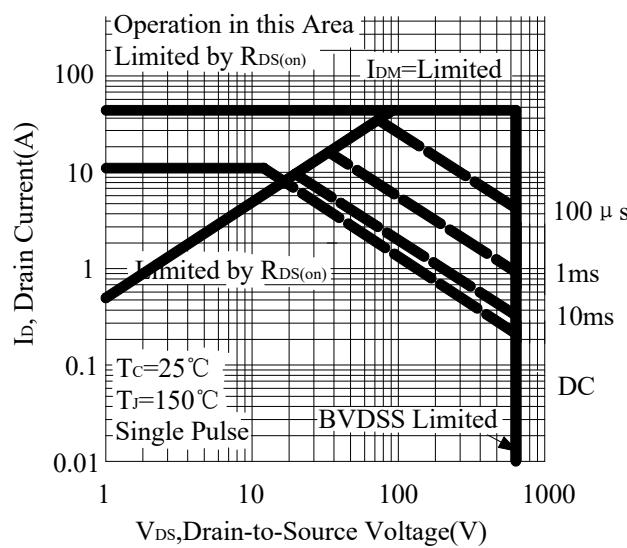


Unclamped Inductive Switching Test Circuit

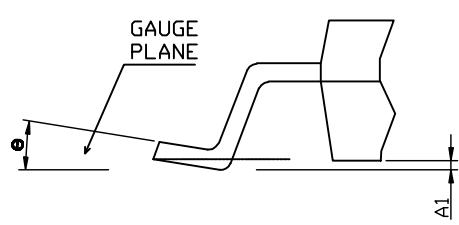
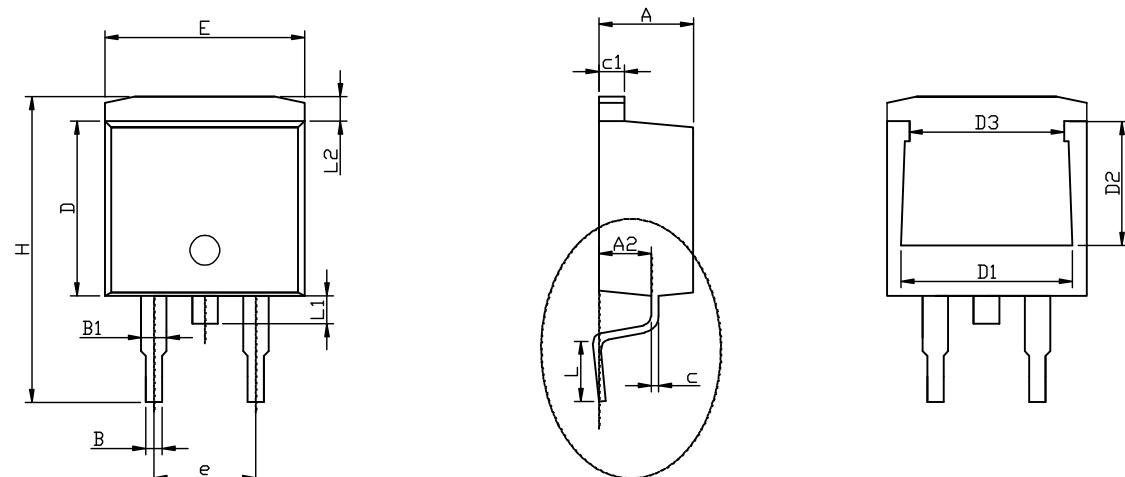
Unclamped Inductive Switching Waveforms

RATING AND CHARACTERISTIC CURVES



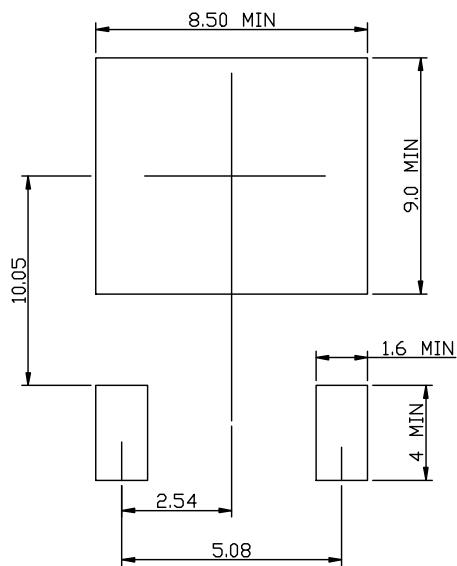


TO-263-2L PACKAGE OUTLINE



VIEW1

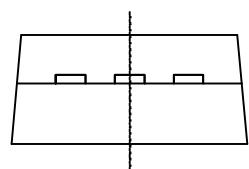
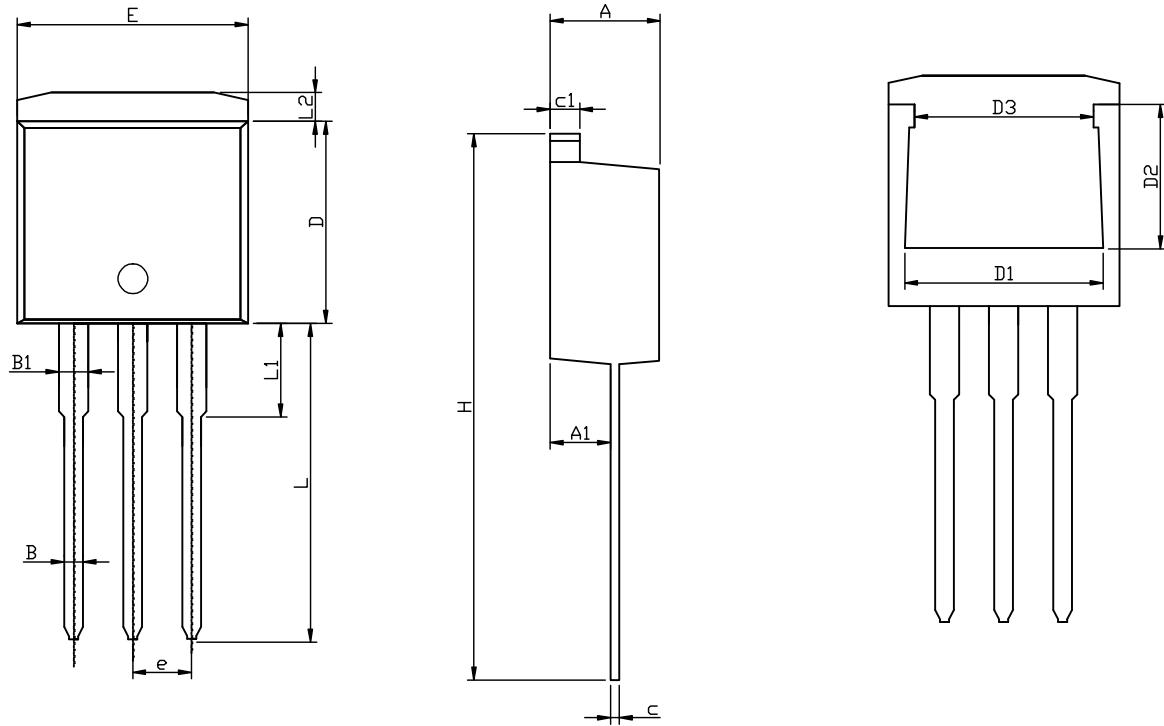
RECOMMENDED LAND PATTERN



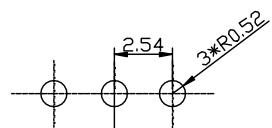
	MIN	NOM	MAX
A	4.50	4.70	4.90
A1	0.05	0.15	0.30
A2	2.45	2.60	2.70
B	0.72	0.82	0.92
B1	1.12	1.27	1.42
c	0.28	0.38	0.48
c1	1.17	1.27	1.37
D	8.46	8.66	8.86
D1	7.90	8.10	8.40
D2	5.50	5.70	5.90
D3	7.10	7.30	7.50
E	9.85	10.15	10.45
e		5.08BCS	
H	14.75	15.15	15.55
L	2.30	2.55	2.80
L1	1.20	1.40	1.60
L2	1.01	1.23	1.50
Θ	0°	7°	8°

UNIT: mm

TO-262-3L PACKAGE OUTLINE



RECOMMENDED LAND PATTERN

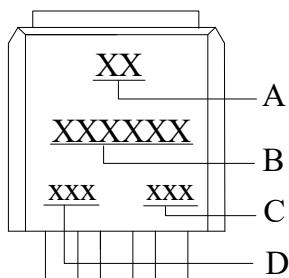


UNIT: mm

	MIN	NOM	MAX
A	4.50	4.70	4.90
A1	2.45	2.60	2.70
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B1	1.12	1.27	1.42
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c1	1.17	1.27	1.37
D	8.46	8.66	8.86
D1	7.90	8.10	8.40
D2	5.50	5.70	5.90
D3	7.10	7.30	7.50
E	9.85	10.15	10.45
e		2.54	
H	23.20	23.60	24.00
L	13.10	13.60	14.10
L1	3.85	4.05	4.35
L2	1.01	1.23	1.50

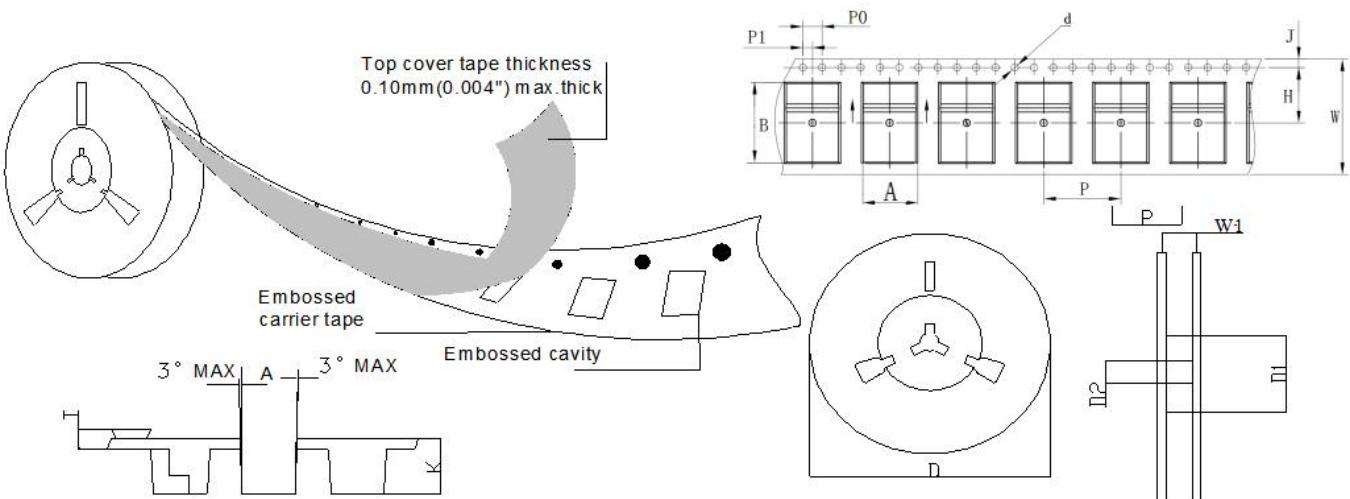
Marking and packaging illustration

1、Marking



SYMBOL	Explanation
A	Trademark
B	Product Name
C	Date Code
D	Product Information

2、Packaging



SPECIFICATIONS mm(inch)		PACKAGE	SPECIFICATIONS mm(inch)		PACKAGE
ITEM	SYM BOL	TO-263	ITEM	SYM BOL	TO-263
Carrier width	A	11.65(0.459)Typ	Carrier depth	K	5.15(0.203)Typ
Carrier length	B	17.05(0.672)Typ	Punch hole pitch	P	16.00(0.630)Typ
Sprocket hole	d	ø1.50(0.059)Typ	Sprocket hole pitch	P0	4.00(0.157)Typ
Reel outer diameter	D	330.0(13.0)Typ	Embossment center	P1	2.00(0.079)Typ
Reel inner diameter	D1	90.0(3.54)Typ	Overall tape thickness	T	0.40(0.016)Typ
Feed hole diameter	D2	13.0(0.512)Typ	Tape width	W	24.0(0.945)Typ
Sprocket hole position	J	1.75(0.069)Typ	Reel width	W1	25.5(1.000)Typ
Punch hole position	H	11.50(0.453)Typ			