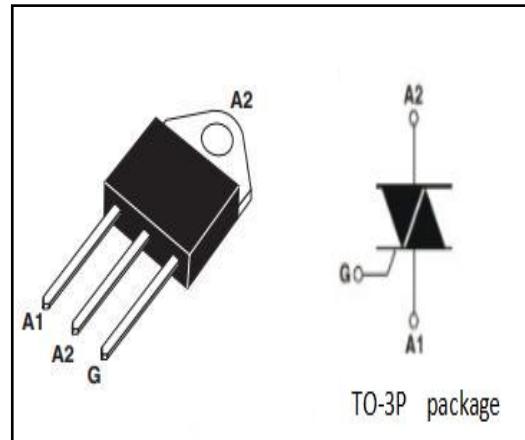


**DESCRIPTION**

- With TO-3P packaging
- Operating in 3 quadrants
- High commutation capability
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

**APPLICATIONS**

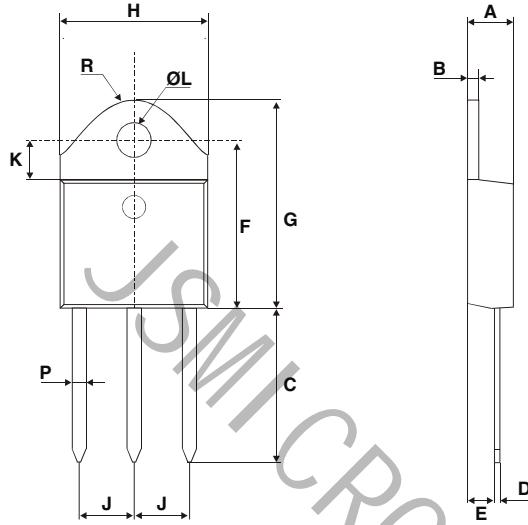
- Switching applications
- Phase control
- Static switching on inductive or resistive load


**ABSOLUTE MAXIMUM RATINGS( $T_a=25^\circ\text{C}$ )**

SYMBOL	PARAMETER	MAX	UNIT
$V_{DRM}$	Repetitive peak off-state voltage	600	V
$V_{RRM}$	Repetitive peak reverse voltage	600	V
$I_{T(RSM)}$	Average on-state current $T_c=105^\circ\text{C}$	25	A
$I_{TSM}$	Surge non-repetitive on-state current	50HZ 60HZ 250 260	A
$P_{G(AV)}$	Average gate power dissipation ( over any 20 ms period ) $T_j=125^\circ\text{C}$	1	W
$T_j$	Operating junction temperature	-40~125	°C
$T_{stg}$	Storage temperature	-40~150	°C

**ELECTRICAL CHARACTERISTICS ( $T_c=25^\circ\text{C}$  unless otherwise specified)**

SYMBOL	PARAMETER	CONDITIONS		MIN	MAX	UNIT
$I_{RRM}$	Repetitive peak reverse current	$V_R=V_{RRM}$ Rated; $V_D=V_{DRM}$ Rated;	$T_j=25^\circ\text{C}$ $T_j=125^\circ\text{C}$	5 3	$\mu\text{A}$ $\text{mA}$	
$I_{DRM}$	Repetitive peak off-state current					
$V_{TM}$	On-state voltage	$I_T=35\text{A}; t_P=380 \mu\text{s}$			1.55	V
$I_{GT}$	Gate-trigger current	$V_D = 12\text{V}; R_L = 33\Omega;$	I	50	mA	
			II	50		
			III	50		
$V_{GT}$	Gate-trigger voltage	$V_D = 12\text{V}; R_L = 33\Omega;$			1.3	V
$R_{th(j-c)}$	Junction to case				0.6	°C/W

**TO-3P (Insulated and non insulated) Package Mechanical Data**


REF.	DIMENSIONS					
	Millimeters			Inches		
	Min.	Typ.	Max.	Min.	Typ.	Max.
A	4.4			4.6	0.173	
B	1.45			1.55	0.057	
C	14.35			15.60	0.565	
D	0.5			0.7	0.020	
E	2.7			2.9	0.106	
F	15.8			16.5	0.622	
G	20.4			21.1	0.815	
H	15.1			15.5	0.594	
J	5.4			5.65	0.213	
K	3.4			3.65	0.134	
ØL	4.08			4.17	0.161	
P	1.20			1.40	0.047	
R		4.60			0.181	