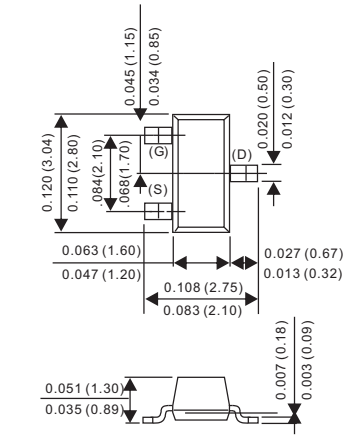
BSS84

P-Channel Enhancement Mode Mosfet

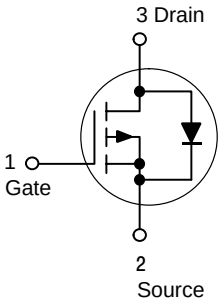
SOT-23



These miniature surface mount MOSFETs reduce power loss conserve energy, making this device ideal for use in small power management circuitry. Typical applications are dc–dc converters, load switching, power management in portable and battery–powered products such as computers, printers, cellular and cordless telephones.

MAXIMUM RATINGS (T_J = 25°C unless otherwise noted)

Rating	Symbol	Value	Unit
Drain–to–Source Voltage	V_{DSS}	50	Vdc
Gate–to–Source Voltage – Continuous	V_{GS}	± 20	Vdc
Drain Current – Continuous @ T_A = 25°C – Pulsed Drain Current ($t_p \leq 10$ μs)	 I_D I_{DM}	 130 520	mA
Total Power Dissipation @ T_A = 25°C	P_D	225	mW
Operating and Storage Temperature Range	T_J, T_{stg}	– 55 to 150	°C
Thermal Resistance – Junction–to–Ambient	$R_{\theta JA}$	556	°C/W
Maximum Lead Temperature for Soldering Purposes, for 10 seconds	T_L	260	°C



Marking **PD**

Dimensions in inches and (millimeters)

ELECTRICAL CHARACTERISTICS (T_A = 25°C unless otherwise noted)

Characteristic		Symbol	Min	Typ	Max	Unit
OFF CHARACTERISTICS						
Drain–to–Source Breakdown Voltage (V_{GS} = 0 Vdc, I_D = 250 μAdc)		$V_{(BR)DSS}$	50	–	–	Vdc
Zero Gate Voltage Drain Current (V_{DS} = 25 Vdc, V_{GS} = 0 Vdc) (V_{DS} = 50 Vdc, V_{GS} = 0 Vdc) (V_{DS} = 50 Vdc, V_{GS} = 0 Vdc, T_J = 125°C)		I_{DSS}	 – – –	 – – –	 0.1 15 60	μAdc
Gate–Body Leakage Current (V_{GS} = ± 20 Vdc, V_{DS} = 0 Vdc)		I_{GSS}	–	–	±60	μAdc
ON CHARACTERISTICS (Note 1.)						
Gate–Source Threaded Voltage (V_{DS} = V_{GS}, I_D = 1.0 mAdc)		$V_{GS(th)}$	0.8	–	2.0	Vdc
Static Drain–to–Source On–Resistance (V_{GS} = 5.0 Vdc, I_D = 100 mAdc)		$r_{DS(on)}$	–	5.0	10	Ohms
Transfer Admittance (V_{DS} = 25 Vdc, I_D = 100 mAdc, f = 1.0 kHz)		$\|y_{fs}\|$	50	–	–	mS
DYNAMIC CHARACTERISTICS						
Input Capacitance	(V_{DS} = 5.0 Vdc)	C_{iss}	–	30	–	pF
Output Capacitance	(V_{DS} = 5.0 Vdc)	C_{oss}	–	10	–	
Transfer Capacitance	(V_{DG} = 5.0 Vdc)	C_{rss}	–	5.0	–	
SWITCHING CHARACTERISTICS (Note 2.)						
Turn–On Delay Time	(V_{DD} = –15 Vdc, I_D = –2.5 Adc, R_L = 50 Ω)	$t_{d(on)}$	–	2.5	–	ns
Rise Time		t_r	–	1.0	–	
Turn–Off Delay Time		$t_{d(off)}$	–	16	–	
Fall Time		t_f	–	8.0	–	
Gate Charge		Q_T	–	6000	–	pC
SOURCE–DRAIN DIODE CHARACTERISTICS						
Continuous Current		I_S	–	–	0.130	A
Pulsed Current		I_{SM}	–	–	0.520	
Forward Voltage (Note 2.)		V_{SD}	–	2.5	–	V

1. Pulse Test: Pulse Width ≤ 300 μs, Duty Cycle ≤ 2%.
2. Switching characteristics are independent of operating junction temperature.

TYPICAL ELECTRICAL CHARACTERISTICS

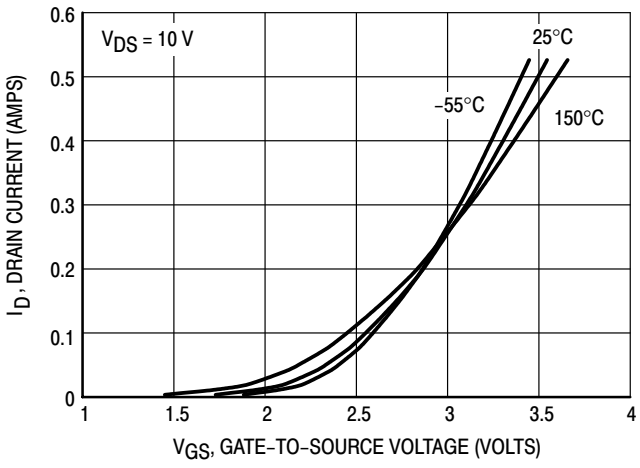


Figure 1. Transfer Characteristics

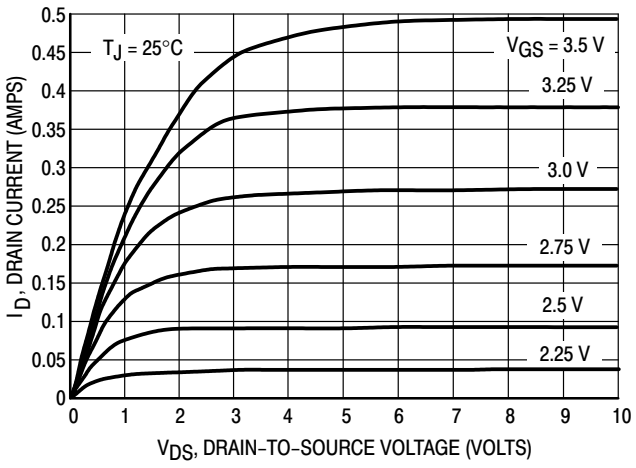


Figure 2. On–Region Characteristics

TYPICAL ELECTRICAL CHARACTERISTICS

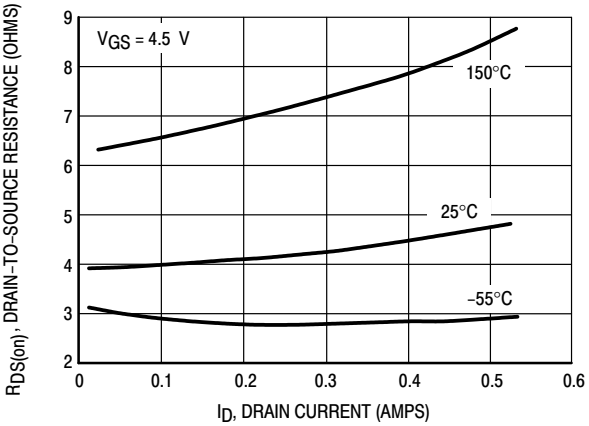


Figure 3. On−Resistance versus Drain Current

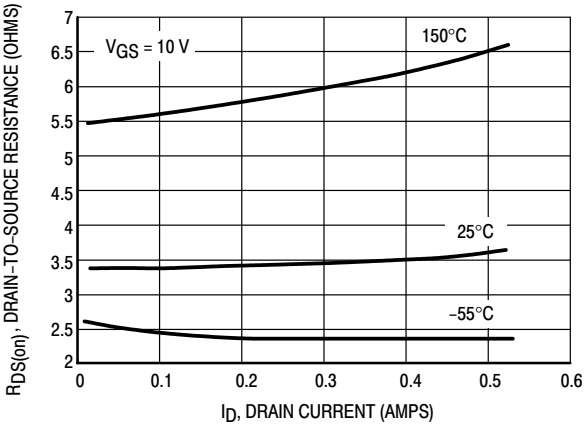


Figure 4. On−Resistance versus Drain Current

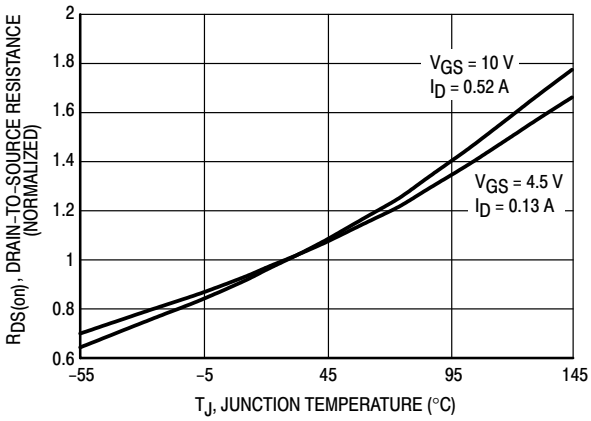


Figure 5. On−Resistance Variation with Temperature

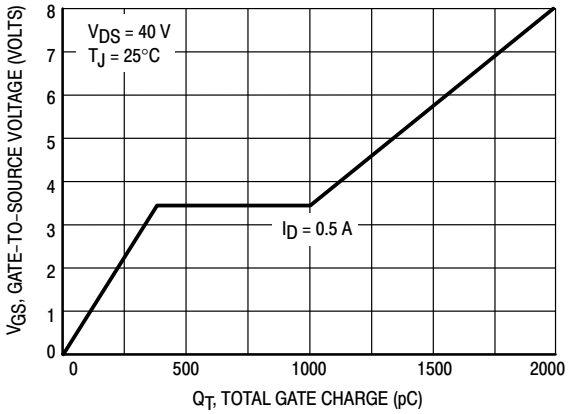


Figure 6. Gate Charge

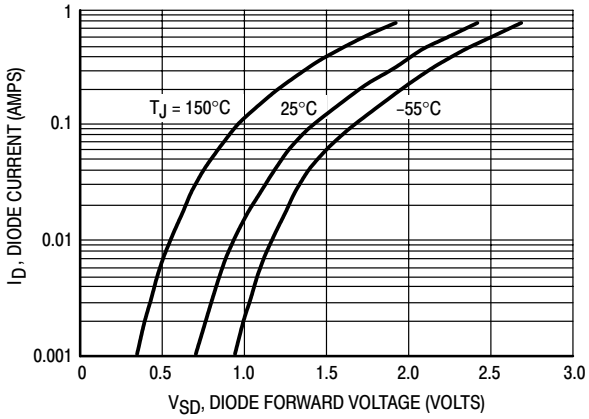


Figure 7. Body Diode Forward Voltage