

N-Channel 100 V (D-S) MOSFET

PRODUCT SUMMARY						
V _{DS} (V)	$R_{DS(on)}(\Omega)$	I _D (A) ^a	Q _g (Typ.)			
	0.0087 at V _{GS} = 10 V	65				
100	0.0094 at V _{GS} = 7.5 V	64	18.3 nC			
	0.0115 at V _{GS} = 5 V	60				

FEATURES

Halogen-free According to IEC 61249-2-21 Definition

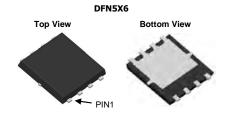


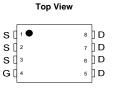
- 100 % R_g Tested 100 % UIS Tested
- Compliant to RoHS Directive 2002/95/EC

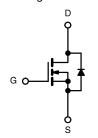


APPLICATIONS

- DC/DC Primary Side Switch
- Telecom/Server 48 V, Full/Half-Bridge dc-to-dc
- Industrial







N-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS	$(T_A = 25 C, unlete)$	ess otherwise no	oted)		
Parameter	Symbol	Limit	Unit		
Drain-Source Voltage	V _{DS}	100	V		
Gate-Source Voltage	V _{GS}	一 			
	$T_C = 25 ^{\circ}C$ $T_C = 70 ^{\circ}C$		65 60		
Continuous Drain Current (T _J = 150 °C)	T _A = 25 °C	I _D	17.6 ^{b, c}		
B. 18.19	T _A = 70 °C		13.9 ^{b, c}	Α	
Pulsed Drain Current		IDM	195		
Continuous Source-Drain Diode Current	T _C = 25 °C	I _S	65		
Solitinada Goardo Brain Bload Garrotti	T _A = 25 °C	.3	4.9 ^{b, c}		
Single Pulse Avalanche Current	L = 0.1 mH	I _{AS}	30		
Single Pulse Avalanche Energy	L = 0.1 IIII1	E _{AS}	45	mJ	
	T _C = 25 °C		83		
Maximum Power Dissipation	T _C = 70 °C	P _D	53	\Box w	
Maximum Fower Dissipation	T _A = 25 °C	ט'ט [5.4 ^{b, c}		
	T _A = 70 °C		3.4 ^{b, c}		
Operating Junction and Storage Temperature Range	T _J , T _{stg}	- 55 to 150	°C		
Soldering Recommendations (Peak Temperature)		260			

THERMAL RESISTANCE RATINGS							
Parameter	Symbol	Typical	Maximum	Unit			
Maximum Junction-to-Ambient	t ≤ 10 s	R_{thJA}	18	23	°C/W		
Maximum Junction-to-Case (Drain)	Steady State	R_{thJC}	1.0	1.5	5/ VV		

Notes:

- a. Package limited.
- b. Surface mounted on 1" x 1" FR4 board.

服务热线:400-655-8788

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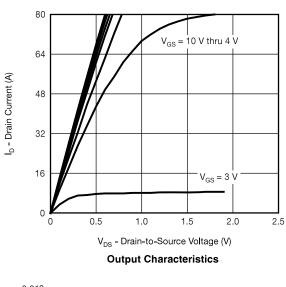
Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit
Static					•	•
Drain-Source Breakdown Voltage	V _{DS}	$V_{GS} = 0 \text{ V, } I_D = 250 \mu\text{A}$	100			V
V _{DS} Temperature Coefficient	$\Delta V_{DS}/T_{J}$	I _D = 250 μA		50		mV/°C
V _{GS(th)} Temperature Coefficient	$\Delta V_{GS(th)}/T_J$	I _D = 250 μA		- 5.8		
Gate-Source Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_D = 250 \mu A$	2		4	٧
Gate-Source Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			± 100	nA
7 0	1	V _{DS} = 100 V, V _{GS} = 0 V			1	μΑ
Zero Gate Voltage Drain Current	IDSS	V _{DS} = 100 V, V _{GS} = 0 V, T _J = 55 °C			10	
On-State Drain Current ^a	I _{D(on)}	$V_{DS} \ge 5 \text{ V}, V_{GS} = 10 \text{ V}$	30			Α
		$V_{GS} = 10 \text{ V}, I_D = 20 \text{ A}$		0.008		Ω
Drain-Source On-State Resistance ^a	R _{DS(on)}	$V_{GS} = 7.5 \text{ V}, I_D = 17 \text{ A}$		0.009		
		V _{GS} = 5 V, I _D = 15 A		0.010		
Forward Transconductancea	9 _{fs}	V _{DS} = 10 V, I _D = 20 A		57		S
Dynamic ^b					l .	
Input Capacitance				3930		
Output Capacitance	C _{oss}	$V_{DS} = 50 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$		420		pF
Reverse Transfer Capacitance	C _{rss}			265		
·	Q _g	V _{DS} = 50 V, V _{GS} = 10 V, I _D = 20 A	38.5 58		58	
Total Gate Charge		$V_{DS} = 50 \text{ V}, V_{GS} = 7.5 \text{ V}, I_D = 20 \text{ A}$		29	44	nC
				18.3	27.5	
Gate-Source Charge	Q _{gs}	$V_{DS} = 50 \text{ V}, V_{GS} = 4.5 \text{ V}, I_{D} = 20 \text{ A}$		5.5		
Gate-Drain Charge	Q _{gd}			7.8		
Gate Resistance	R_{g}	f = 1 MHz	0.4	1.9	3.8	Ω
Turn-On Delay Time	t _{d(on)}			12	24	
Rise Time	t _r	$V_{DD} = 50 \text{ V}, R_L = 5 \Omega$		12	24	
Turn-Off Delay Time	t _{d(off)}	$I_D \cong 10 \text{ A}, V_{GEN} = 10 \text{ V}, R_g = 1 \Omega$		36	70	
Fall Time	t _f			9	18	
Turn-On Delay Time	t _{d(on)}			13	26	ns
Rise Time	t _r	$V_{DD} = 50 \text{ V}, R_L = 5 \Omega$		15	30	
Turn-Off Delay Time	t _{d(off)}	$I_D\cong$ 10 A, V_{GEN} = 7.5 V, R_g = 1 Ω		35	70	
Fall Time	t _f			8	16	
Drain-Source Body Diode Characteristic	s				•	
Continuous Source-Drain Diode Current	I _S	T _C = 25 °C			60	_
Pulse Diode Forward Current ^a	I _{SM}				80	A
Body Diode Voltage	V _{SD}	I _S = 5 A		0.75	1.1	٧
Body Diode Reverse Recovery Time	t _{rr}			64	120	ns
Body Diode Reverse Recovery Charge	Q _{rr}	1 10 A dl/dt 100 A/v- T 05 00		80	160	nC
Reverse Recovery Fall Time	ta	I _F = 10 A, dl/dt = 100 A/μs, T _J = 25 °C		24		ns
Reverse Recovery Rise Time	t _b			40		

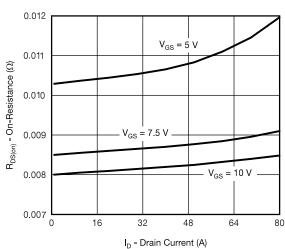
Notes:

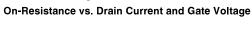
- a. Pulse test; pulse width \leq 300 μ s, duty cycle \leq 2 %.
- b. Guaranteed by design, not subject to production testing.

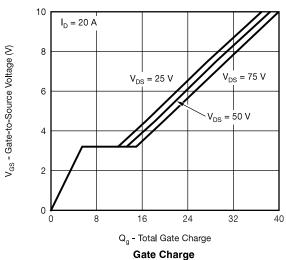
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

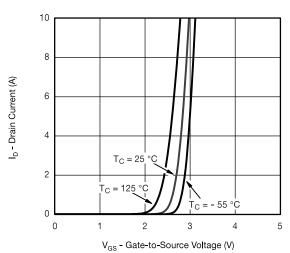




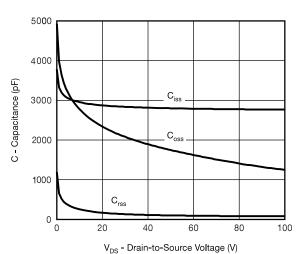




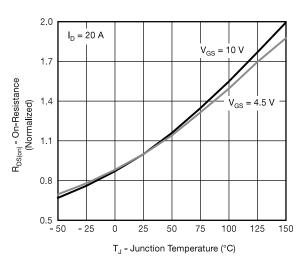






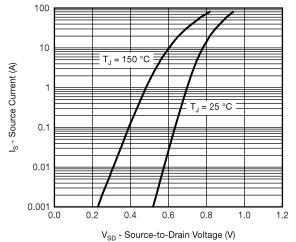


Capacitance

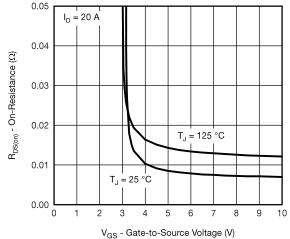


On-Resistance vs. Junction Temperature

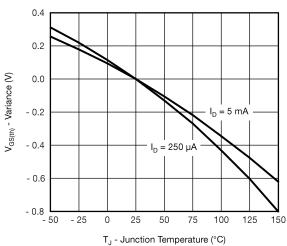




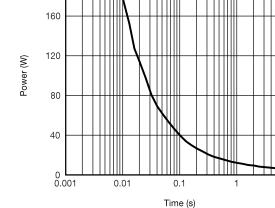
Source-Drain Diode Forward Voltage



On-Resistance vs. Gate-to-Source Voltage

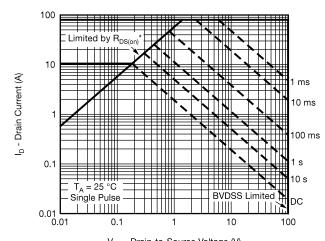


Junction Temperature (°C
Threshold Voltage



200

Single Pulse Power, Junction-to-Ambient



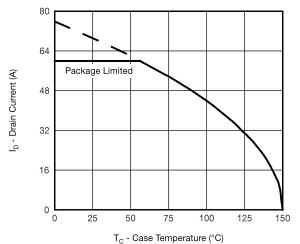
 $\rm V_{DS}$ - Drain-to-Source Voltage (V) * $\rm V_{GS} > minimum \ V_{GS}$ at which $\rm R_{DS(on)}$ is specified

Safe Operating Area, Junction-to-Ambient

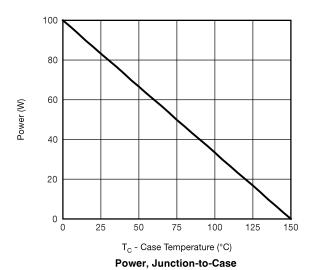
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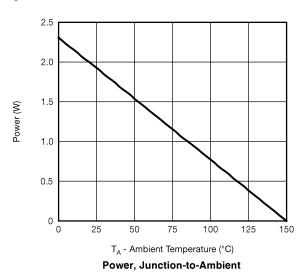
10





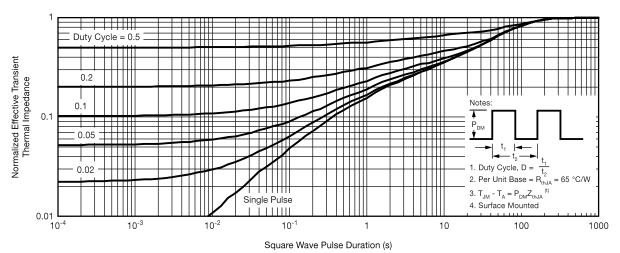
Current Derating*



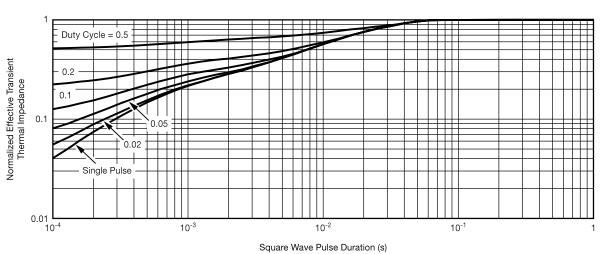


^{*} The power dissipation P_D is based on $T_{J(max)}$ = 150 °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.





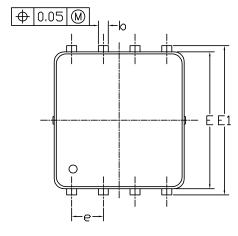
Normalized Thermal Transient Impedance, Junction-to-Ambient

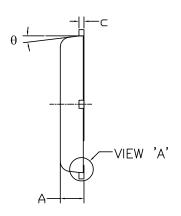


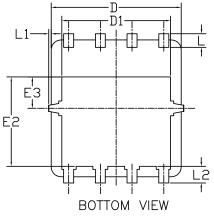
Normalized Thermal Transient Impedance, Junction-to-Case

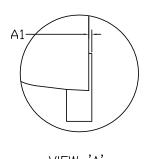


DFN5x6_8L_EP1_P PACKAGE OUTLIN



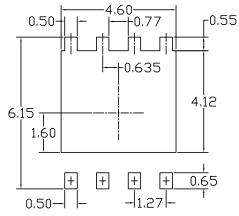






<u>VIEW 'A'</u> (SCALE 5:1)

RECOMMENDED LAND PATTERN



SYMBOLS	DIMENSIONS IN MILLIMETERS			DIMENSIONS IN INCHES			
	MIN	NOM	MAX	MIN	NOM	MAX	
A	0.85	0. 95	1.00	0.033	0.037	0.039	
A1	0.00		0.05	0.000		0.002	
b	0.30	0.40	0.50	0.012	0.016	0.020	
c	0.15	0. 20	0. 25	0.006	0.008	0.010	
D	5. 10	5. 20	5. 30	0. 201	0. 205	0. 209	
D1	4. 25	4. 35	4. 45	0. 167	0. 171	0. 175	
Е	5. 45	5. 55	5. 65	0. 215	0. 219	0. 222	
E1	5. 95	6.05	6. 15	0. 234	0. 238	0. 242	
E2	3. 525	3.625	3. 725	0.139	0. 143	0. 147	
E3	1. 175	1. 275	1. 375	0.046	0.050	0.054	
e	1. 27 BSC			0.050 BSC			
L	0.45	0. 55	0.65	0.018	0.022	0.026	
L1	0		0. 15	0		0.006	
L2	0.68 REF			0.027 REF			
θ	0°		10°	0°		10°	

NOTE

UNIT: mm

- 1. PACKAGE BODY SIZES EXCLUDE MOLD FLASH AND GATE BURRS. MOLD FLASH AT THE NON-LEAD SIDES SHOULD BE LESS THAN 6 MILS EACH.
- 2. CONTROLLING DIMENSION IS MILLIMETER. CONVERTED INCH DIMENSIONS ARE NOT NECESSARILY EXACT.



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