# MSKSEMI 美森科













ESD

TV

TSS

MOV

GDT

PIFD

# MS50N03

**Product specification** 





#### **General Features**

- 30V,50A, RDS(ON) =  $9m\Omega@VGS = 10V$
- Improved dv/ dt capability
- Fast switching
- Green Device Available

## **Application**

- MB / VGA / Vcore
- POL Applications
- SMPS 2<sup>nd</sup> SR

BVDSS	RDSON	ID
30V	7mΩ	50A

## **Reference News**

PACKAGE OUTLINE	Pin Configuration	Marking
MSKS EMICONOLETOR	G S	MSKSEMI 50N03 BN
TO-252	N-Channel MOSFET	



# Absolute Maximum Ratings TA=25°C unless otherwise noted

Symbol	Parameter	Rating	Units
VDS	Drain- Source Voltage	30	V
Vgs	Gate-Source Voltage	±20	V
	Drain Current - Continuous (Tc=250)	50	Α
lD	Drain Current - Continuous (Tc=1000)	30	А
Ірм	Drain Current – Pulsed <sup>1</sup>	200	Α
_	Power Dissipation (Tc=250)	30	W
Po	Power Dissipation – Derate above 25 ℃	0.32	W/°C
Тѕтс	Storage Temperature Range	-55 to 150	°C
TJ	Operating Junction Temperature Range	-55 to 150	°C

#### **Thermal Characteristics**

Symbol	Parameter	Тур.	Max.	Unit
Reja	Thermal Resistance Junction to ambient	-	62	°C/W
Rejc	Thermal Resistance Junction to Case	-	3.1	°C/W



## **Static State Characteristics**

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
BVDSS	Drain- Source Breakdown Voltage	Vgs=0V , ID =250uA	30	-	_	V
ΔBVDSS/Δ TJ	BVpss Temperature Coefficient	Reference to 25 °C, ID=1mA	_	0.04	ı	V/°C
Ipss	Drain-Source Leakage Current	VDS=30V, VGS=0V, TJ=25°C	_	_	1	μA
IDSS	9 -	Vps=24V, Vgs=0V, TJ=125℃	_	_	10	μA
Igss	Gate-Source Leakage Current	Vgs= ±20V, Vps=0V	_	_	±100	μΑ
	0 0 . 0	Vgs=10V , ID=16A	_	7.0	10	mΩ
RDS(ON) Static Drain-Source On-Resistance <sup>3</sup>		Vgs=4.5V , ID=8A	_	9 .0	18	mΩ
VGS(th)	Gate Threshold Voltage	Vgs=Vps . Ip =250uA	1	1.5	2.5	V
ΔVGS(th)	V <sub>GS(th)</sub> Temperature Coefficient	VG3- VD3 , ID -230UA	_	-4	_	mV/℃

# **Dynamic Characteristics**

Qg	Total Gate Charge <sup>3 , 4</sup>		_	7.5	_	
Qgs	Gate-Source Charge <sup>3, 4</sup>	V <sub>DS</sub> =15V , V <sub>GS</sub> =4.5V , I <sub>D</sub> =20A	_	1.3		°C
Qgd	Gate-Drain Charge <sup>3,4</sup>		_	4.5	_	
Td(on)	Turn-On Delay Time <sup>3, 4</sup>		-	4.8	-	
Tr	Rise Time <sup>3 , 4</sup>	VDD=15V, VGS=10V,	-	12.5	_	ns
Td(off)	Turn-Off Delay Time <sup>3, 4</sup>	R <sub>G</sub> =3.3Ω I <sub>D</sub> =15A	-	27.6	-	
Tf	Fall Time <sup>3, 4</sup>	110-0.022 IB-107	-	8.2	_	
Ciss	Input Capacitance		_	1100	_	
Coss	Output Capacitance	V <sub>DS</sub> =25V , V <sub>GS</sub> =0V , F=1MHz	-	150	_	pF
Crss	Reverse Transfer Capacitance		_	110	-	
Rg	Gate resistance	Vgs=0V, Vps=0V, F=1MHz	_	2.7	-	Ω

# **Guaranteed Avalanche Energy**

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
EAS	Single Pulse Avalanche Energy	VDD=25V,L=0.1mH,IAS=15A	12	-	-	mJ

## **Drain-Source Diode Characteristics**

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
ls	Continuous Source Current		-	_	50	Α
lsм	Pulsed Source Current <sup>3</sup>	V <sub>G</sub> =V <sub>D</sub> =0V , Force Current		-	200	Α
VsD	Diode Forward Voltage <sup>3</sup>	Vgs=0V , Is=1A , TJ=25℃	-	-	1.2	V



## **Typical Performance Characteristics**

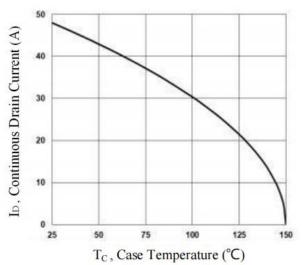


Fig. 1 Continuous Drain Current vs. To

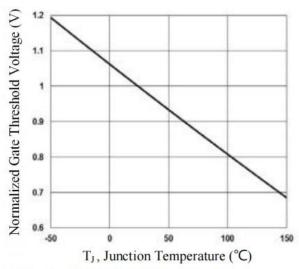


Fig. 3 Normalized V<sub>th</sub> vs. T<sub>J</sub>

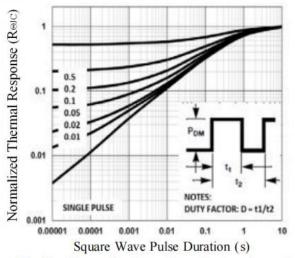


Fig. 5 Normalized Transient Impedance

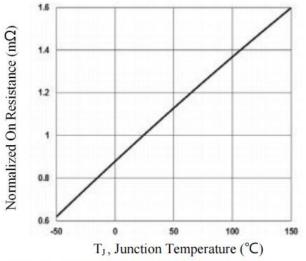


Fig. 2 Normalized RDSON vs. TJ

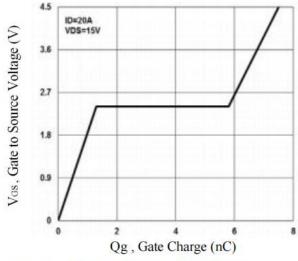


Fig. 4 Gate Charge Waveform

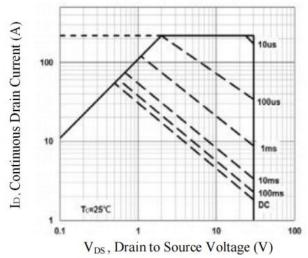


Fig. 6 Maximum Safe Operation Area

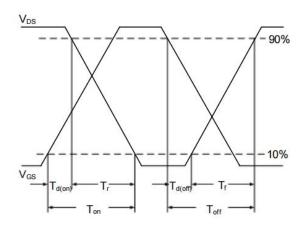
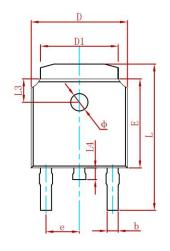
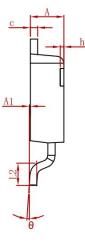


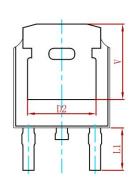
Fig. 7 Switching Time Waveform



## PACKAGE MECHANICAL DATA

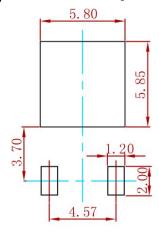






Symbol	Dimensions	In Millimeters	Dimensions	In Inches
Symbol	Min.	Max.	Min.	Max.
Α	2.200	2.400	0.087	0.094
A1	0.000	0.127	0.000	0.005
b	0.635	0.770	0.025	0.030
С	0.460	0.580	0.018	0.023
D	6.500	6.700	0.256	0.264
D1	5.100	5.460	0.201	0.215
D2	4.830	REF.	0.190	REF.
E	6.000	6.200	0.236	0.244
е	2.186	2.386	0.086	0.094
L	9.712	10.312	0.382	0.406
L1	2.900	REF.	0.114	REF.
L2	1.400	1.700	0.055	0.067
L3	1.600	REF.	0.063	REF.
L4	0.600	1.000	0.024	0.039
Ф	1.100	1.300	0.043	0.051
θ	0°	8°	0°	8°
h	0.000	0.300	0.000	0.012
V	5.250	REF.	0.207	REF.

# **Suggested Pad Layout**



#### Note:

- 1. Controlling dimension: in millimeters.
- 2.General tolerance:±0.05mm.
- 3. The pad layout is for reference purposes only

#### **REEL SPECIFICATION**

P/N	PKG	QTY
MS50N03	TO-252	2500





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