



Features

- $V_{DS} = 30V, I_D = 5.8A$
- $R_{DS(ON)} < 31m\Omega @ V_{GS}=10V$
- $R_{DS(ON)} < 43m\Omega @ V_{GS}=4.5V$
- High Power and current handing capability
- Lead free product is acquired
- Surface mount package

Typical Applications

- Load switch
- PWM application

Shipping Quantity

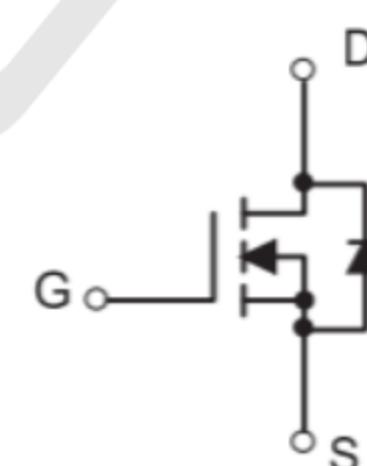
- 3000pcs / Tape & Reel

SOT23



Marking:A49T

Circuit Diagram



N-MOS

Absolute Maximum Ratings ($T_A=25^\circ C$ unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V_{DS}	30	V
Gate-Source Voltage	V_{GS}	± 20	V
Drain Current-Continuous	I_D	5.8	A
Drain Current-Pulsed ^(Note 1)	I_{DM}	20	A
Maximum Power Dissipation	P_D	1.4	W
Operating Junction and Storage Temperature Range	T_J, T_{STG}	-55 To 150	°C

Thermal Characteristic

Thermal Resistance,Junction-to-Ambient ^(Note 2)	$R_{\theta JA}$	89	°C/W
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IRLML0030TRPBF

N-Channel Enhancement Mode MOSFET

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Electrical Characteristics ($T_A=25^\circ\text{C}$ unless otherwise noted)

Parameter	Symbol	Condition	Min	Typ	Max	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	BV_{DSS}	$\text{V}_{\text{GS}}=0\text{V}, \text{I}_D=250\mu\text{A}$	30	33	-	V
Zero Gate Voltage Drain Current	I_{DSS}	$\text{V}_{\text{DS}}=30\text{V}, \text{V}_{\text{GS}}=0\text{V}$	-	-	1	μA
Gate-Body Leakage Current	I_{GSS}	$\text{V}_{\text{GS}}=\pm 20\text{V}, \text{V}_{\text{DS}}=0\text{V}$	-	-	± 100	nA
On Characteristics ^(Note 3)						
Gate Threshold Voltage	$\text{V}_{\text{GS(th)}}$	$\text{V}_{\text{DS}}=\text{V}_{\text{GS}}, \text{I}_D=250\mu\text{A}$	1.2	1.6	2.4	V
Drain-Source On-State Resistance	$\text{R}_{\text{DS(ON)}}$	$\text{V}_{\text{GS}}=10\text{V}, \text{I}_D=5\text{A}$	-	25.5	31	$\text{m}\Omega$
		$\text{V}_{\text{GS}}=4.5\text{V}, \text{I}_D=4\text{A}$	-	36	43	$\text{m}\Omega$
Forward Transconductance	g_{FS}	$\text{V}_{\text{DS}}=5\text{V}, \text{I}_D=5\text{A}$	-	15	-	S
Dynamic Characteristics ^(Note 4)						
Input Capacitance	C_{iss}	$\text{V}_{\text{DS}}=15\text{V}, \text{V}_{\text{GS}}=0\text{V}, \text{F}=1.0\text{MHz}$	-	255	-	PF
Output Capacitance	C_{oss}		-	45	-	PF
Reverse Transfer Capacitance	C_{rss}		-	35	-	PF
Switching Characteristics ^(Note 4)						
Turn-on Delay Time	$t_{\text{d(on)}}$	$\text{V}_{\text{DD}}=15\text{V}, \text{R}_L=3\Omega$ $\text{V}_{\text{GS}}=10\text{V}, \text{R}_{\text{GEN}}=3\Omega$	-	4.5	-	nS
Turn-on Rise Time	t_r		-	2.5	-	nS
Turn-Off Delay Time	$t_{\text{d(off)}}$		-	14.5	-	nS
Turn-Off Fall Time	t_f		-	3.5	-	nS
Total Gate Charge	Q_g	$\text{V}_{\text{DS}}=15\text{V}, \text{I}_D=5\text{A}, \text{V}_{\text{GS}}=10\text{V}$	-	5.2	-	nC
Gate-Source Charge	Q_{gs}		-	0.85	-	nC
Gate-Drain Charge	Q_{gd}		-	1.3	-	nC
Drain-Source Diode Characteristics						
Diode Forward Voltage ^(Note 3)	V_{SD}	$\text{V}_{\text{GS}}=0\text{V}, \text{I}_S=5\text{A}$	-	-	1.2	V
Diode Forward Current ^(Note 2)	I_S		-	-	5	A

Typical Performance Characteristics ($T_A=25^\circ\text{C}$ unless otherwise Specified)

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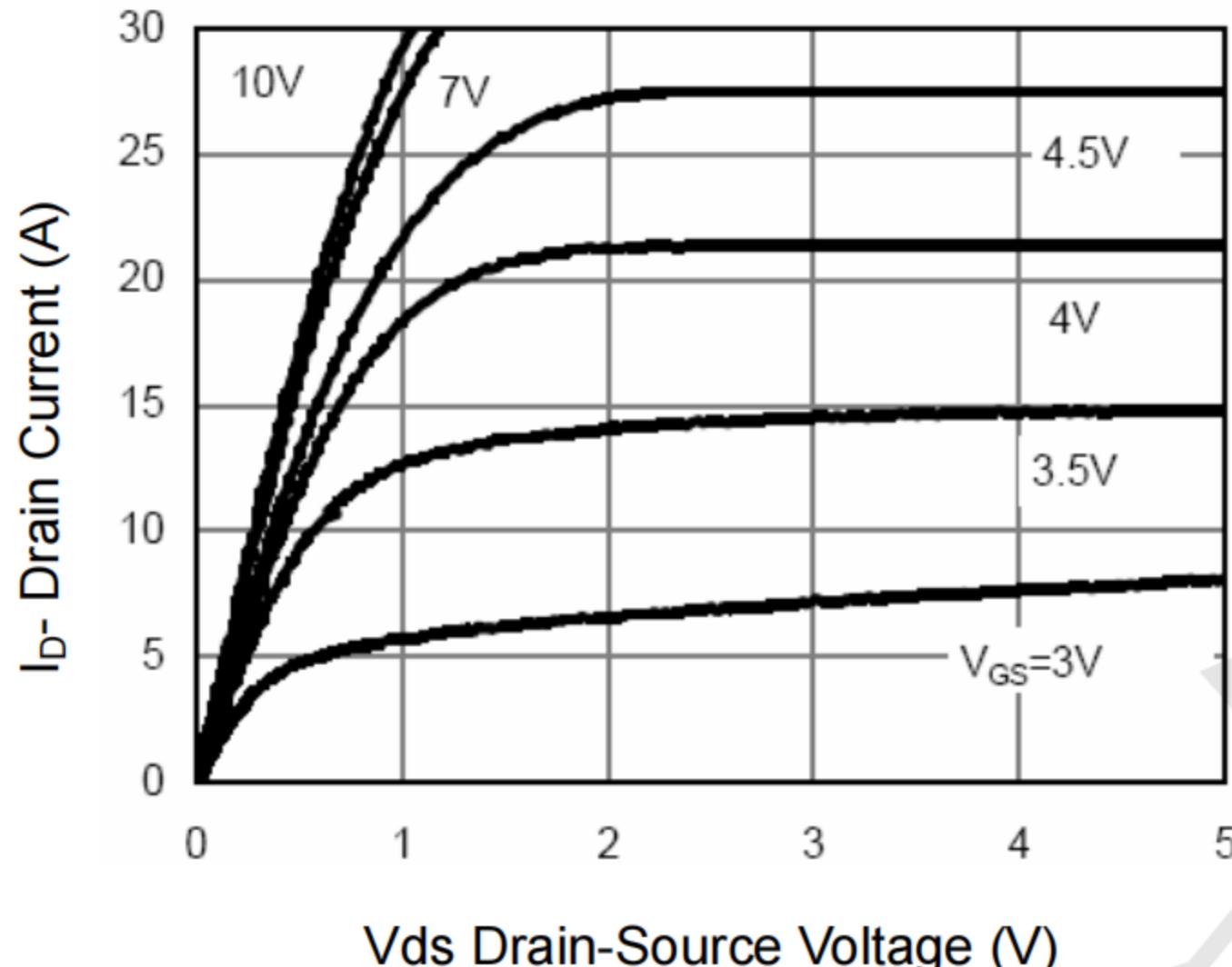


Figure 1 Output Characteristics

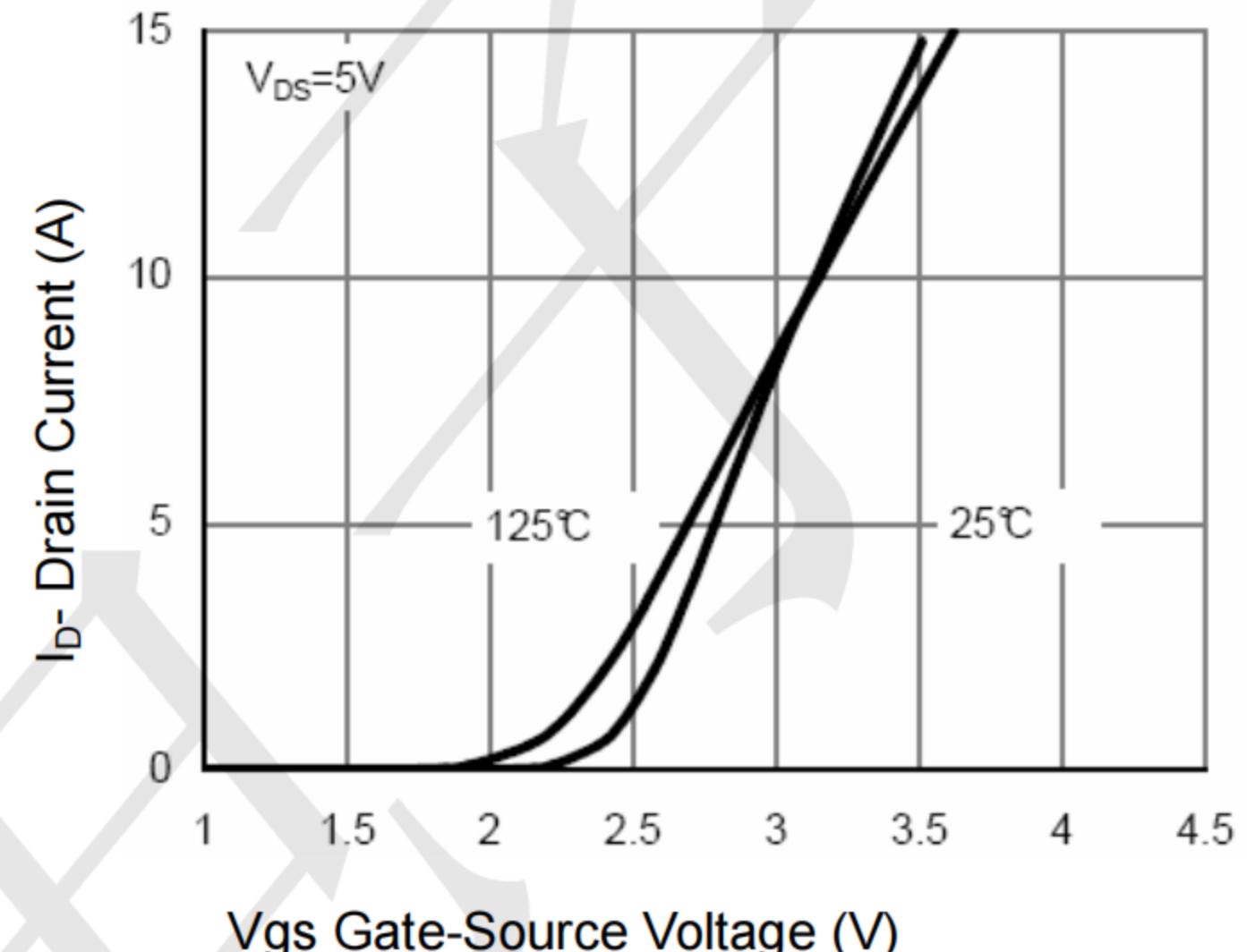


Figure 2 Transfer Characteristics

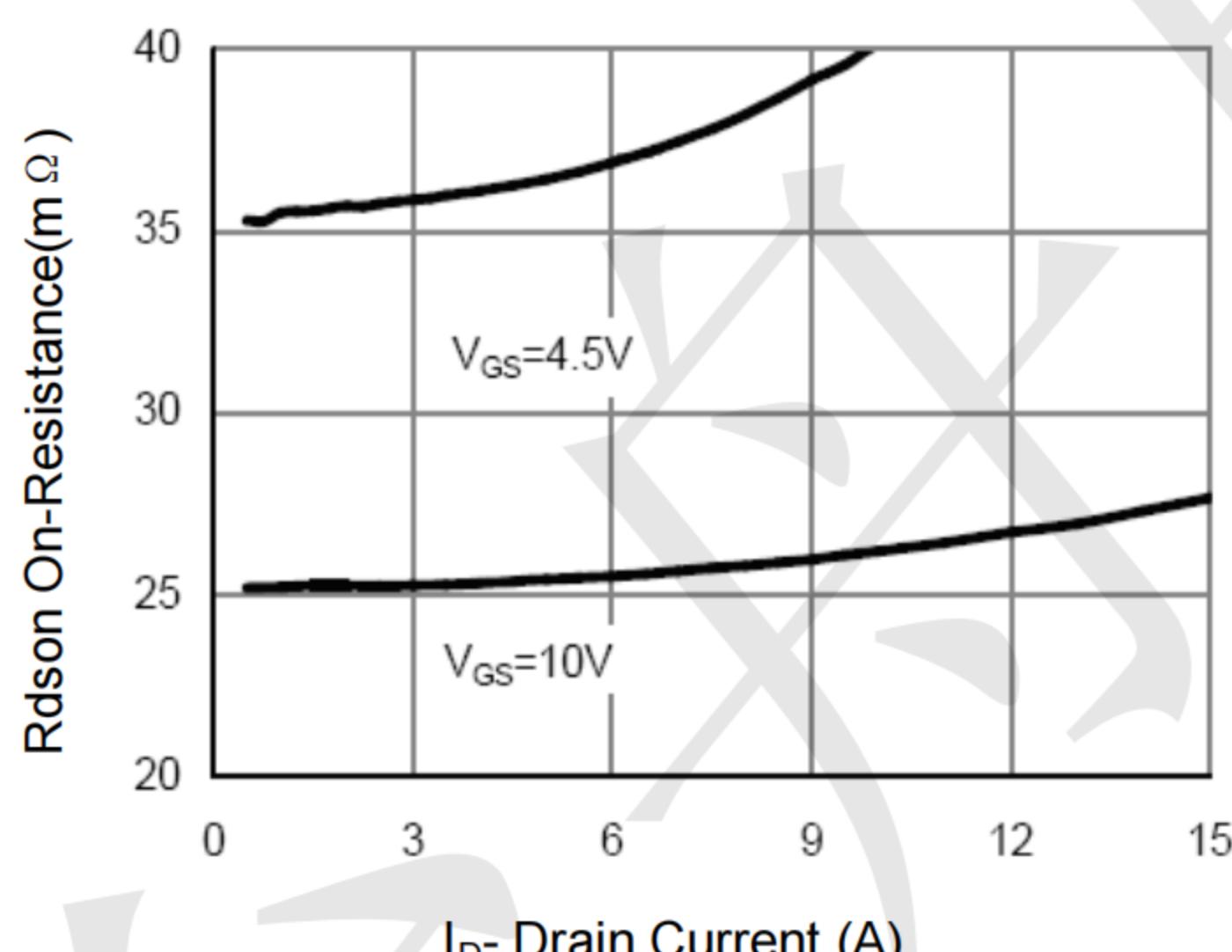


Figure 3 Drain-Source On-Resistance

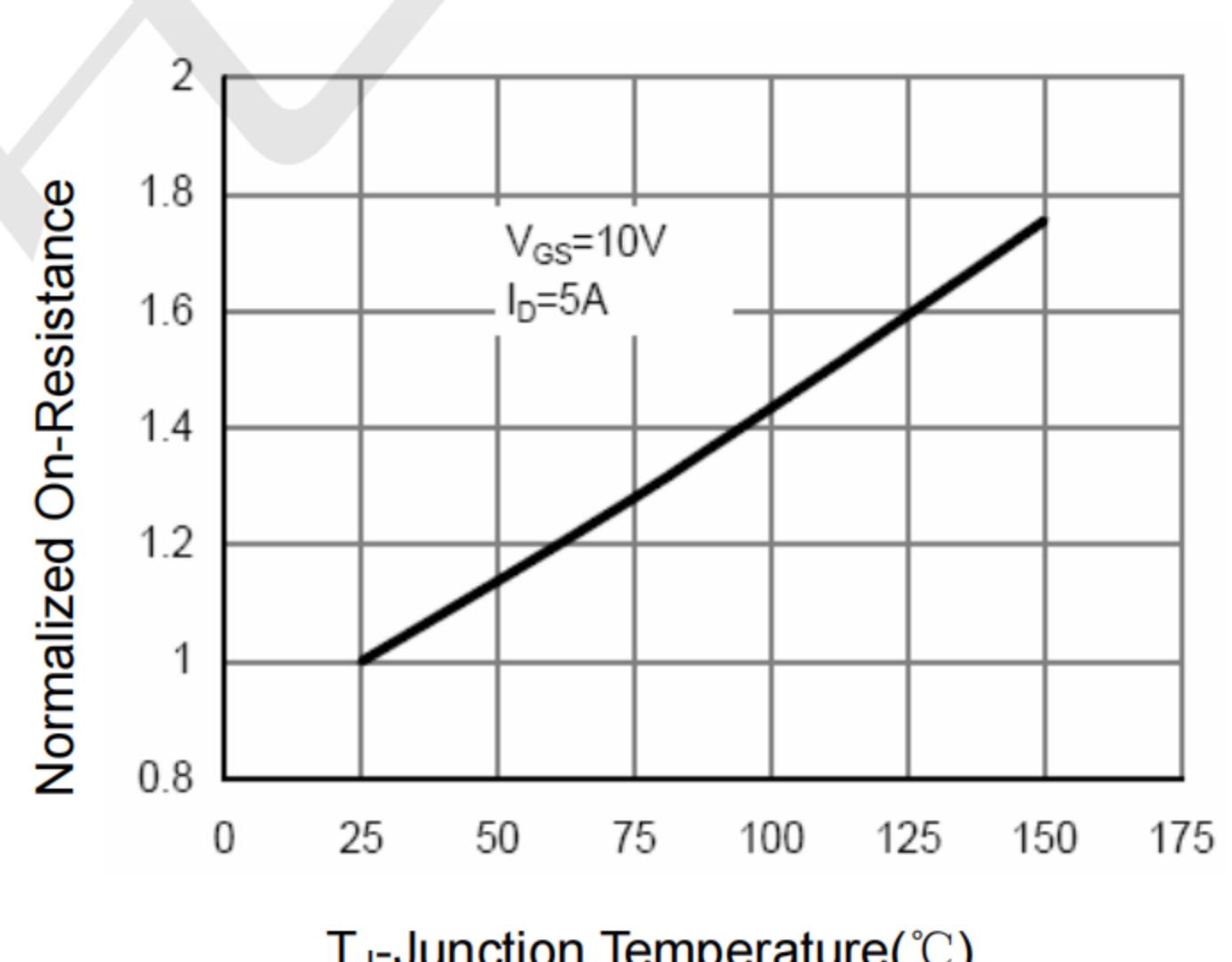


Figure 4 Drain-Source On-Resistance

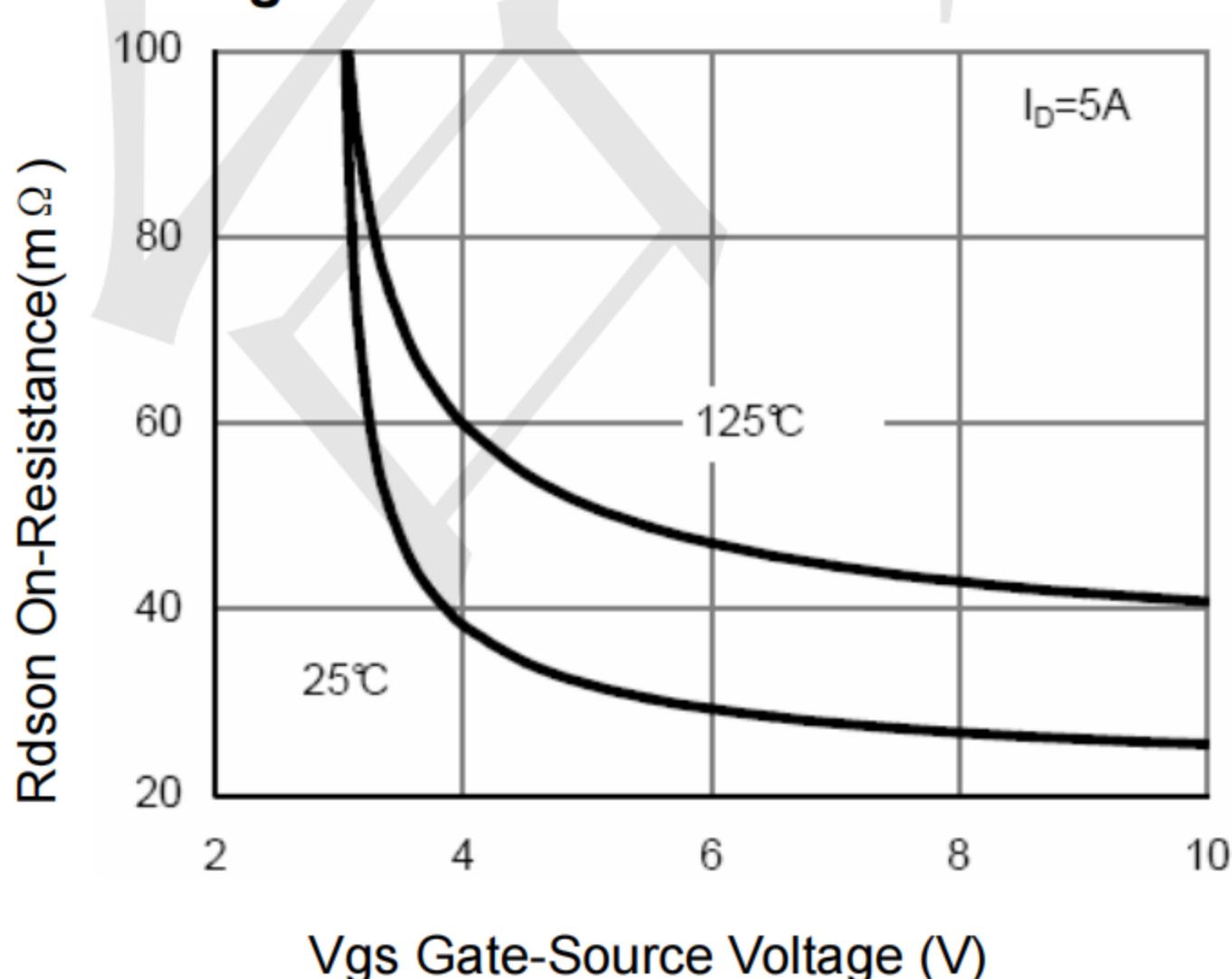


Figure 5 Rdson vs Vgs

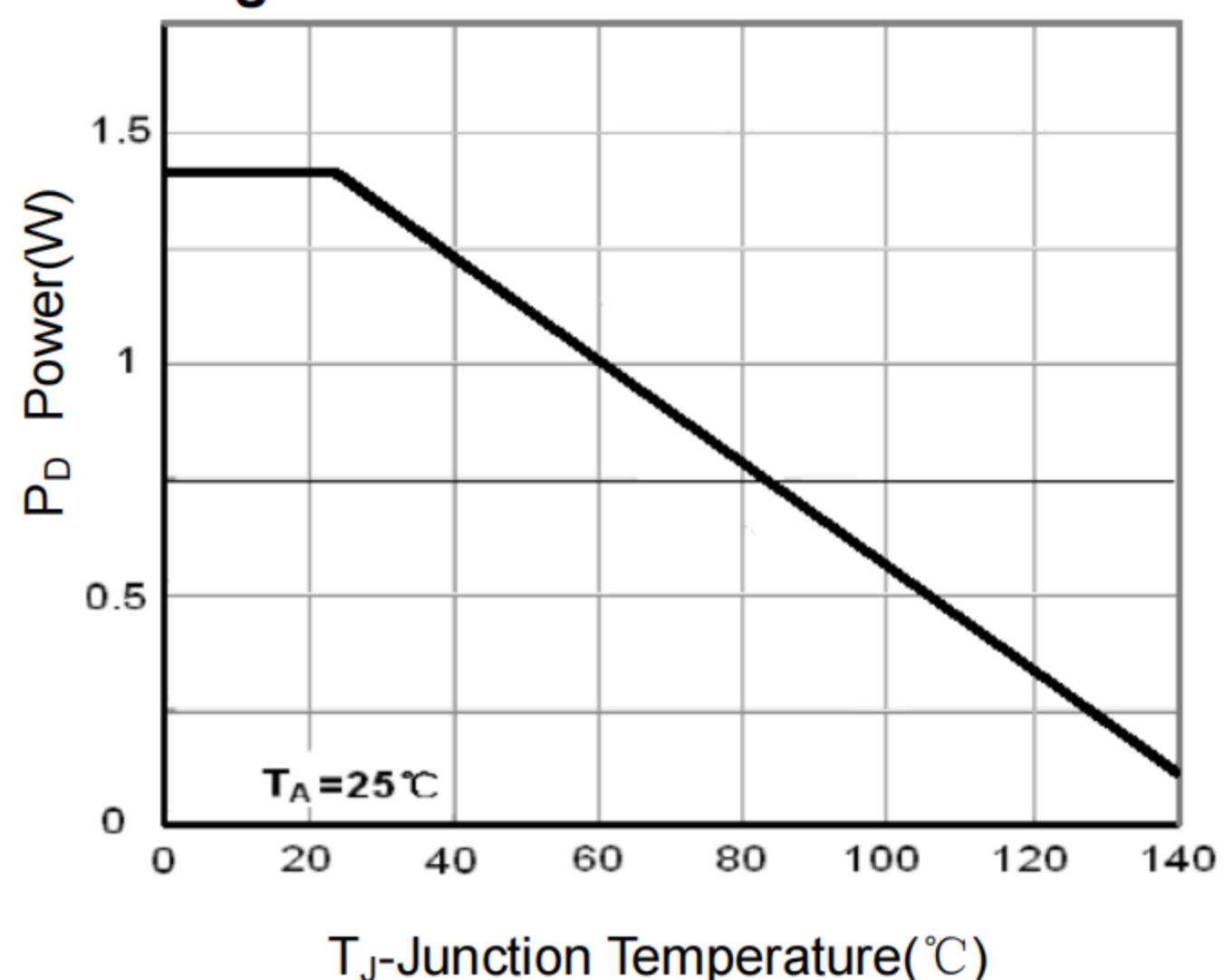


Figure 6 Power Dissipation

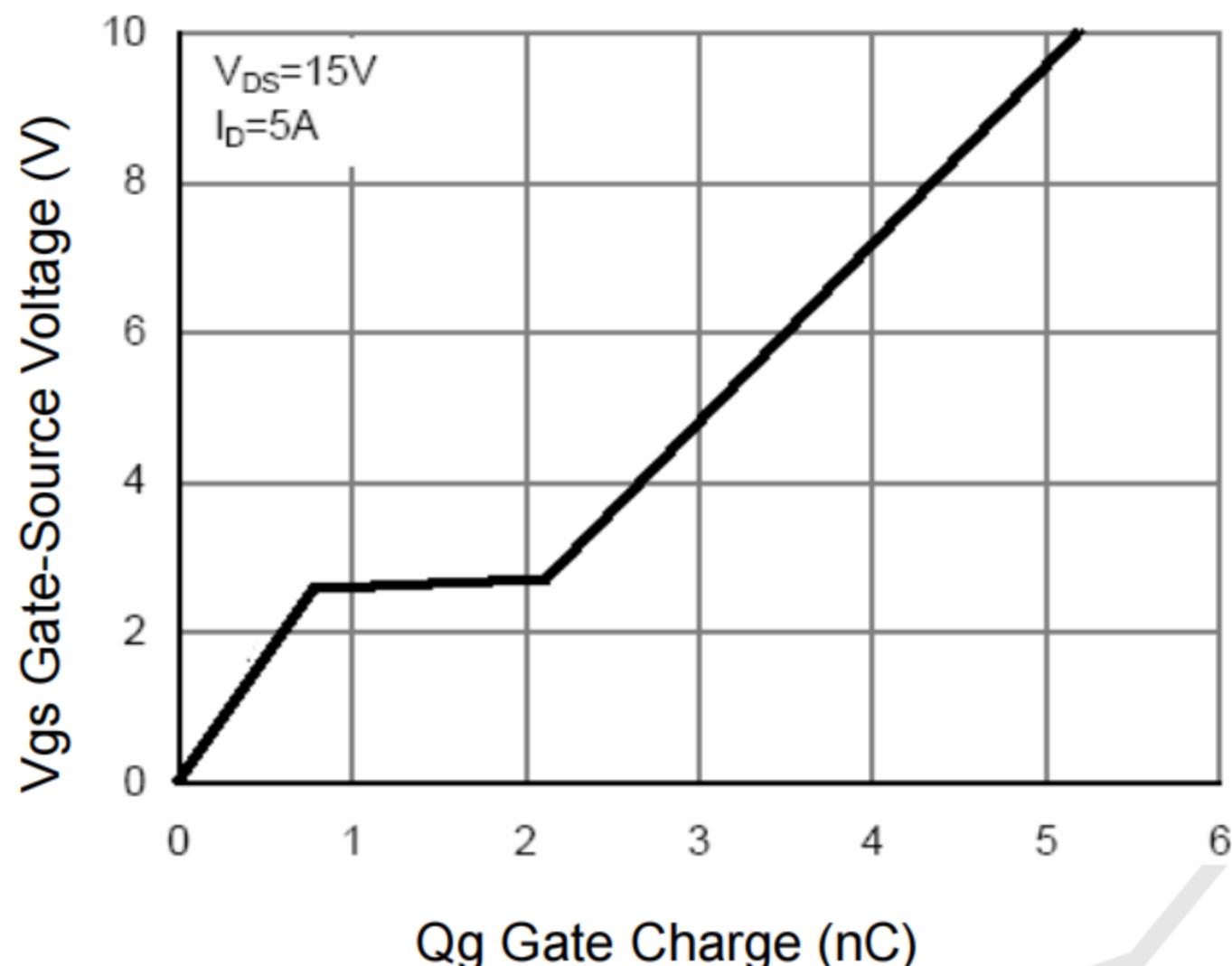


Figure 7 Gate Charge

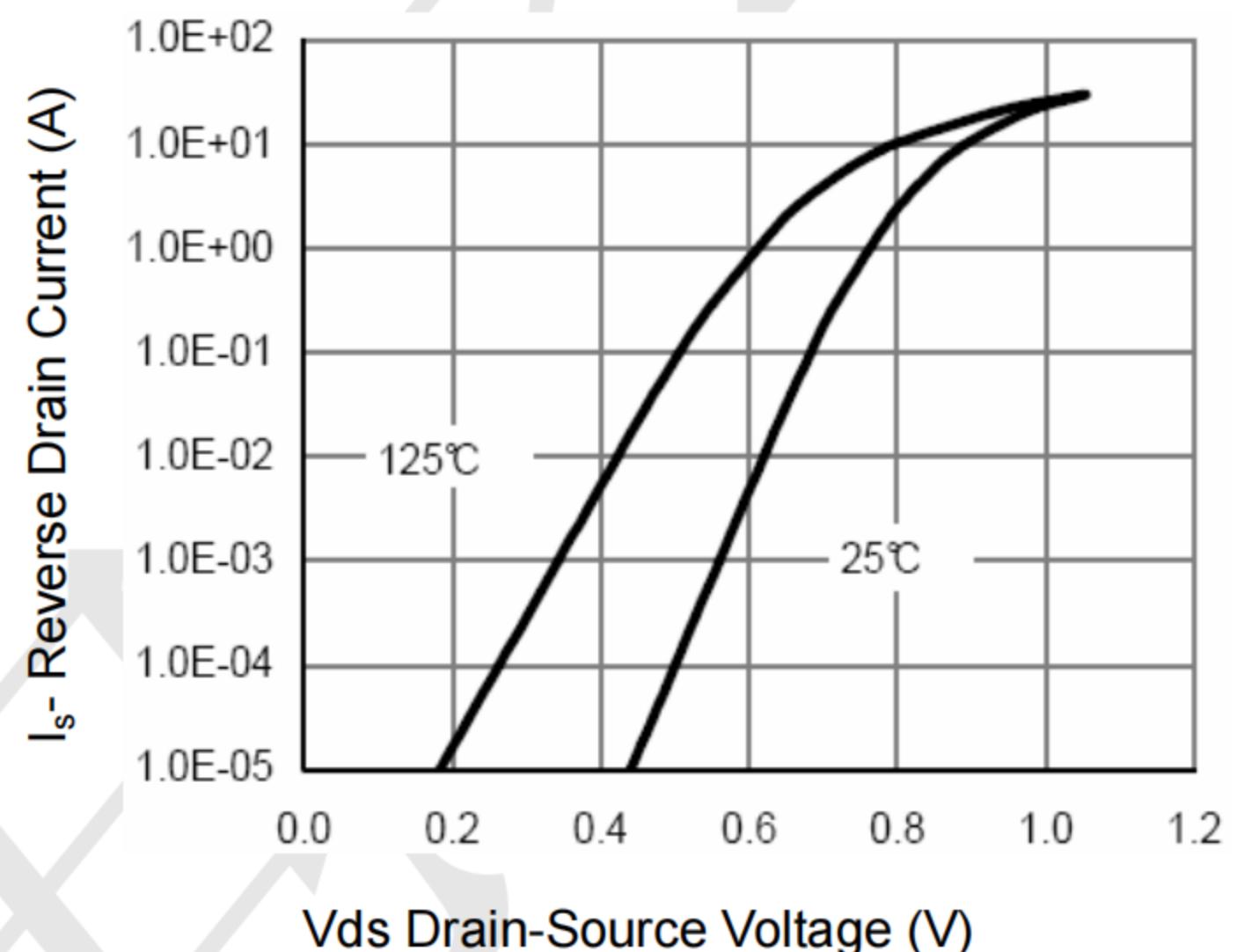


Figure 8 Source- Drain Diode Forward

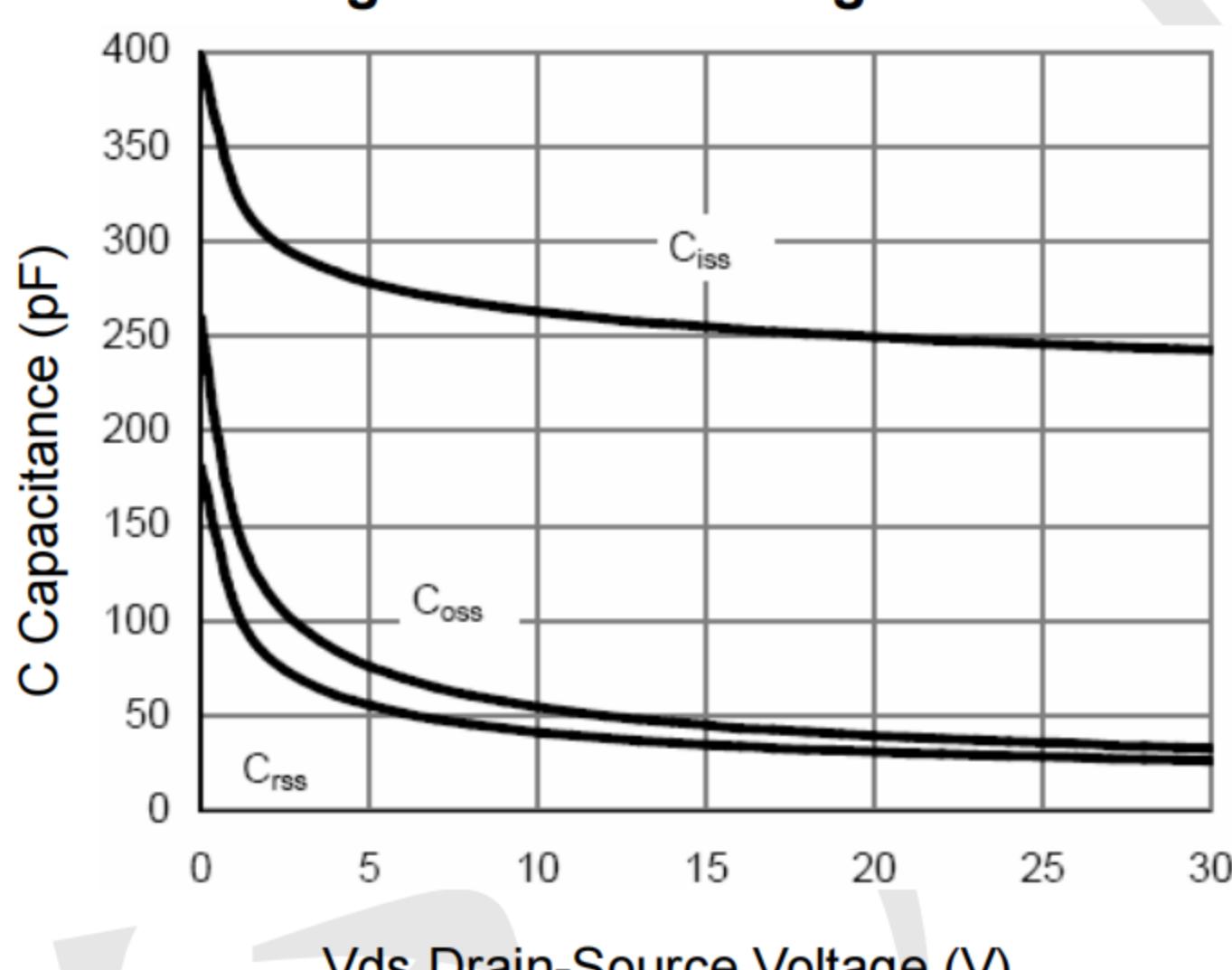


Figure 9 Capacitance vs Vds

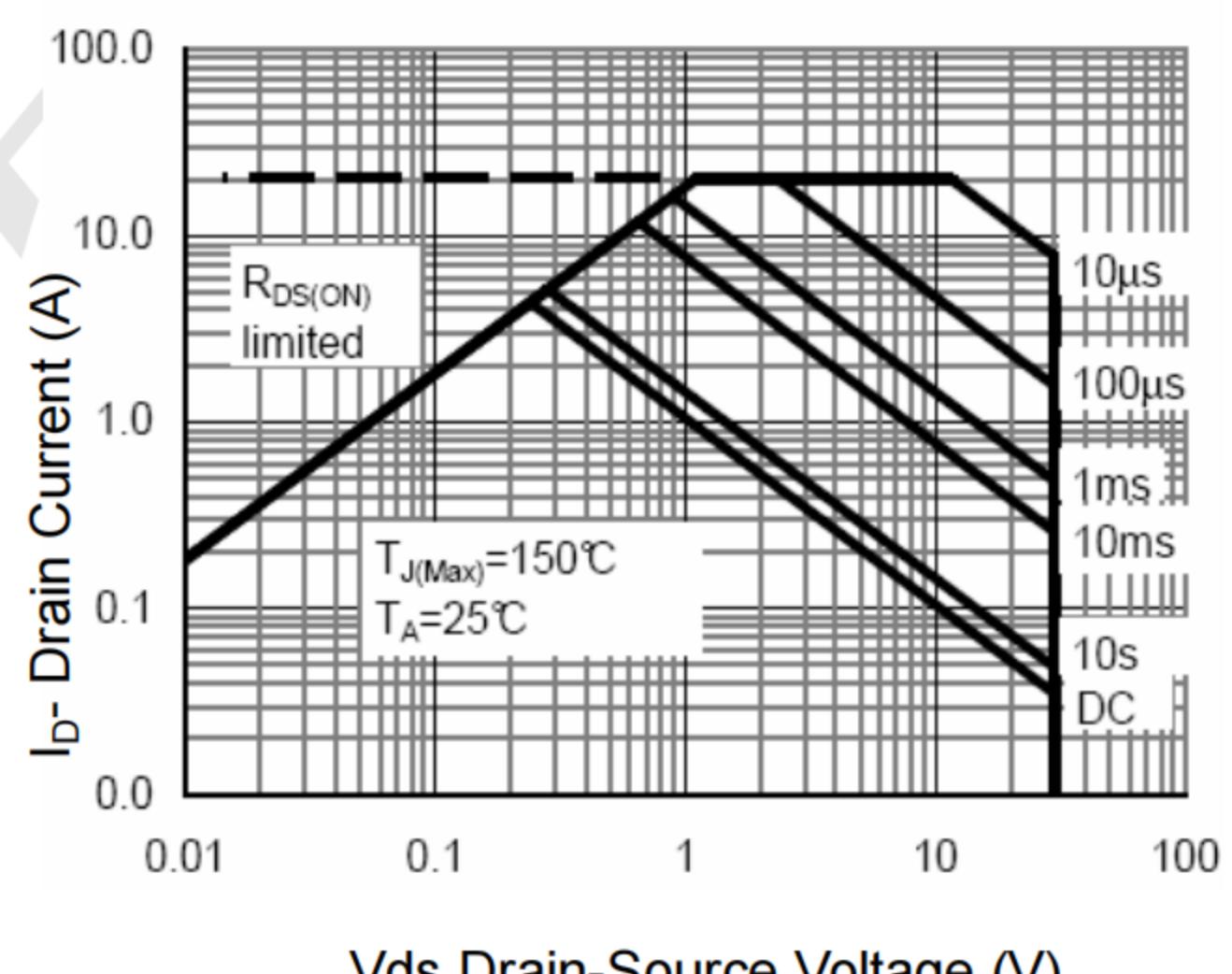


Figure 10 Safe Operation Area

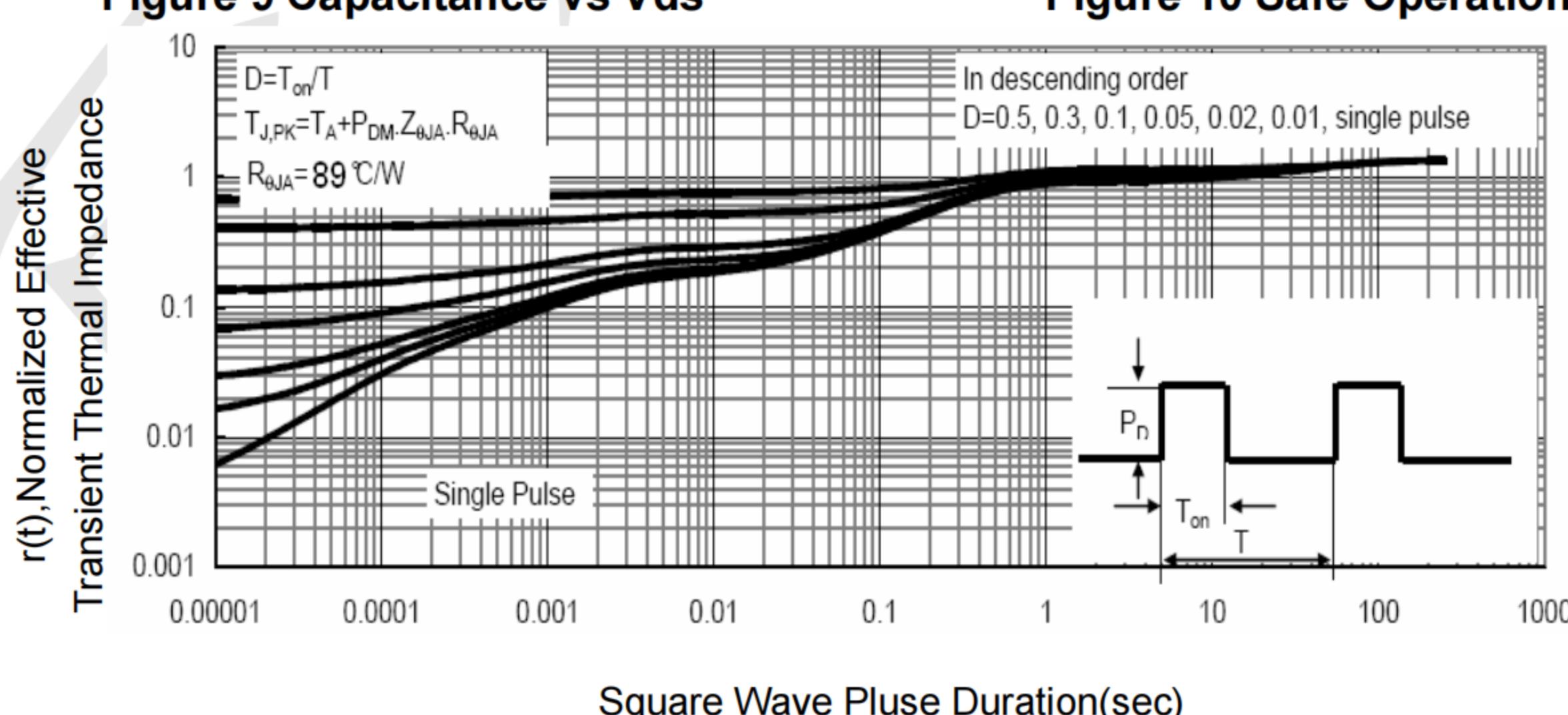
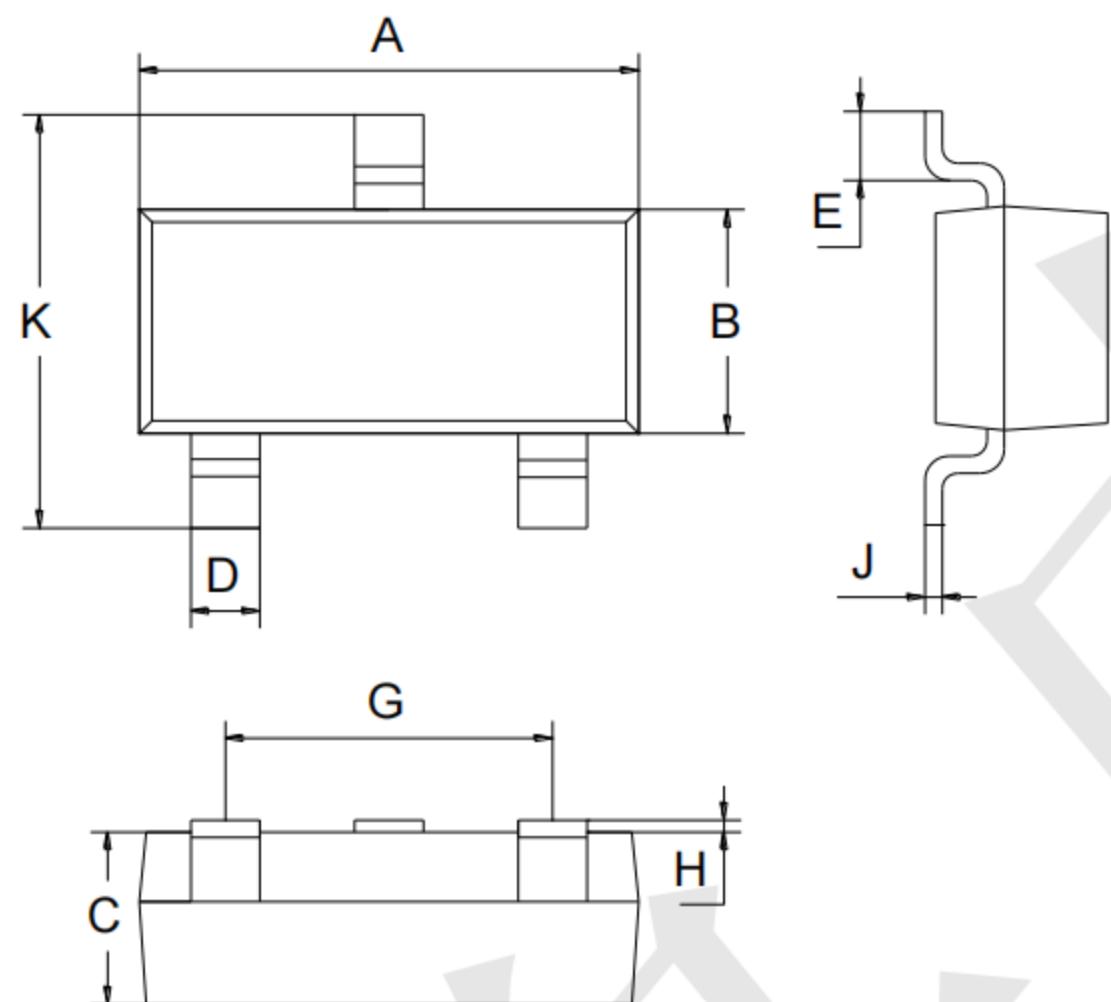


Figure 11 Normalized Maximum Transient Thermal Impedance



Outline Drawing - SOT23



SOT-23		
Dimension	Min.	Max.
A	2.70	3.10
B	1.10	1.50
C	0.90	1.10
D	0.30	0.50
E	0.35	0.48
G	1.80	2.00
H	0.02	0.10
J	0.05	0.15
K	2.20	2.60

Land Pattern - SOT23

