

● General Description

The AGM614MN combines advanced trench MOSFET technology with a low resistance package to provide extremely low $R_{DS(ON)}$.

This device is ideal for load switch and battery protection applications.

● Features

- Advance high cell density Trench technology
- Low $R_{DS(ON)}$ to minimize conductive loss
- Low Gate Charge for fast switching

- Low Thermal resistance

- 100% Avalanche tested

- 100% DVDS tested

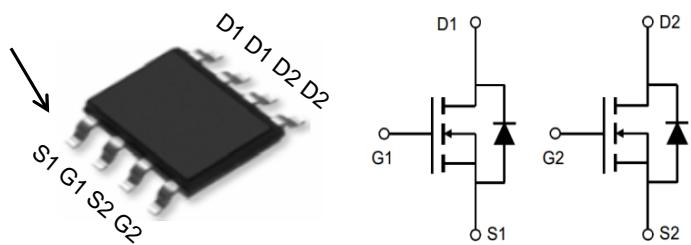
● Application

- Electronic Ballast
- Electronic Transformer
- Switch Mode Power Supply

Product Summary

| BVDSS | RDS(on) | ID |
|-------|---------|-----|
| 60V | 14.5mΩ | 17A |

SOP8 Pin Configuration



Package Marking and Ordering Information

| Device Marking | Device | Device Package | Reel Size | Tape width | Quantity |
|----------------|----------|----------------|-----------|------------|----------|
| AGM614MN | AGM614MN | SOP8 | 330mm | 12mm | 3000 |

Table 1. Absolute Maximum Ratings (TA=25°C)

| Symbol | Parameter | Value | Unit |
|-------------|---|------------|------|
| VDS | Drain-Source Voltage (VGS=0V) | 60 | V |
| VGS | Gate-Source Voltage (VDS=0V) | ± 20 | V |
| ID | Drain Current-Continuous(Tc=25°C) (Note 1) | 17 | A |
| | Drain Current-Continuous(Tc=100°C) | 10 | A |
| IDM (pulse) | Drain Current-Continuous@ Current-Pulsed (Note 2) | 68 | A |
| PD | Maximum Power Dissipation(Tc=25°C) | 6.3 | W |
| | Maximum Power Dissipation(Tc=100°C) | 2.5 | W |
| EAS | Avalanche energy (Note 3) | 23 | mJ |
| TJ,TSTG | Operating Junction and Storage Temperature Range | -55 To 150 | °C |

Table 2. Thermal Characteristic

| Symbol | Parameter | Typ | Max | Unit |
|------------------|---|-----|-----|------|
| R _{θJA} | Thermal Resistance Junction-ambient (Steady State) ¹ | --- | 60 | °C/W |

Table 3. Electrical Characteristics (TJ=25°C unless otherwise noted)

| Symbol | Parameter | Conditions | Min | Typ | Max | Unit |
|---|----------------------------------|---|-----|------|------|------|
| On/Off States | | | | | | |
| BVDSS | Drain-Source Breakdown Voltage | VGS=0V ID=250μA | 60 | -- | -- | V |
| IDSS | Zero Gate Voltage Drain Current | VDS=60V, VGS=0V | -- | -- | 1 | μA |
| IGSS | Gate-Body Leakage Current | VGS=±20V, VDS=0V | -- | -- | ±100 | nA |
| VGS(th) | Gate Threshold Voltage | VDS=VGS, ID=250μA | 1.2 | 1.6 | 2.1 | V |
| gFS | Forward Transconductance | VDS=5V, ID=6A | -- | 5 | -- | S |
| RDS(on) | Drain-Source On-State Resistance | VGS=10V, ID=10A | -- | 14.5 | 17.5 | mΩ |
| | | VGS=4.5V, ID=6A | -- | 20 | 25.5 | mΩ |
| Dynamic Characteristics | | | | | | |
| Ciss | Input Capacitance | VDS=30V, VGS=0V, F=1MHZ | -- | 657 | -- | pF |
| Coss | Output Capacitance | | -- | 227 | -- | pF |
| Crss | Reverse Transfer Capacitance | | -- | 13 | -- | pF |
| Rg | Gate resistance | VGS=0V, Scan F mode | -- | 2.5 | -- | Ω |
| Switching Times | | | | | | |
| td(on) | Turn-on Delay Time | VDD=30V, RD =5Ω, RG = 10Ω VGS=10V | -- | 6 | -- | nS |
| tr | Turn-on Rise Time | | -- | 14 | -- | nS |
| td(off) | Turn-Off Delay Time | | -- | 12 | -- | nS |
| tf | Turn-Off Fall Time | | -- | 3 | -- | nS |
| Qg | Total Gate Charge | VGS=10V, VDD=30V, ID=10A | -- | 13.2 | -- | nC |
| Qgs | Gate-Source Charge | | -- | 1.87 | -- | nC |
| Qgd | Gate-Drain Charge | | -- | 3.33 | -- | nC |
| Source-Drain Diode Characteristics | | | | | | |
| ISD | Source-Drain Current(Body Diode) | | -- | -- | 17 | A |
| VSD | Forward on Voltage | VGS=0V, ISD=6A | -- | -- | 1.2 | V |
| trr | Reverse Recovery Time | VDD=30V, IF=6A , dI/dt=100A/μs , TJ=25°C | -- | -- | -- | ns |
| Qrr | Reverse Recovery Charge | | -- | -- | -- | nc |

Notes 1.The maximum current rating is package limited.

Notes 2.Repetitive Rating: Pulse width limited by maximum junction temperature

Notes 3.EAS condition: TJ=25°C

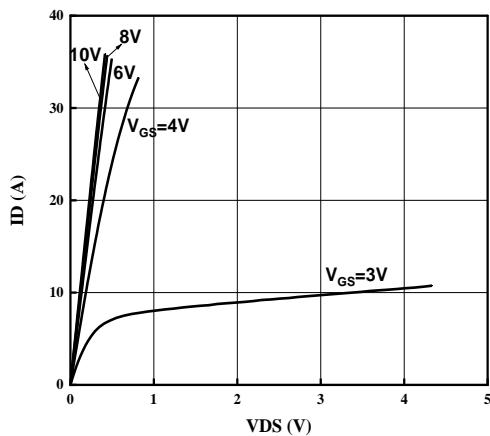


Fig1. Typical Output Characteristics

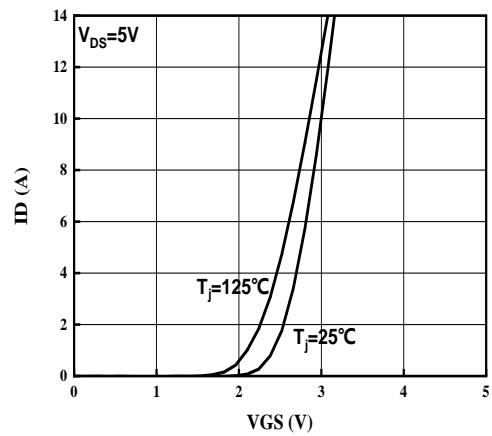


Fig2. Typical Transfer Characteristics

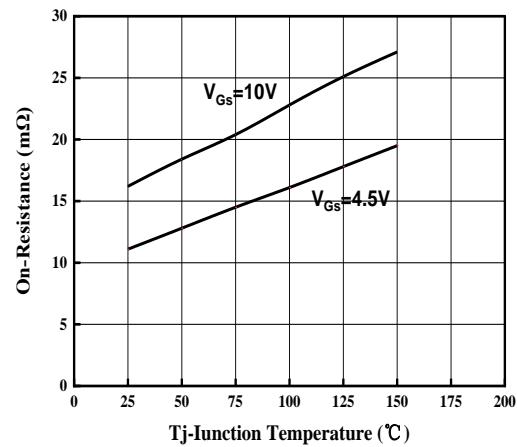


Fig3. On-Resistance Vs. Temperature

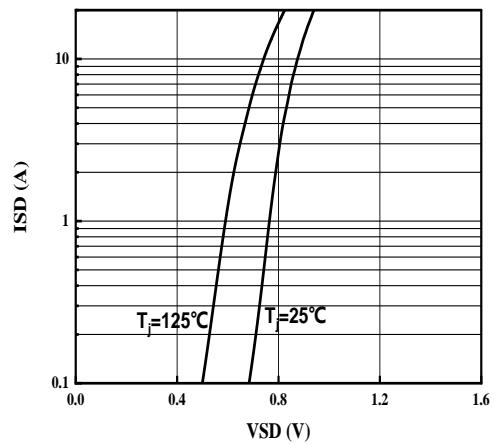


Fig4. Typical Source-Drain Diode Forward Voltage

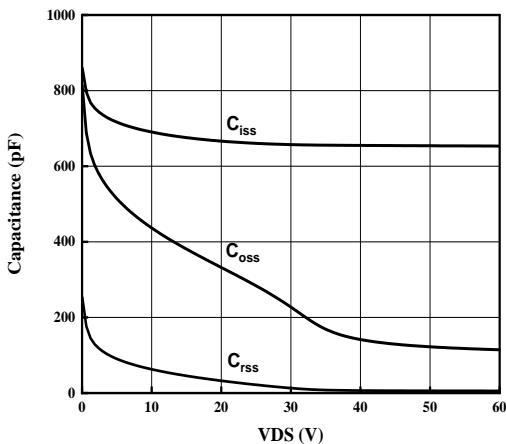


Fig5. Typical Capacitance

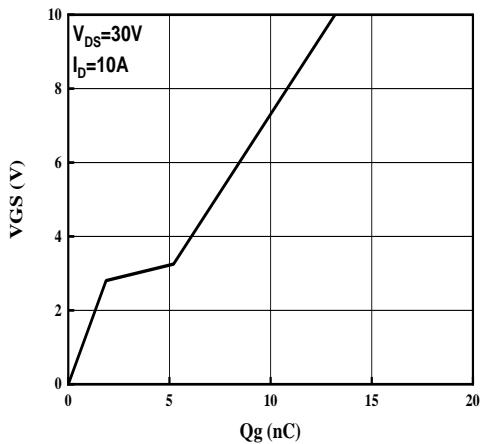


Fig6. Typical Gate Charge

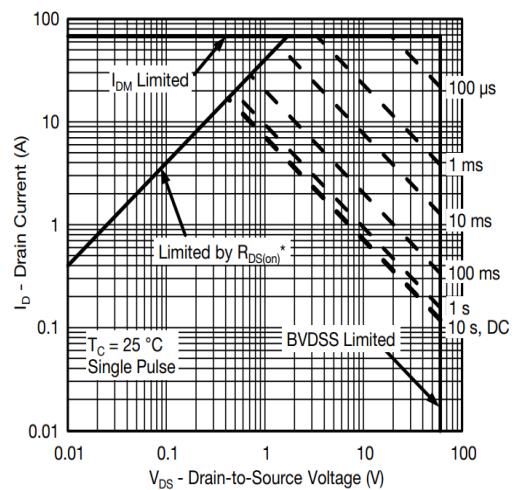


Fig7. Safe Operating Area

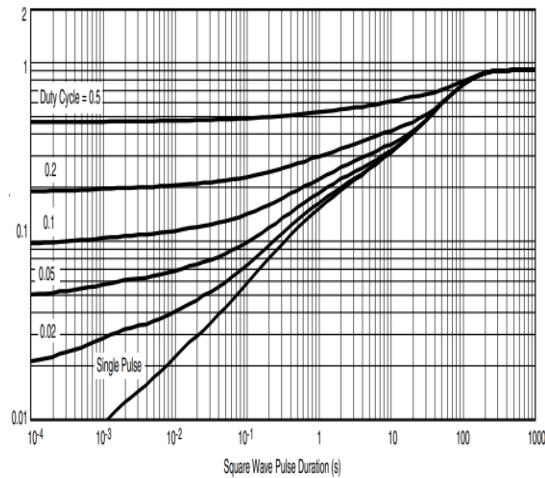
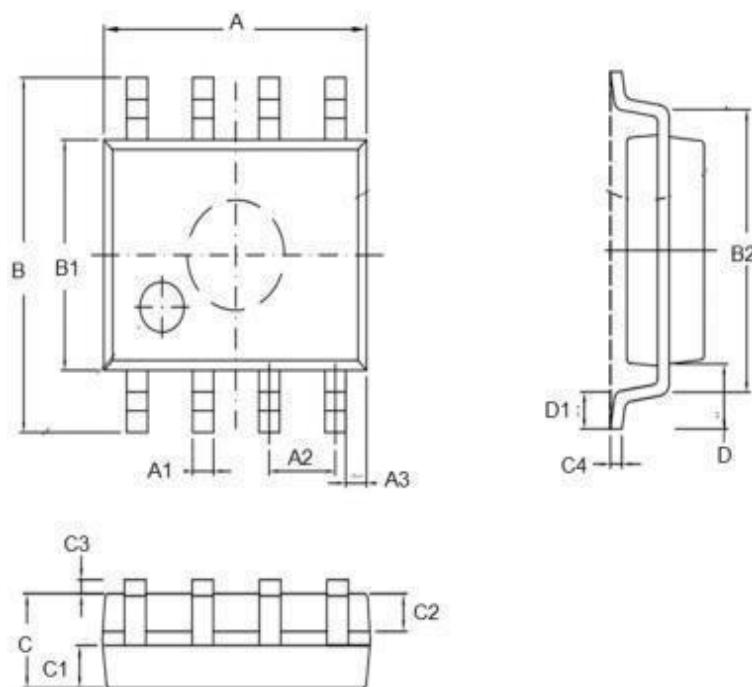


Fig8. Normalized transient thermal impedance

•Dimensions(SOP8)

| SYMBOL | min | TYP | max | SYMBOL | min | | max |
|--------|------|------|------|--------|------|------|------|
| A | 4.80 | | 5.00 | C | 1.30 | | 1.50 |
| A1 | 0.37 | | 0.47 | C1 | 0.55 | | 0.75 |
| A2 | | 1.27 | | C2 | 0.55 | | 0.65 |
| A3 | | 0.41 | | C3 | 0.05 | | 0.20 |
| B | 5.80 | | 6.20 | C4 | 0.19 | 0.20 | 0.23 |
| B1 | 3.80 | | 4.00 | D | | 1.05 | |
| B2 | | 5.00 | | D1 | 0.40 | | 0.62 |



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