

Product Preview

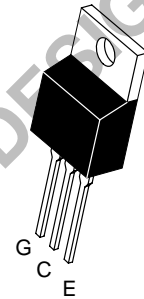
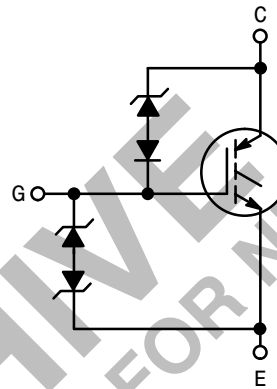
SMARTDISCRETES™
Internally Clamped, N-Channel
IGBT

This Logic Level Insulated Gate Bipolar Transistor (IGBT) features Gate–Emitter ESD protection, Gate–Collector overvoltage protection from SMARTDISCRETES™ monolithic circuitry for usage as an **Ignition Coil Driver**.

- Temperature Compensated Gate–Collector Clamp Limits Stress Applied to Load
- Integrated ESD Diode Protection
- Low Threshold Voltage to Interface Power Loads to Logic or Microprocessors
- Low Saturation Voltage
- High Pulsed Current Capability

MGP20N14CL

20 AMPERES
VOLTAGE CLAMPED
N-CHANNEL IGBT
 $V_{CE(on)} = 1.9$ VOLTS
135 VOLTS (CLAMPED)



CASE 221A-09
STYLE 9
TO-220AB

MAXIMUM RATINGS ($T_J = 25^\circ\text{C}$ unless otherwise noted)

Rating	Symbol	Value	Unit
Collector–Emitter Voltage	V_{CES}	CLAMPED	Vdc
Collector–Gate Voltage	V_{CGR}	CLAMPED	Vdc
Gate–Emitter Voltage	V_{GE}	CLAMPED	Vdc
Collector Current — Continuous	I_C	20	Adc
— Single Pulsed ($t_p = \pm 10 \mu\text{s}$)	I_{CM}	60	Apk
Total Power Dissipation (TO-220) Derate Above 25°C	P_D	150 1.0	Watts W/ $^\circ\text{C}$
Operating and Storage Temperature Range	T_J, T_{stg}	-55 to 175	$^\circ\text{C}$
Single Pulse Collector–Emitter Avalanche Energy @ Starting $T_J = 25^\circ\text{C}$ ($V_{CC} = 80$ V, $V_{GE} = 5$ V, Peak $I_L = 10$ A, $L = 10$ mH)	E_{AS}	500	mJ

THERMAL CHARACTERISTICS

Thermal Resistance — Junction to Case – (TO-220) — Junction to Ambient	$R_{\theta JC}$ $R_{\theta JA}$	1.0 62.5	$^\circ\text{C/W}$
Maximum Lead Temperature for Soldering Purposes, 1/8" from case for 5 seconds	T_L	260	$^\circ\text{C}$
Mounting Torque, 6-32 or M3 screw	10 lbf•in (1.13 N•m)		

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This document contains information on a new product. Specifications and information herein are subject to change without notice.

MGP20N14CL

ELECTRICAL CHARACTERISTICS (T_J = 25°C unless otherwise noted)

Characteristic	Symbol	Min	Typ	Max	Unit
OFF CHARACTERISTICS					
Clamp Voltage (I _{Clamp} = 10 mA, T _J = -40 to 150°C)	V _{(BR)CES}	135			Vdc
Zero Gate Voltage Collector Current (V _{CE} = 100 V, V _{GE} = 0 V) (V _{CE} = 100 V, V _{GE} = 0 V, T _J = 150°C)	I _{CES}	—	—	10 100	μA
Gate–Emitter Clamp Voltage (I _G = 1 mA)	V _{(BR)GES}	10			Vdc
Gate–Emitter Leakage Current (V _{GE} = ±5 V, V _{CE} = 0 V)	I _{GES}	—	—	1.0	μA

ON CHARACTERISTICS (1)

Gate Threshold Voltage (V _{CE} = V _{GE} , I _C = 1 mA) Threshold Temperature Coefficient (Negative)	V _{GE(th)}	1.0	1.5 4.4	2.0	V mV/°C
Collector–Emitter On–Voltage (V _{GE} = 5 V, I _C = 10 A) (V _{GE} = 5 V, I _C = 10 Adc, T _J = 175°C)	V _{CE(on)}	—		1.9 1.8	V
Forward Transconductance (V _{CE} > 15 V, I _C = 10 A)	g _{fe}	8.0	15	—	Mhos

DYNAMIC CHARACTERISTICS

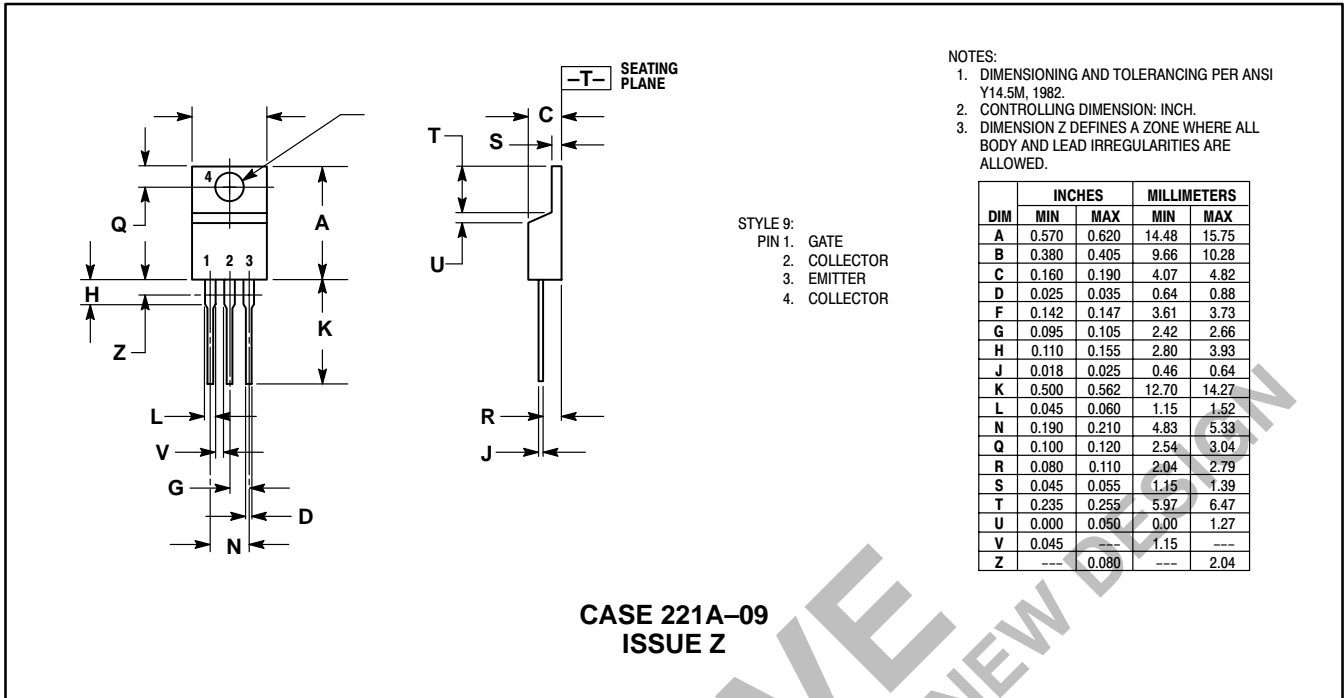
Input Capacitance	(V _{CE} = 25 Vdc, V _{GE} = 0 Vdc, f = 1.0 MHz)	C _{ies}	—	430	600	pF
Output Capacitance		C _{oes}	—	182	250	
Transfer Capacitance		C _{res}	—	48	100	

SWITCHING CHARACTERISTICS (1)

Turn–On Delay Time	(V _{CC} = 68 V, I _C = 20 A, V _{GE} = 5 V, R _G = 9.1 Ω)	t _{d(on)}	—	TBD	TBD	ns
Rise Time		t _r	—	TBD	TBD	
Turn–Off Delay Time		t _{d(off)}	—	TBD	TBD	
Fall Time		t _f	—	TBD	TBD	
Total Gate Charge	(V _{CC} = 108 V, I _C = 20 A, V _{GE} = 5 V)	Q _T	—	14	20	nC
Gate–Emitter Charge		Q _{ge}	—	3.0	—	
Gate–Collector Charge		Q _{gc}	—	6.0	—	

(1) Pulse Test: Pulse Width ≤ 300 μs, Duty Cycle ≤ 2%.


PACKAGE DIMENSIONS



ARCHIVE

DEVICE NOT RECOMMENDED FOR NEW DESIGN

ARCHIVE
RECOMMENDED FOR NEW DESIGN

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