



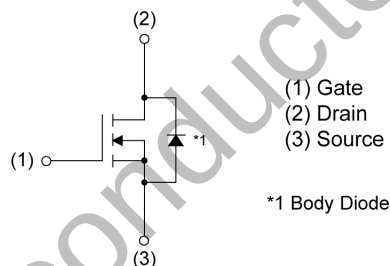
WGA28N65SE

Features:

- ☐ Low Intrinsic Capacitances.
- ☐ Excellent Switching Characteristics.
- ☐ Extended Safe Operating Area.
- ☐ Unrivalled Gate Charge: $Q_g = 120\text{nC}$ (Typ.).
- ☐ $V_{DS} = 650\text{V}, I_D = 28\text{A}$
- ☐ $R_{DS(on)} : 0.32\ \Omega$ (Max) @ $V_G = 10\text{V}$
- ☐ 100% Avalanche Tested



TO-247



Absolute Maximum Ratings $T_C = 25^\circ\text{C}$, unless otherwise noted

Parameter	Symbol	Value	Unit
Drain-Source Voltage ($V_{GS} = 0\text{V}$)	V_{DS}	650	V
Continuous Drain Current	I_D	28	A
Pulsed Drain Current (note1)	I_{DM}	112	A
Gate-Source Voltage	V_{GSS}	± 30	V
Single Pulse Avalanche Energy (note2)	E_{AS}	1350	mJ
Avalanche Current (note1)	I_{AR}	20	A
Repetitive Avalanche Energy (note1)	E_{AR}	90	mJ
Power Dissipation ($T_C = 25^\circ\text{C}$)	P_D	150	W
Operating Junction and Storage Temperature Range	T_J, T_{stg}	-55~+150	$^\circ\text{C}$

Thermal Resistance

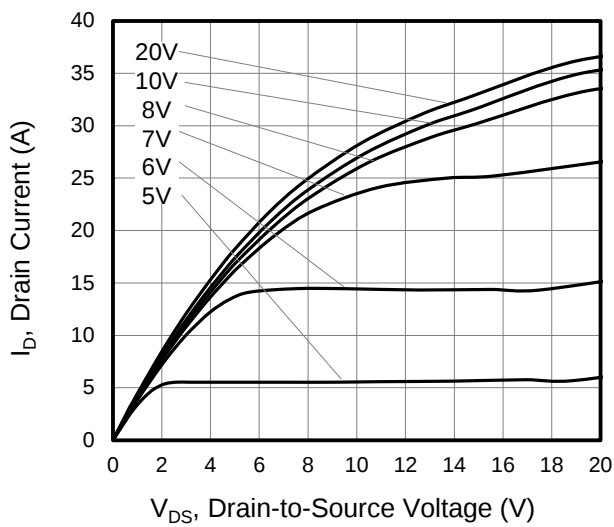
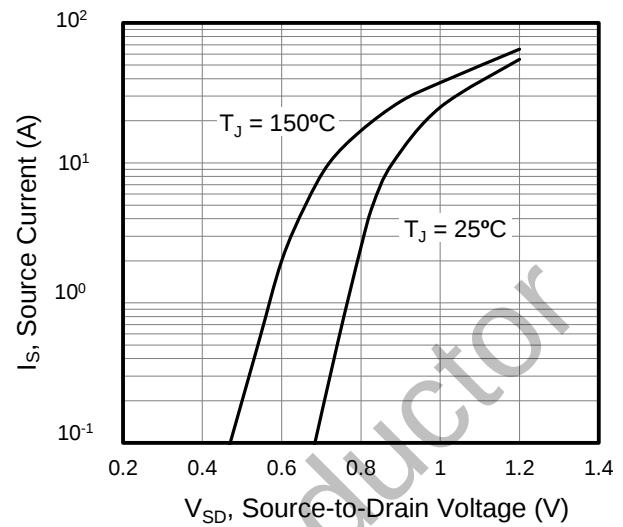
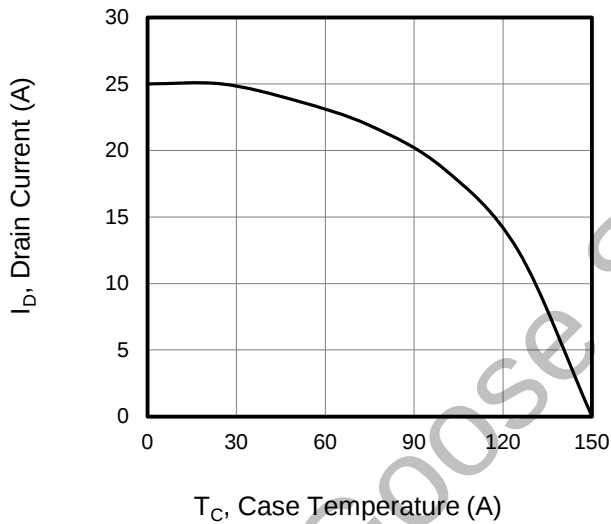
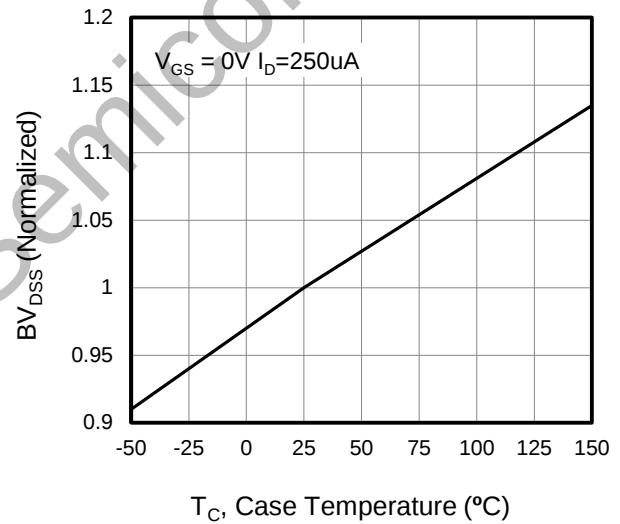
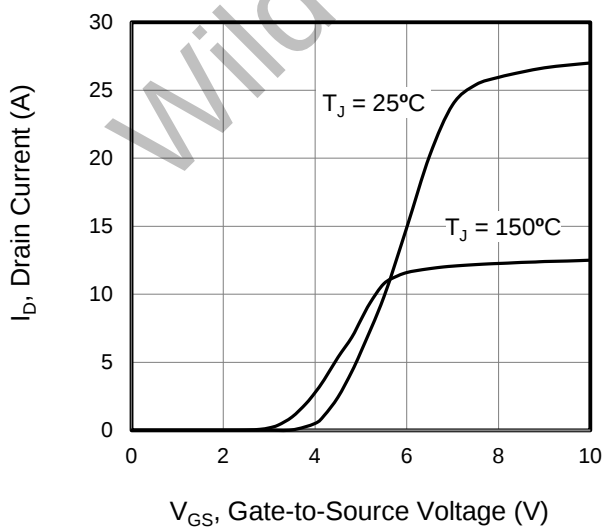
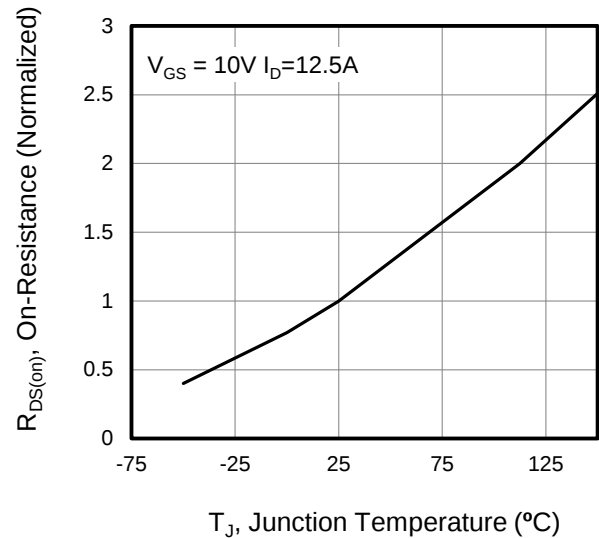
Parameter	Symbol	Value	Unit
Thermal Resistance, Junction-to-Case	R_{thJC}	0.85	$^\circ\text{C/W}$
Thermal Resistance, Junction-to-Ambient	R_{thJA}	62.5	

Specifications $T_J = 25^\circ\text{C}$, unless otherwise noted

Parameter	Symbol	Test Conditions	Value			Unit
			Min.	Typ.	Max.	
Static						
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS} = 0V, I_D = 250\mu A$	650	--	--	V
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS} = 650V, V_{GS} = 0V, T_J = 25^{\circ}C$	--	--	1	μA
Gate-Source Leakage	I_{GSS}	$V_{GS} = \pm 30V$	--	--	± 100	nA
Gate-Source Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = 250\mu A$	3.0	--	4.0	V
Drain-Source On-Resistance (Note3)	$R_{DS(on)}$	$V_{GS} = 10V, I_D = 12.5A$	--	0.29	0.32	Ω
Dynamic						
Input Capacitance	C_{iss}	$V_{GS} = 0V,$ $V_{DS} = 25V,$ $f = 1.0MHz$	--	4550	--	pF
Output Capacitance	C_{oss}		--	440	--	
Reverse Transfer Capacitance	C_{rss}		--	60	--	
Total Gate Charge	Q_g	$V_{DD} = 520V, I_D = 28A,$ $V_{GS} = 10V$	--	120	--	nC
Gate-Source Charge	Q_{gs}		--	18	--	
Gate-Drain Charge	Q_{gd}		--	51	--	
Turn-on Delay Time	$t_{d(on)}$	$V_{DD} = 325V, I_D = 28A,$ $R_G = 25\Omega$	--	40	--	ns
Turn-on Rise Time	t_r		--	70	--	
Turn-off Delay Time	$t_{d(off)}$		--	180	--	
Turn-off Fall Time	t_f		--	90	--	
Drain-Source Body Diode Characteristics						
Continuous Body Diode Current	I_S	$T_C = 25^{\circ}C$	--	--	28	A
Pulsed Diode Forward Current	I_{SM}		--	--	112	
Body Diode Voltage	V_{SD}	$T_J = 25^{\circ}C, I_{SD} = 28A, V_{GS} = 0V$	--	--	1.4	V
Reverse Recovery Time	t_{rr}	$V_{GS} = 0V, I_S = 28A,$ $di_F/dt = 100A/\mu s$	--	480	--	ns
Reverse Recovery Charge	Q_{rr}		--	8	--	μC

Notes

1. Repetitive Rating: Pulse width limited by maximum junction temperature
2. $I_{AS} = 20A, V_{DD} = 50V, R_G = 25\Omega$, Starting $T_J = 25^\circ\text{C}$
3. Pulse Test: Pulse width $\leq 350\mu s$, Duty Cycle $\leq 1\%$

Typical Characteristics $T_J = 25^\circ\text{C}$, unless otherwise noted**Figure 1. Output Characteristics ($T_J = 25^\circ\text{C}$)****Figure 2. Body Diode Forward Voltage****Figure 3. Drain Current vs. Temperature****Figure 4. BV_{DSS} Variation vs. Temperature****Figure 5. Transfer Characteristics****Figure 6. On-Resistance vs. Temperature**

Typical Characteristics $T_J = 25^{\circ}\text{C}$, unless otherwise noted

Figure 7. Capacitance

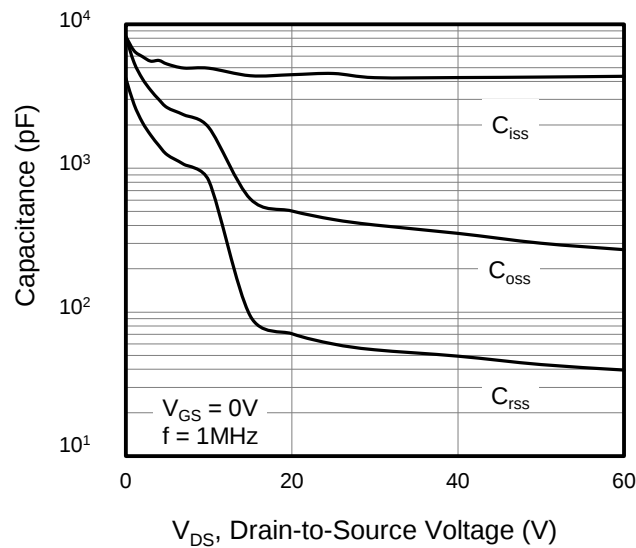


Figure 8. Gate Charge

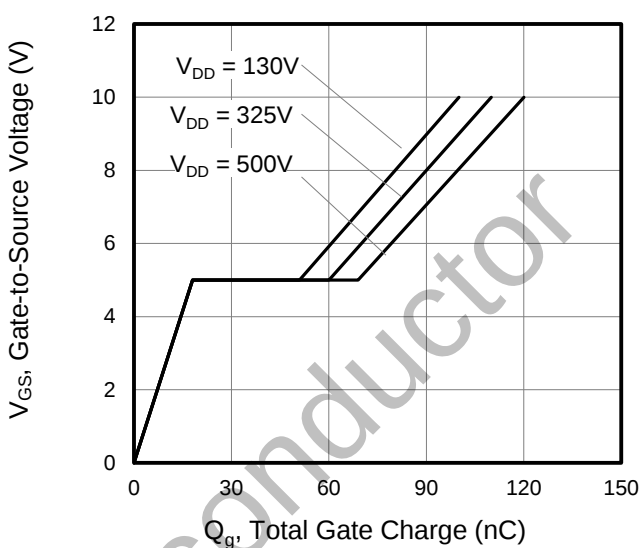


Figure 9. Transient Thermal Impedance

TO-220F

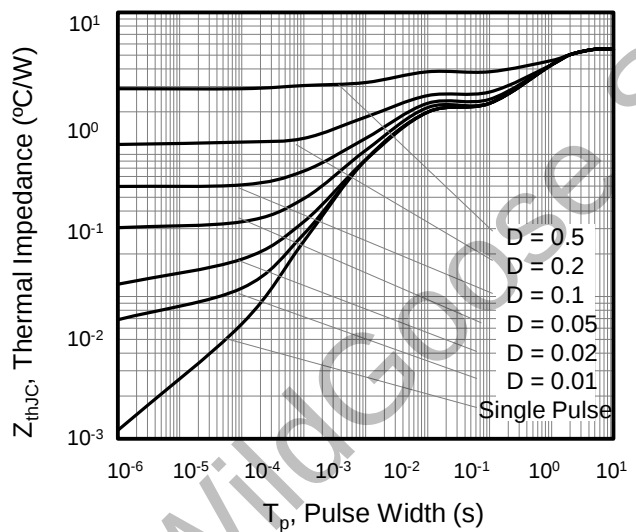


Figure A: Gate Charge Test Circuit and Waveform

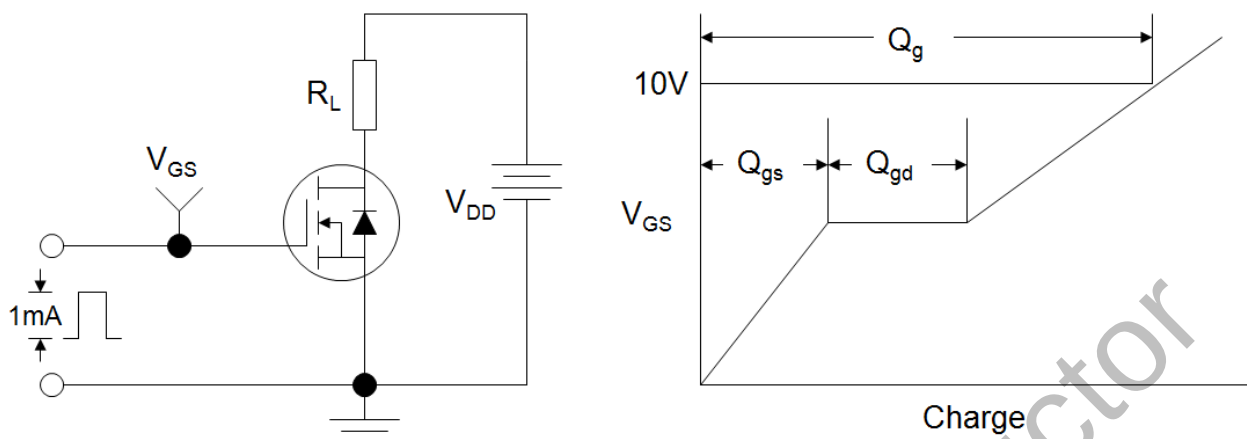


Figure B: Resistive Switching Test Circuit and Waveform

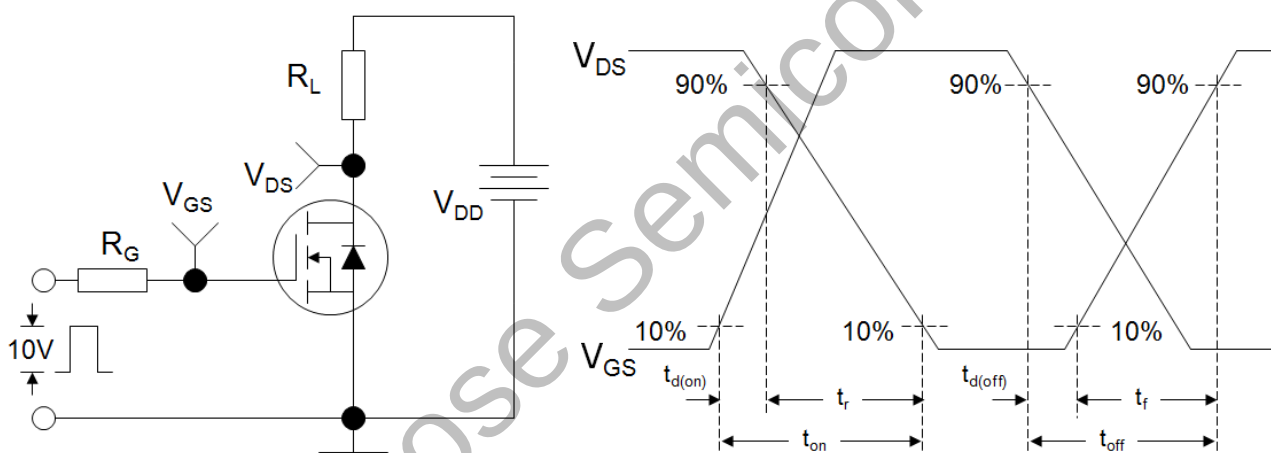
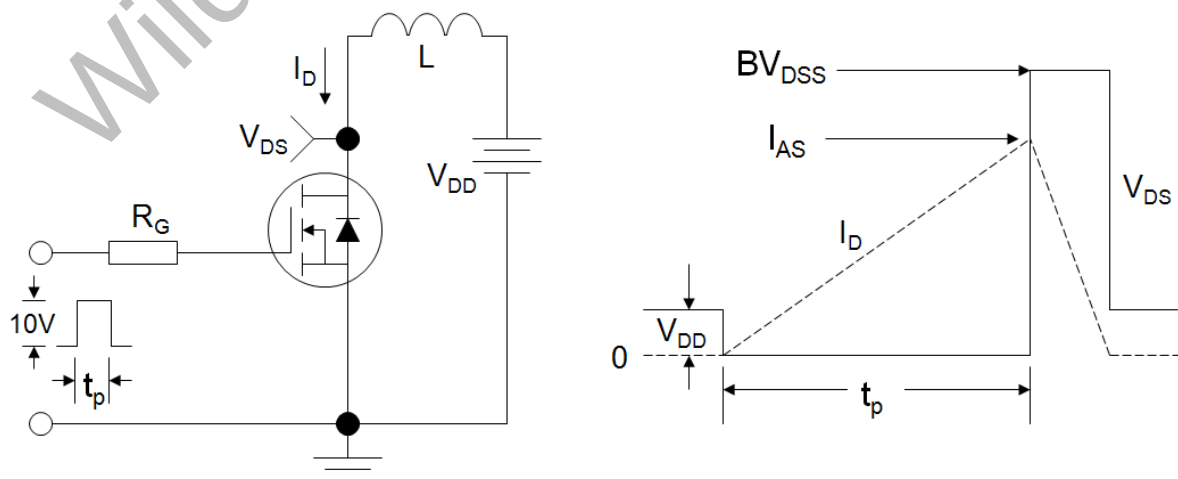


Figure C: Unclamped Inductive Switching Test Circuit and Waveform



Package Dimension**TO-247**