

February 2011

FAN7601B Green Current Mode PWM Controller

Features

- Green Current Mode PWM Control
- Low Operating Current: Maximum 4mA
- Burst Mode Operation
- Internal High-Voltage Startup Switch
- Under-Voltage Lockout (UVLO): 12V/8V
- Latch Protection and Soft-Start Function
- Over-Voltage Protection: 19V
- Operating Frequency up to 300kHz
- Maximum Duty Cycle: 95%

Applications

- Offline Adapter Applications
- Auxiliary Power Supplies

Related Resources

 AN4129 — Green Current Mode PWM Controller FAN7601

Description

The FAN7601B is a programmable frequency green current mode PWM controller. It is specially designed for the offline adapter applications and the auxiliary power supplies that require high efficiency at light load and no load. The internal high-voltage startup switch and burst mode reduce the power loss.

FAN7601B includes protections, such as latch protection and over-voltage protection. The latch protection can be used for over-voltage protection, thermal protection, and others. The soft-start prevents the output voltage overshoot at startup.

Ordering Information

Part Number	Operating Junction Temperature	Top Mark	Package	Packing Method
FAN7601BM		7601B	8-SOP	Rail
FAN7601BMX	-40°C to +85°C	7601B	8-SOP	Tape & Reel
FAN7601BGX		7601B	10-SSOP	Tape & Reel

Block Diagram

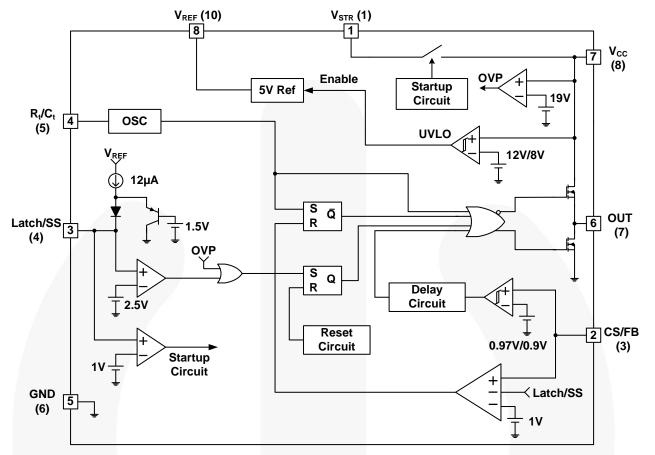


Figure 1. Internal Block Diagram

Note:

1. () is 10-SSOP Pin Number.

Pin Configurations

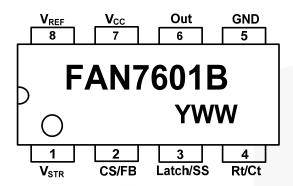


Figure 2. 8-Pin Configuration (Top View)

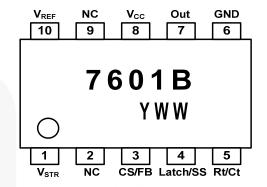


Figure 3. 10-Pin Configuration (Top View)

Pin Definitions

Pin # (8-Pin)	Pin # (10-Pin)	Name	Description
1	1	V_{STR}	Startup
2	3	CS/FB	Current Sense and Feedback
3	4	Latch/SS	Latch Protection and Soft-Start
4	5	Rt/Ct	Oscillator Timing
5	6	GND	Ground
6	7	Out	Gate Drive Output
7	8	V _{CC}	IC Power Supply
8	10	V_{REF}	Voltage Reference

Absolute Maximum Ratings

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only.

Symbol		Min.	Max.	Unit	
V _{CC}	Supply Voltage		20	V	
V _{CS/FB}	Input Voltage CS/FB	-0.3	20.0	V	
T _{STG}	Storage Temperature	-55	+150	°C	
TJ	Recommended Operatin	-40	+150	°C	
Io	Output Current		250	mA	
V_{STR}	V _{STR} Input Voltage		500	V	
ESD	Electrostatic Discharge Capability	Human Body Model, JESD22-A114		2000	V
ESD		Charged Device Model, JESD22-C101		1500	V

Thermal Impedance

Symbol	Parameter		Value	Unit
0	Thermal Desistance, Junction to Ambient	8-SOP	180	۸۸۷ ٥
θ_{JA}	Thermal Resistance, Junction-to-Ambient	10-SSOP	130	°C/W

Electrical Characteristics

 $T_{A}\text{=-}25^{\circ}\text{C}\text{--}125^{\circ}\text{C},\ V_{\text{CC}}\text{=-}14\text{V},\ R_{\text{T}}\text{=-}9.5\text{k}\Omega,\ C_{\text{T}}\text{=-}2.2\text{nF}\ unless\ otherwise\ specified}.$

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
Reference S	Section	·	•	•	•	
V_{REF}	Reference Output Voltage	I _O =1mA	4.85	5.00	5.15	V
ΔV_{REF1}	Line Regulation	V _{CC} =10V~18V		10	20	mV
ΔV_{REF2}	Load Regulation	I _O =1mA ~ 10mA		20	30	mV
Oscillator S	ection					
f _{OSC}	Initial Accuracy		90	100	110	kHz
ST_V	Voltage Stability	V _{CC} =10V~18V		1.0	1.5	%
V_{OSC}	Amplitude	V _{pin4 peak-to-peak}		1.25		V
PWM Section	on					
V _{CS/FB1}	CS/FB Threshold Voltage1		0.9	1.0	1.1	٧
D_{MAX}	Maximum Duty Cycle	T _A =25°C	92	95	98	%
D_{MIN}	Minimum Duty Cycle				0	%
Burst Mode						
$V_{CS/FB2}$	CS/FB Threshold Voltage2 ⁽²⁾		0.77	0.97	1.17	V
V _{CS/FB3}	CS/FB Threshold Voltage3 ⁽²⁾		0.7	0.9	1.1	V
Soft-Start S	ection					
I _{SS}	Soft-Start Current	V _{pin3} =GND	9	12	15	μА
V_{SL}	Soft-Start Limit Voltage ⁽³⁾	I _{SS} =1μA	1.2	1.5	1.8	V
Protection S	Section					
V_{LATCH}	Latch Voltage		2.25	2.50	2.75	V
V_{OVP}	Over-Voltage Protection		18	19	20	V
UVLO Secti	on					
V_{tH}	Start Threshold Voltage		11	12	13	V
V_{tL}	Minimum Operating Voltage		7	8	9	V
Total Curre	nt Section				1	
I _{OP}	Operating Supply Current			3	4	mA
Output Sect	tion					
V_{OL}	Low Output Voltage	T _A =25°C, I _O =100mA		2.0	2.5	V
V_{OH}	High Output Voltage	T _A =25°C, I _O =-100mA	11.5	12.0	14.0	V
t _r	Rising Time ⁽²⁾	$T_A=25$ °C, $C_I=1$ nF		45	150	ns
t _f	Falling Time ⁽²⁾	T _A =25°C, C _I =1nF		35	150	ns
Startup Sec	tion					
I _{str}	V _{STR} Startup Current	V _{STR} =30V, T _A =25°C	0.5	1.0	1.5	mA
Motes:	J	VIII / A				

Notes:

- 2. These parameters, although guaranteed, are not 100% tested in production.
- 3. It is recommended to connect a $1M\Omega$ resistor between the Latch/SS pin and GND to prevent abnormal operation of the latch protection by noise coupling.

Typical Performance Characteristics

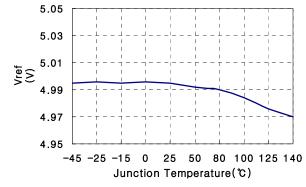


Figure 4. Trimmed Reference Voltage

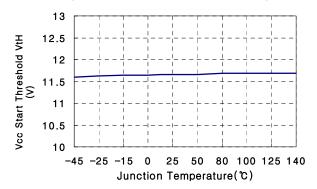


Figure 5. Supply Current

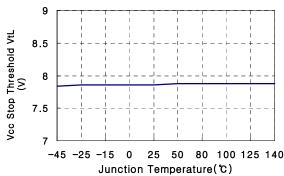


Figure 6. V_{CC} Start Threshold Voltage

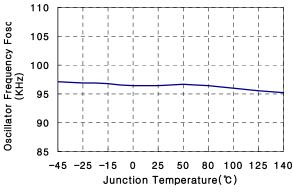


Figure 7. V_{CC} Stop Threshold Voltage

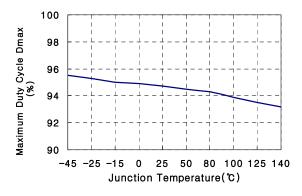


Figure 8. Oscillator Frequency

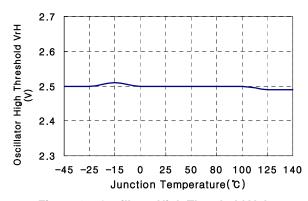


Figure 9. Maximum Duty Cycle

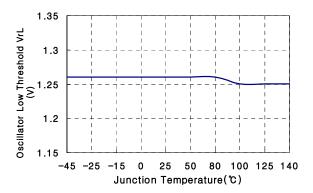


Figure 10. Oscillator High Threshold Voltage

Figure 11. Oscillator Low Threshold Voltage

Typical Performance Characteristics (Continued)

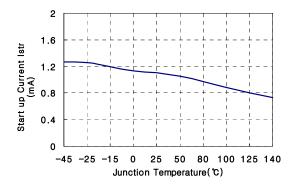


Figure 12. Startup Current

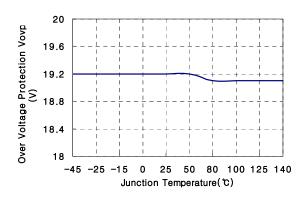


Figure 13. Over-Voltage Protection Level

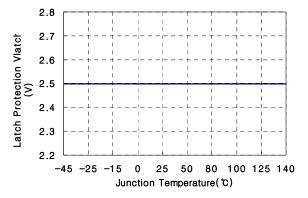


Figure 14. Latch Protection Voltage

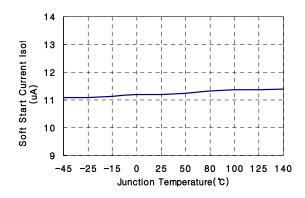


Figure 15. Soft-Start Current

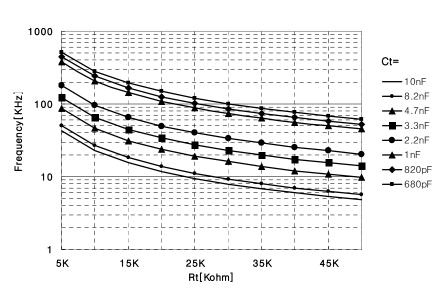
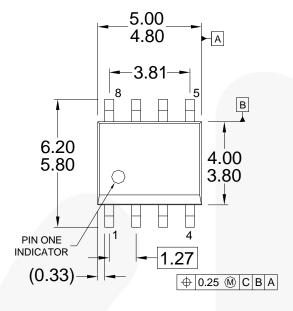
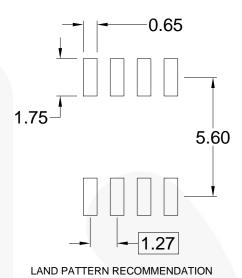
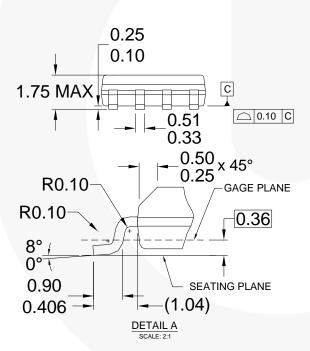


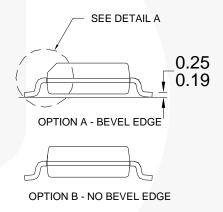
Figure 16. Oscillator Frequency Characteristic

Physical Dimensions









NOTES: UNLESS OTHERWISE SPECIFIED

- A) THIS PACKAGE CONFORMS TO JEDEC
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- C) DIMENSIONS DO NOT INCLUDE MOLD FLASH OR BURRS.
- D) LANDPATTERN STANDARD: SOIC127P600X175-8M.
- E) DRAWING FILENAME: M08AREV13

Figure 17. 8-Lead, Small Outline Package (SOP)

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Physical Dimensions

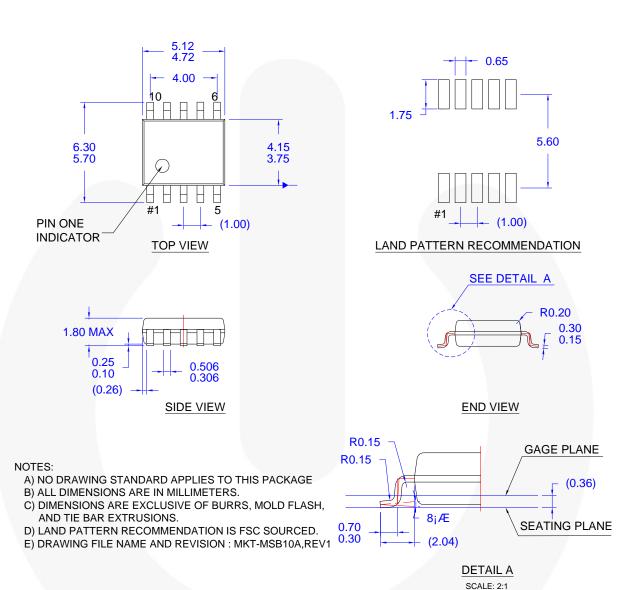


Figure 18. 10-Lead, Small Outline Package (SSOP)

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