June 2009

# FAN6961 Boundary Mode PFC Controller

#### Features

- Boundary Mode PFC Controller
- Low Input Current THD

SEMICONDUCTOR

- Controlled On-Time PWM
- Zero-Current Detection
- Cycle-by-Cycle Current Limiting
- Leading-Edge Blanking instead of RC Filtering
- Low Startup Current: 10µA Typical
- Low Operating Current: 4.5mA Typical
- Feedback Open-Loop Protection
- Programmable Maximum On-Time (MOT)
- Output Over-Voltage Clamping Protection
- Clamped Gate Output Voltage 16.5V

#### Applications

- Electric Lamp Ballasts
- AC-DC Switching Mode Power Converter
- Open Frame Power Supplies and Power Adapters
- Flyback Power Converters with ZCS / ZVS

## Description

The FAN6961 is an 8-pin, boundary-mode, PFC controller IC intended for controlling PFC preregulators. The FAN6961 provides a controlled on-time to regulate the output DC voltage and achieve natural power factor correction. The maximum on-time of the external switch is programmable to ensure safe operation during AC brownouts. An innovative multivector error amplifier is built in to provide rapid transient response and precise output voltage clamping. A built-in circuit disables the controller if the output feedback loop is opened. The startup current is lower than 20µA and the operating current has been reduced to under 6mA. The supply voltage can be up to 25V, maximizing application flexibility.

# **Ordering Information**

Part Number	Operating Temperature Range	Package	Eco Status	Packing Method
FAN6961SZ	-40°C to +125°C	8-Pin, Small Outline Package (SOP)	RoHS	Tape & Reel
FAN6961DZ	-40°C to +125°C	8-Pin, Dual In-line Package (DIP)	P) RoHS Tu	
FAN6961SY	-40°C to +125°C	8-Pin, Small Outline Package (SOP)	Green	Tape & Reel

Ø For Fairchild's definition of Eco Status, please visit: <u>http://www.fairchildsemi.com/company/green/rohs\_green.html</u>.





		divider. This pin is also used for over-voltage clamping and open-loop feedback protection.
2	COMP	<b>Output of the Error Amplifier</b> . To create a precise clamping protection, a compensation network between this pin and GND is suggested.
3	МОТ	<b>Maximum On Time</b> . A resistor from MOT to GND is used to determine the maximum on-time of the external power MOSFET. The maximum output power of the converter is a function of the maximum on time.
4	CS	<b>Current Sense</b> . Input to the over-current protection comparator. When the sensed voltage across the sense resistor reaches the internal threshold (0.8V), the switch is turned off to activate cycle-by-cycle current limiting.
5	ZCD	<b>Zero Current Detection</b> . This pin is connected to an auxiliary winding via a resistor to detect the zero crossing of the switch current. When the zero crossing is detected, a new switching cycle is started. If it is connected to GND, the device is disabled.
6	GND	<b>Ground</b> . The power ground and signal ground. Placing a $0.1\mu$ F decoupling capacitor between VCC and GND is recommended.
7	GATE	<b>Driver Output</b> . Totem-pole driver output to drive the external power MOSFET. The clamped gate output voltage is 16.5V.
8	VCC	Power Supply. Driver and control circuit supply voltage.

## **Absolute Maximum Ratings**

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only. All voltage values, except differential voltage, are given with respect to GND pin.

Symbol	Parameter			Max.	Unit	
V <sub>VCC</sub>	DC Supply Voltage			30	V	
V <sub>HIGH</sub>	Gate Driver		-0.3	30.0	V	
$V_{\text{LOW}}$	Others (INV, COMP, MOT, CS)		-0.3	7.0	V	
$V_{ZCD}$	Input Voltage to ZCD Pin		-0.3	12.0	V	
PD	Power Discipation	SOP		400	mW	
PD	Power Dissipation	DIP		800		
TJ	Operating Junction Temperature	-40	+125	°C		
0	Thermal Desistance (Junction to Air)	SOP		150	00004	
θ <sub>JA</sub>	Thermal Resistance (Junction-to-Air)	DIP		113	°C/W	
T <sub>STG</sub>	Storage Temperature Range		-65	+150	°C	
-		SOP		+230	00	
ΤL	Lead Temperature (Wave Soldering or IR, 10 Seconds)			+260	°C	
ESD	Human Body Model: JESD22-A114			2.5	KV	
ESD	Machine Model: JESD22-A115			200	V	

# **Recommended Operating Conditions**

The Recommended Operating Conditions table defines the conditions for actual device operation. Recommended operating conditions are specified to ensure optimal performance to the datasheet specifications. Fairchild does not recommend exceeding them or designing to Absolute Maximum Ratings.

Symbol	Parameter	Min.	Тур.	Max.	Unit
T <sub>A</sub>	Operating Ambient Temperature	-40		+125	°C

# **Electrical Characteristics**

Unless otherwise noted,  $V_{CC}$ =15V and  $T_{J}$ = -40°C to 125°C. Current is defined as positive into the device and negative out of the device.

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Units
V <sub>CC</sub> Section	on		•	•	•	•
V <sub>CC-OP</sub>	Continuous Operation Voltage				24.5	V
$V_{\text{CC-ON}}$	Turn-On Threshold Voltage		11.5	12.5	13.5	V
V <sub>CC-OFF</sub>	Turn-Off Threshold Voltage		8.5	9.5	10.5	V
I <sub>CC-ST</sub>	Startup Current	$V_{CC}=V_{CC-ON}-0.16V$		10	20	μA
I <sub>CC-OP</sub>	Operating Supply Current	V <sub>CC</sub> =12V, V <sub>CS</sub> =0V, C <sub>L</sub> =3nF, f <sub>SW</sub> =60KHz		4.5	6	mA
$V_{\text{CC-OVP}}$	V <sub>DD</sub> Over-Voltage Protection Level		26.8	27.8	28.8	V
t <sub>D-VCCOVP</sub>	V <sub>DD</sub> Over-Voltage Protection Debounce			30		μs
Error Am	plifier Section					
$V_{REF}$	Reference Voltage		2.475	2.500	2.525	V
Gm	Transconductance			125		µmho
VINVH	Clamp High Feedback Voltage			2.65	2.70	V
VINVL	Clamp Low Feedback Voltage		2.25	2.30		V
Vout ніgh	Output High Voltage		4.8			V
Voz	Zero Duty Cycle Output Voltage		1.15	1.25	1.35	V
VINV-OVP	Over Voltage Protection for INV Input		2.70	2.75	2.80	V
V <sub>INV-UVP</sub>	Under Voltage Protection for INV Input		0.40	0.45	0.50	V
		V <sub>INV</sub> =2.35V, V <sub>COMP</sub> =1.5V	10	20		
ICOMP	Source Current	V <sub>INV</sub> =1.5V,	550	800		μA
	Sink Current	V <sub>INV</sub> =2.65V, V <sub>COMP</sub> =5V	10	20		
Current-S	Sense Section	L	1		1	7
V <sub>PK</sub>	Threshold Voltage for Peak Current Limit Cycle-by-Cycle Limit		0.77	0.82	0.87	v
t <sub>PD</sub>	Propagation Delay				200	ns
		$R_{MOT}=24k\Omega$ , $V_{COMP}=5V$		400	500	
t <sub>LEB</sub>	Leading-Edge Blanking Time	$\begin{array}{l} R_{MOT} = 24k\Omega, \\ V_{COMP} = V_{OZ} + 50mV \end{array}$		270	350	ns
Gate Sect	tion					2
V <sub>Z</sub> -OUT	Output Voltage Maximum (Clamp)	V <sub>CC</sub> =25V	14.5	16.0	17.5	V
V <sub>OL</sub>	Output Voltage Low	V <sub>CC</sub> =15V, I <sub>O</sub> =100mA			1.4	V
V <sub>OH</sub>	Output Voltage High	V <sub>CC</sub> =14V, I <sub>O</sub> =100mA	8			V
t <sub>R</sub>	Rising Time	V <sub>CC</sub> =12V, C <sub>L</sub> =3nF, 20~80%		80		ns
t⊦	Falling Time	V <sub>CC</sub> =12V, C <sub>L</sub> =3nF, 80~20%		40		ns

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# **Electrical Characteristics**

Unless otherwise noted,  $V_{CC}$ =15V and  $T_J$ =-40°C to 125°C. Current is defined as positive into the device and negative out of the device.

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Units
Zero Curi	rent Detection Section		1			
V <sub>ZCD</sub>	Input Threshold Voltage Rising Edge	V <sub>ZCD</sub> Increasing	1.9	2.1	2.3	V
$\begin{array}{c} H_{YS} \text{ of } \\ V_{ZCD} \end{array}$	Threshold Voltage Hysteresis V <sub>ZCD</sub> Decreasing			0.35		V
V <sub>ZCD-HIGH</sub>	Upper Clamp Voltage	I <sub>ZCD</sub> =3mA			12	V
$V_{\text{ZCD-LOW}}$	Lower Clamp Voltage	I <sub>ZCD</sub> =-1.5mA	0.3			V
t <sub>DEAD</sub>	Maximum Delay, ZCD to Output Turn-On	y, ZCD to Output Turn-On V <sub>COMP</sub> =5V, f <sub>SW</sub> =60KHz			400	ns
t <sub>restart</sub>	Restart Time	e Output Turned Off by ZCD		500	700	μs
t <sub>inhib</sub>	Inhibit Time (Maximum Switching Frequency Limit)	R <sub>MOT</sub> =24kΩ		2.8		μs
VDIS	Disable Threshold Voltage		130	200	250	mV
tzcd-dis	Disable Function Debounce Time	$R_{MOT}$ =24k $\Omega$ , V <sub>ZCD</sub> =100mV	800			μs
Maximum	On Time Section					
V <sub>MOT</sub>	Maximum On Time Voltage		1.25	1.30	1.35	V
t <sub>on-max</sub>	Maximum On Time Programming (Resistor Based)	$\begin{array}{l} R_{\text{MOT}} = 24 k \Omega, \ V_{\text{CS}} = 0 V, \\ V_{\text{COMP}} = 5 V \end{array}$		25		μs





#### **Functional Description**

#### **Error Amplifier**

The inverting input of the error amplifier is referenced to INV. The output of the error amplifier is referenced to COMP. The non-inverting input is internally connected to a fixed  $2.5V \pm 2\%$  voltage. The output of the error amplifier is used to determine the on-time of the PWM output and regulate the output voltage. To achieve a low input current THD, the variation of the on time within one input AC cycle should be very small. A multivector error amplifier is built in to provide fast transient response and precise output voltage clamping.

For FAN6961, connecting a capacitance, such as  $1\mu$ F, between COMP and GND is suggested. The error amplifier is a trans-conductance amplifier that converts voltage to current with a 125µmho.

#### **Startup Current**

Typical startup current is less than 20µA. This ultra-low startup current allows the usage of high resistance, low-wattage startup resistor. For example,  $1M\Omega/0.25W$  startup resistor and a  $10\mu$ F/25V (V<sub>CC</sub> hold-up) capacitor are recommended for an AC-to-DC power adaptor with a wide input range 85-265V<sub>AC</sub>.

#### **Operating Current**

Operating current is typically 4.5mA. The low operating current enables a better efficiency and reduces the requirement of  $V_{CC}$  hold-up capacitance.

#### Maximum On-Time Operation

Given a fixed inductor value and maximum output power, the relationship between on-time and line voltage is:

$$t_{on} = \frac{2 \cdot L \cdot P_o}{V_{rms}^2 \cdot \eta} \tag{1}$$

If the line voltage is too low or the inductor value is too high,  $t_{ON}$  is too long. To avoid extra low operating frequency and achieve brownout protection, the maximum value of  $t_{ON}$  is programmable by one resistor,  $R_{I}$ , connected between MOT and GND. A 24k $\Omega$  resistor  $R_{I}$  generates corresponds to 25µs maximum on time:

$$t_{on(\max)} = R_I(k\Omega) \bullet \frac{25}{24}(\mu s)$$
(2)

The range of the maximum on-time is designed as 10  $\sim$  50  $\mu s.$ 

#### **Peak Current Limiting**

The switch current is sensed by one resistor. The signal is feed into CS pin and an input terminal of a comparator. A high voltage in CS pin terminates a switching cycle immediately and cycle-by-cycle current limit is achieved. The designed threshold of the protection point is 0.82V.

#### Leading-Edge Blanking (LEB)

A turn-on spike on CS pin appears when the power MOSFET is switched on. At the beginning of each switching pulse, the current-limit comparator is disabled for around 400ns to avoid premature termination. The gate drive output cannot be switched off during the blanking period. Conventional RC filtering is not necessary, so the propagation delay of current limit protection can be minimized.

#### Under-Voltage Lockout (UVLO)

The turn-on and turn-off threshold voltage is fixed internally at 12V/9.5V. This hysteresis behavior guarantees a one-shot startup with proper startup resistor and hold-up capacitor. With an ultra-low startup current of 20 $\mu$ A, one 1M $\Omega$  R<sub>IN</sub> is sufficient for startup under low input line voltage, 85V<sub>rms</sub>. Power dissipation on R<sub>IN</sub> would be less than 0.1W even under high line (V<sub>AC</sub>=265V<sub>rms</sub>) condition.

#### **Output Driver**

With low on resistance and high current driving capability, the output driver can drive an external capacitive load larger than 3000pF. Cross conduction current has been avoided to minimize heat dissipation, improving efficiency and reliability. This output driver is internally clamped by a 16.5V Zener diode.

#### **Zero-Current Detection (ZCD)**

The zero-current detection of the inductor is achieved using its auxiliary winding. When the stored energy of the inductor is fully released to output, the voltage on ZCD goes down and a new switching cycle is enabled after a ZCD trigger. The power MOSFET is always turned on with zero inductor current such that turn-on loss and noise can be minimized. The converter works in boundary-mode and peak inductor current is always exactly twice of the average current. A natural power factor correction function is achieved with the lowbandwidth, on-time modulation. An inherent maximum off time is built in to ensure proper start-up operation. This ZCD pin can be used as a synchronous input.

#### **Noise Immunity**

Noise on the current sense or control signal can cause significant pulse-width jitter, particularly in the boundary-mode operation. Slope compensation and built-in debounce circuit can alleviate this problem. Because the FAN6961 has a single ground pin, high sink current at the output cannot be returned separately. Good high-frequency or RF layout practices should be followed. Avoiding long PCB traces and component leads, locating compensation and filter components near to the FAN6961, and increasing the power MOSFET gate resistance improve performance.







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