

VN2222LL

Preferred Device

Small Signal MOSFET 150 mA, 60 Volts

N-Channel TO-92

Features

- Pb-Free Packages are Available*



ON Semiconductor®

<http://onsemi.com>

150 mA, 60 V

R_{D(on)} = 7.5 Ω

MAXIMUM RATINGS

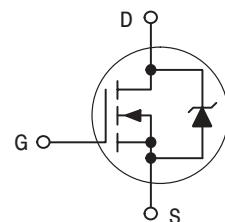
Rating	Symbol	Value	Unit
Drain-Source Voltage	V _{DSS}	60	Vdc
Drain-Gate Voltage (R _{GS} = 1.0 MΩ)	V _{DGR}	60	Vdc
Gate-Source Voltage – Continuous – Non-repetitive (t _p ≤ 50 μs)	V _{GS} V _{GSM}	±20 ±40	Vdc Vpk
Drain Current – Continuous – Pulsed	I _D I _{DM}	150 1000	mAdc
Total Power Dissipation @ T _A = 25°C Derate above 25°C	P _D	400 3.2	mW mW/°C
Operating and Storage Temperature Range	T _J , T _{stg}	-55 to +150	°C

Maximum ratings are those values beyond which device damage can occur. Maximum ratings applied to the device are individual stress limit values (not normal operating conditions) and are not valid simultaneously. If these limits are exceeded, device functional operation is not implied, damage may occur and reliability may be affected.

THERMAL CHARACTERISTICS

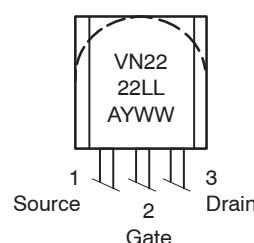
Characteristic	Symbol	Max	Unit
Thermal Resistance, Junction-to-Ambient	R _{θJA}	312.5	°C/W
Maximum Lead Temperature for Soldering Purposes, 1/16" from case for 10 seconds	T _L	300	°C

N-Channel



TO-92
CASE 29
STYLE 22

MARKING DIAGRAM & PIN ASSIGNMENT



A = Assembly Location
Y = Year
WW = Work Week

ORDERING INFORMATION

See detailed ordering and shipping information in the package dimensions section on page 1096 of this data sheet.

Preferred devices are recommended choices for future use and best overall value.

*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

VN2222LL

ELECTRICAL CHARACTERISTICS ($T_C = 25^\circ\text{C}$ unless otherwise noted)

Characteristic	Symbol	Min	Max	Unit
OFF CHARACTERISTICS				
Drain–Source Breakdown Voltage ($V_{GS} = 0$, $I_D = 100 \mu\text{A}_{\text{dc}}$)	$V_{(\text{BR})\text{DSS}}$	60	–	Vdc
Zero Gate Voltage Drain Current ($V_{DS} = 48 \text{ Vdc}$, $V_{GS} = 0$) ($V_{DS} = 48 \text{ Vdc}$, $V_{GS} = 0$, $T_J = 125^\circ\text{C}$)	I_{DSS}	– –	10 500	μA_{dc}
Gate–Body Leakage Current, Forward ($V_{GSF} = 30 \text{ Vdc}$, $V_{DS} = 0$)	I_{GSSF}	–	–100	nA _{dc}

ON CHARACTERISTICS (Note 1)

Gate Threshold Voltage ($V_{DS} = V_{GS}$, $I_D = 1.0 \text{ mA}_{\text{dc}}$)	$V_{GS(\text{th})}$	0.6	2.5	Vdc
Static Drain–Source On–Resistance ($V_{GS} = 10 \text{ Vdc}$, $I_D = 0.5 \text{ A}_{\text{dc}}$) ($V_{GS} = 10 \text{ Vdc}$, $I_D = 0.5 \text{ Vdc}$, $T_C = 125^\circ\text{C}$)	$r_{DS(\text{on})}$	– –	7.5 13.5	Ω
Drain–Source On–Voltage ($V_{GS} = 5.0 \text{ Vdc}$, $I_D = 200 \text{ mA}_{\text{dc}}$) ($V_{GS} = 10 \text{ Vdc}$, $I_D = 500 \text{ mA}_{\text{dc}}$)	$V_{DS(\text{on})}$	– –	1.5 3.75	Vdc
On–State Drain Current ($V_{GS} = 10 \text{ Vdc}$, $V_{DS} \geq 2.0 \text{ V}_{DS(\text{on})}$)	$I_{D(\text{on})}$	750	–	mA
Forward Transconductance ($V_{DS} = 10 \text{ Vdc}$, $I_D = 500 \text{ mA}_{\text{dc}}$)	g_{fs}	100	–	μmhos

DYNAMIC CHARACTERISTICS

Input Capacitance	$(V_{DS} = 25 \text{ Vdc}$, $V_{GS} = 0$, $f = 1.0 \text{ MHz}$)	C_{iss}	–	60	pF
Output Capacitance		C_{oss}	–	25	
Reverse Transfer Capacitance		C_{rss}	–	5.0	

SWITCHING CHARACTERISTICS (Note 1)

Turn–On Delay Time	$(V_{DD} = 15 \text{ Vdc}$, $I_D = 600 \text{ mA}$, $R_{\text{gen}} = 25 \Omega$, $R_L = 23 \Omega$)	t_{on}	–	10	ns
Turn–Off Delay Time		t_{off}	–	10	

1. Pulse Test: Pulse Width $\leq 300 \mu\text{s}$, Duty Cycle $\leq 2.0\%$.

ORDERING INFORMATION

Device	Package	Shipping [†]
VN2222LL	TO–92	1000 Unit / Box
VN2222LLG	TO–92 (Pb-Free)	1000 Unit / Box
VN2222LLRL	TO–92	1000 Unit / Box
VN2222LLRLRA	TO–92	2000 Tape & Reel
VN2222LLRLRAG	TO–92 (Pb-Free)	2000 Tape & Reel
VN2222LLRLRM	TO–92	2000 Unit / Ammo Box

[†]For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

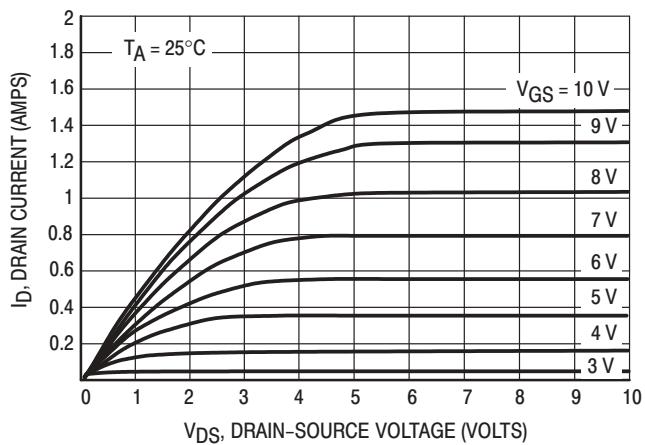


Figure 1. Ohmic Region

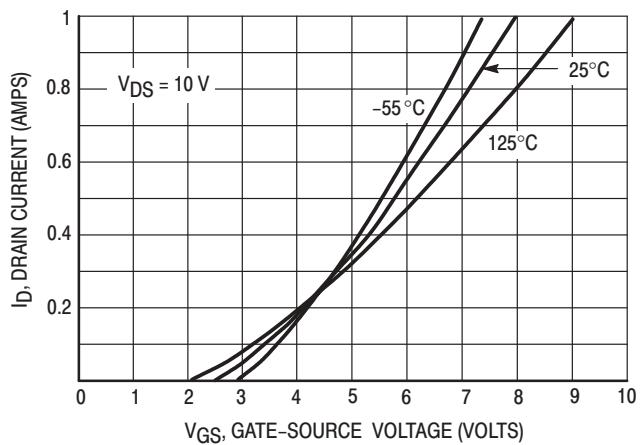


Figure 2. Transfer Characteristics

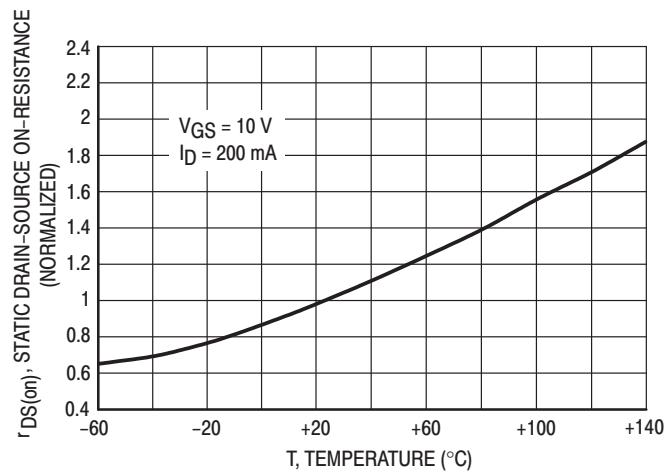


Figure 3. Temperature versus Static Drain-Source On-Resistance

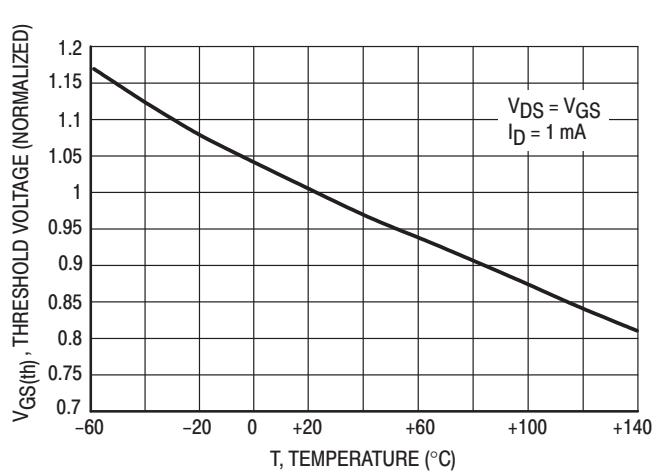


Figure 4. Temperature versus Gate Threshold Voltage

VN2406L

Preferred Device

Small Signal MOSFET 200 mAmps, 240 Volts N-Channel TO-92

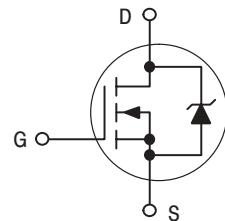


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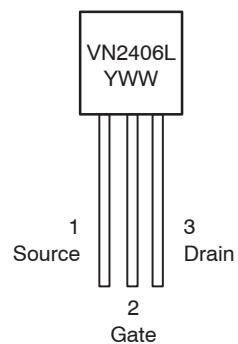
**200 mAmps
240 Volts
RDS(on) = 6 Ω**

N-Channel



TO-92
CASE 29
Style 22

MARKING DIAGRAM & PIN ASSIGNMENT



Y = Year
WW = Work Week

ORDERING INFORMATION

Device	Package	Shipping
VN2406L	TO-92	1000 Units/Box
VN2406LZL1	TO-92	2000 Ammo Pack

Preferred devices are recommended choices for future use and best overall value.

VN2406L

ELECTRICAL CHARACTERISTICS ($T_A = 25^\circ\text{C}$ unless otherwise noted)

Characteristic	Symbol	Min	Max	Unit
STATIC CHARACTERISTICS				
Drain – Source Breakdown Voltage ($V_{GS} = 0$, $I_D = 100 \mu\text{A}$)	$V_{(BR)DSS}$	240	–	Vdc
Zero Gate Voltage Drain Current ($V_{DS} = 120 \text{ Vdc}$, $V_{GS} = 0$) ($V_{DS} = 120 \text{ Vdc}$, $V_{GS} = 0$, $T_A = 125^\circ\text{C}$)	I_{DSS}	– –	10 500	μAdc
Gate– Body Leakage ($V_{DS} = 0$, $V_{GS} = \pm 15 \text{ V}$)	I_{GSS}	–	± 100	nAdc
Gate Threshold Voltage ($V_{DS} = V_{GS}$, $I_D = 1.0 \text{ mA}$)	$V_{GS(\text{th})}$	0.8	2.0	Vdc
On-State Drain Current (Note 1) ($V_{GS} = 10 \text{ V}$, $V_{DS} \geq 2.0 \text{ V}_{DS(\text{on})}$)	$I_{D(on)}$	1.0	–	Adc
Drain–Source On Resistance (Note 1) ($V_{GS} = 2.5 \text{ V}$, $I_D = 0.1 \text{ A}$) ($V_{GS} = 10 \text{ V}$, $I_D = 0.5 \text{ A}$)	$r_{DS(on)}$	– –	10 6.0	Ω
Forward Transconductance (Note 1) ($V_{DS} = 10 \text{ V}$, $I_D = 0.5 \text{ A}$)	g_{fs}	300	–	mS

DYNAMIC CHARACTERISTICS

Input Capacitance	$(V_{DS} = 25 \text{ Vdc}, V_{GS} = 0,$ $f = 1.0 \text{ MHz})$	C_{iss}	–	125	pF
Output Capacitance		C_{oss}	–	50	pF
Reverse Transfer Capacitance		C_{rss}	–	20	pF

SWITCHING CHARACTERISTICS

Turn-On Time	$(V_{DD} = 60 \text{ Vdc}, I_D = 0.4 \text{ A},$ $R_L = 150 \Omega, R_G = 25 \Omega)$	$t_{(\text{on})}$	–	8.0	ns
Turn-Off Time		$t_{(\text{r})}$	–	8.0	ns
		$t_{(\text{off})}$	–	23	ns
		$t_{(\text{f})}$	–	34	ns

1. Pulse Test; Pulse Width < 300 μs , Duty Cycle $\leq 2.0\%$.