

FAIRCHILD DIGITAL

TTL

SSI FUNCTIONS (Cont'd)

Item	Function ⁽¹⁾	9000 Series 8 ns/10 mW	Low Power Schottky 54LS/74LS 5 ns/2 mW	Std. TTL 54/74 10 ns/10 mW	High Speed 54H/74H 6 ns/22 mW	High Speed Schottky 54S/74S 3 ns/19 mW	Logic/Connection Diagram ⁽²⁾	Packages ⁽³⁾
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NOR Gates

1	Quad 2-Input	—	54LS/74LS02	54/7402	—	54S/74S02	D10	3I,6A,9A
2	Quad 2-Input	9015	—	—	—	—	D11	4L,6B
3	Triple 3-Input	—	54LS/74LS27	54/7427	—	—	D12	3I,6A,9A
4	Dual 4-Input w/Strobe	—	—	54/7425	—	—	D13	3I,6A,9A
5	Dual 4-Input (Exp)	—	—	54/7423	—	—	D14	4L,6B,9B
6	Dual 5-Input	—	54LS/74LS260	—	—	—	D72	3I,6A,9A
7	Quad 2-Input	—	54LS/74LS28	—	—	—	D10	3I,6A,9A
8	Quad 2-Input (OC)	—	74LS33	—	—	—	D10	6A,9A

AND Gates

9	Hex Buffer (OC/15 V)	—	—	54/7417	—	—	D15	3I,6A,9A
10	Hex Buffer (OC/30 V)	—	—	54/7407	—	—	D15	3I,6A,9A
11	Quad 2-Input	—	54LS/74LS08	54/7408	54H/74H08	54S/74S08	D16	3I,6A,9A
12	Quad 2-Input (OC)	—	54LS/74LS09	54/7409	—	54S/74S09	D16	3I,6A,9A
13	Quad 2-2-3-3 Input	—	—	—	—	9S41	D17	4L,6B,9B
14	Triple 3-Input	—	54LS/74LS11	54/7411	54H/74H11	54S/74S11	D18	3I,6A,9A
15	Triple 3-Input (OC)	—	54LS/74LS15	—	—	54S/74S15	D18	3I,6A,9A
16	Dual 4-Input	—	54LS/74LS21	54/7421	54H/74H21	—	D19	3I,6A,9A

OR Gates

17	Quad 2-Input	—	54LS/74LS32	54/7432	—	54S/74S32	D20	3I,6A,9A
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Exclusive OR Gates

18	Quad 2-Input	—	54LS/74LS86	54/7486	—	54S/74S86	D21	3I,6A,9A
19	Quad 2-Input (OC)	—	54LS/74LS136	—	—	—	D21	3I,6A,9A
20	Quad 2-Input OR/NOR	9014	—	—	—	—	D22	4L,6B
21	Quad 2-Input OR/NOR	—	—	—	—	54S/74S135	D23	4L,6B,9B
22	Quad 2-Input	—	54LS/74LS386	—	—	—	D94	3I,6A,9A

Notes on following pages.