

# MSKSEMI 美森科

SEMICONDUCTOR



ESD



TVS



TSS



MOV



GDT



PLED


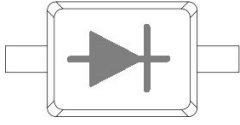
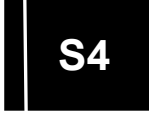


## SD103AW-SD103CW

Product specification

## FEATURES

- Low Forward Voltage Drop
- Guard Ring Construction for Transient Protection
- Negligible Reverse Recovery Time
- Low Capacitance

## Reference News

PACKAGE OUTLINE	PIN CONFIGURATION	SD103AW	SD103BW	SD103CW
				
SOD-123		MARKING:S4	MARKING:S5	MARKING:S6

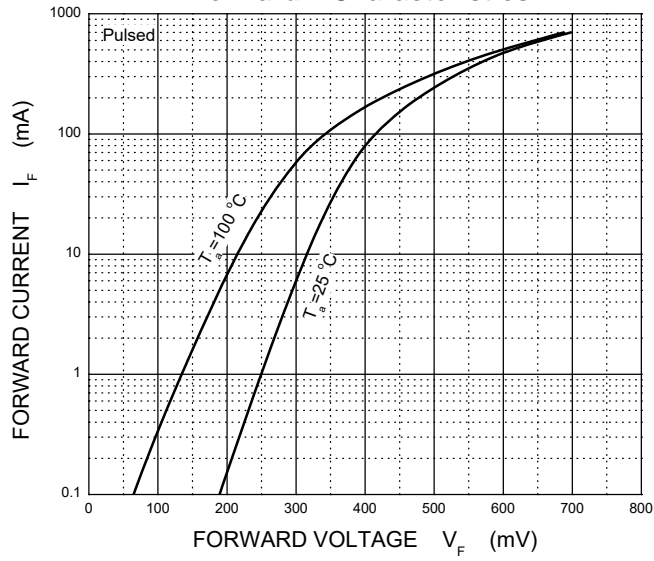
## MAXIMUM RATINGS ( Ta=25°C unless otherwise noted )

Symbol	Parameter	Value			Unit
		SD103AW	SD103BW	SD103CW	
$V_{RRM}$	Peak Repetitive Reverse Voltage	40	30	20	V
$V_{RWM}$	Working Peak Reverse Voltage				
$V_{R(RMS)}$	RMS Reverse Voltage	28	21	14	V
$I_{FM}$	Forward Continuous Current	350			mA
$I_{FSM}$	Non-repetitive Peak Forward Surge Current@t= 8.3ms	2			A
$P_D$	Power Dissipation	400			mW
$R_{JA}$	Thermal Resistance from Junction to Ambient	250			°C/W
$T_j$	Operating Junction Temperature Range	-40 ~ +125			°C
$T_{stg}$	Storage Temperature Range	-55 ~ +150			°C

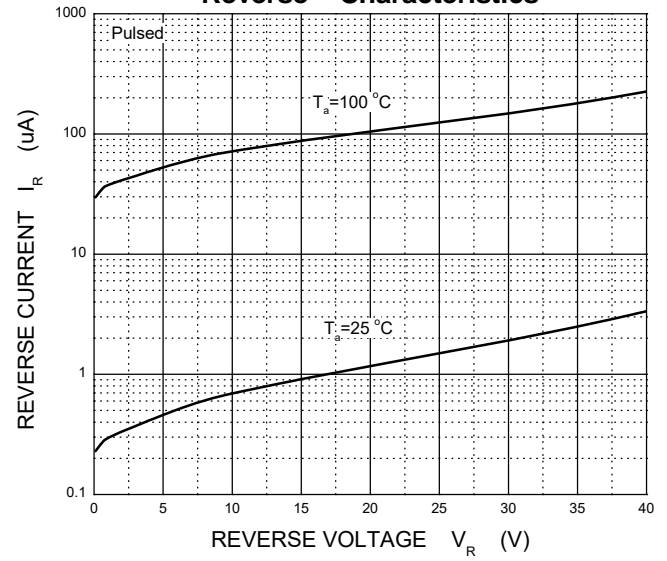
## ELECTRICAL CHARACTERISTICS(Ta=25°C unless otherwise specified)

Parameter	Symbol	Test conditions	Min	Typ	Max	Unit
Reverse voltage	$V_{(BR)}$	$I_R=100\mu A$ SD103AW	40			V
		SD103BW	30			
		SD103CW	20			
Reverse current	$I_R$	$V_R=30V$ SD103AW			5	$\mu A$
		$V_R=20V$ SD103BW				
		$V_R=10V$ SD103CW				
Forward voltage	$V_F$	$I_F=20mA$			0.37	V
		$I_F=200mA$			0.6	
Total capacitance	$C_{tot}$	$V_R=0V, f=1MHz$		50		pF
Reverse recovery time	$t_{rr}$	$I_F=I_R=200mA, I_{rr}=0.1 \times I_R, R_L=100\Omega$		10		ns

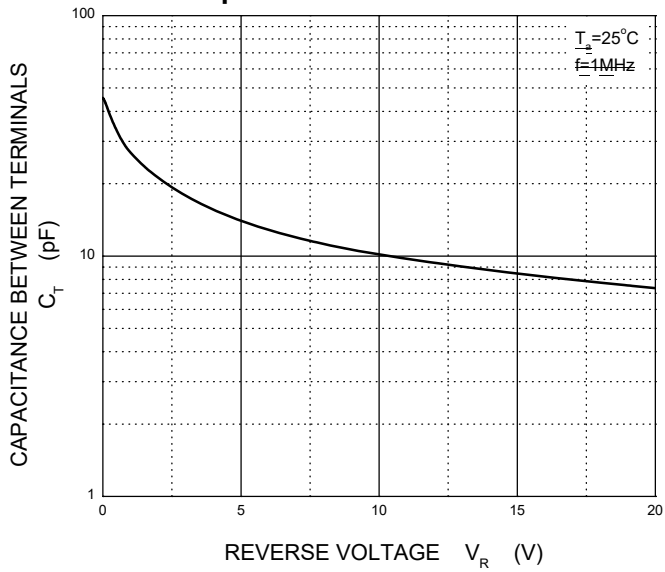
**Forward Characteristics**



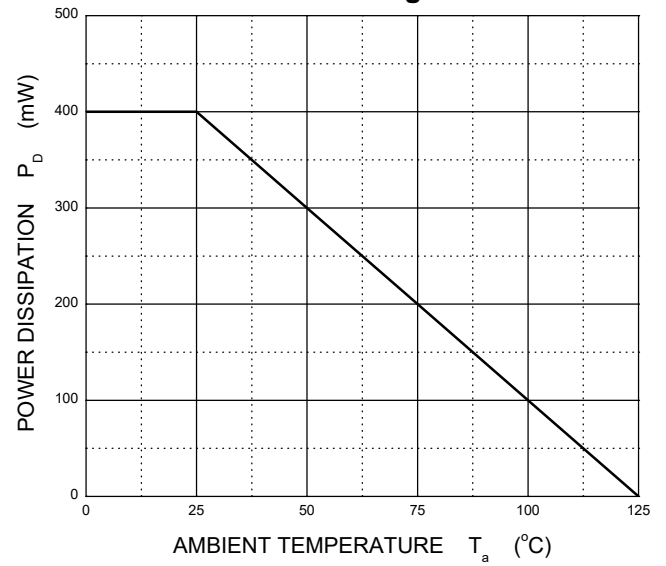
**Reverse Characteristics**



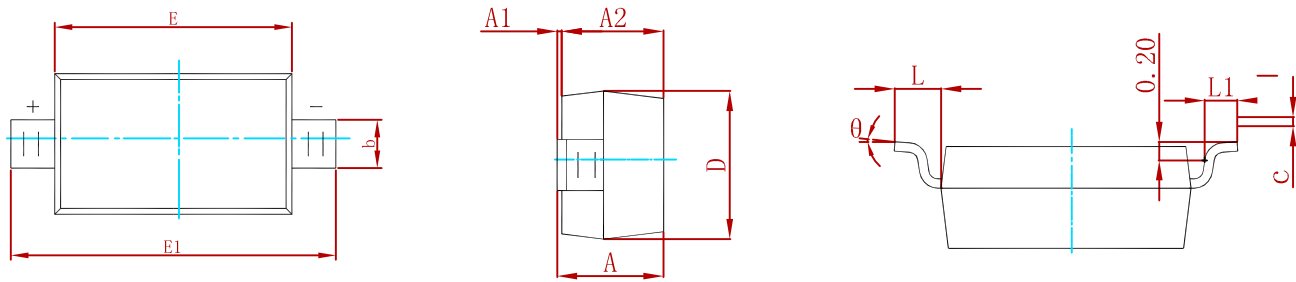
**Capacitance Characteristics**



**Power Derating Curve**

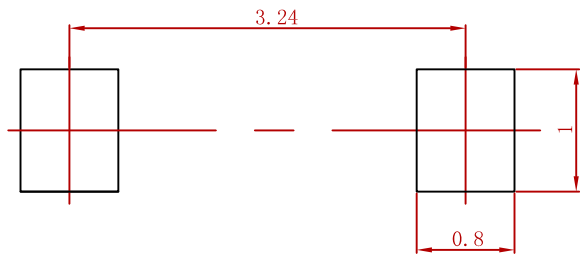


PACKAGE MECHANICAL DATA



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	1.050	1.250	0.041	0.049
A1	0.000	0.100	0.000	0.004
A2	1.050	1.150	0.041	0.045
b	0.450	0.650	0.018	0.026
c	0.080	0.150	0.003	0.006
D	1.500	1.700	0.059	0.067
E	2.600	2.800	0.102	0.110
E1	3.550	3.850	0.140	0.152
L	0.500 REF		0.020 REF	
L1	0.250	0.450	0.010	0.018
θ	0°	8°	0°	8°

Suggested Pad Layout



Note:  
1.Controlling dlmension:in mllimeters.  
2.General tolerance:± 0.05mm.  
3.The pad layout ls for reference purposes only.

REEL SPECIFICATION

P/N	PKG	QTY
SD103AW-SD103CW	SOD-123	3000

## Attention

■ Any and all MSKSEMI Semiconductor products described or contained herein do not have specifications that can handle applications that require extremely high levels of reliability, such as life-support systems, aircraft's control systems, or other applications whose failure can be reasonably expected to result in serious physical and/or material damage. Consult with your MSKSEMI Semiconductor representative nearest you before using any MSKSEMI Semiconductor products described or contained herein in such applications.

■ MSKSEMI Semiconductor assumes no responsibility for equipment failures that result from using products at values that exceed, even momentarily, rated values (such as maximum ratings, operating condition ranges, or other parameters) listed in products specification of any and all MSKSEMI Semiconductor products described or contained herein.

■ Specifications of any and all MSKSEMI Semiconductor products described or contained herein stipulate the performance, characteristics, and functions of the described products in the independent state, and are not guarantees of the performance, characteristics, and functions of the described products as mounted in the customer's products or equipment. To verify symptoms and states that cannot be evaluated in an independent device, the customer should always evaluate and test devices mounted in the customer's products or equipment.

■ MSKSEMI Semiconductor strives to supply high-quality high-reliability products. However, any and all semiconductor products fail with some probability. It is possible that these probabilistic failures could give rise to accidents or events that could endanger human lives, that could give rise to smoke or fire, or that could cause damage to other property. When designing equipment, adopt safety measures so that these kinds of accidents or events cannot occur. Such measures include but are not limited to protective circuits and error prevention circuits for safe design, redundant design, and structural design.

■ In the event that any or all MSKSEMI Semiconductor products (including technical data, services) described or contained herein are controlled under any of applicable local export control laws and regulations, such products must not be exported without obtaining the export license from the authorities concerned in accordance with the above law.

■ No part of this publication may be reproduced or transmitted in any form or by any means, electronic or mechanical, including photocopying and recording, or any information storage or retrieval system, or otherwise, without the prior written permission of MSKSEMI Semiconductor.

■ Information (including circuit diagrams and circuit parameters) herein is for example only ; it is not guaranteed for volume production. MSKSEMI Semiconductor believes information herein is accurate and reliable, but no guarantees are made or implied regarding its use or any infringement of intellectual property rights or other rights of third parties.

■ Any and all information described or contained herein are subject to change without notice due to product/technology improvement, etc. When designing equipment, refer to the "Delivery Specification" for the MSKSEMI Semiconductor product that you intend to use.