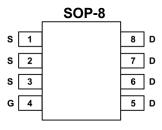
# FDS6675BZ-HX P-Channel 30-V (D-S) MOSFET

PRODUCT SUMMARY				
VDS (V)	RDS(on) (Ω)	Qg (Typ.)	ID (A)d	
-30	0.0125 at VGS = - 10 V	2250	- 11.6	
	0.0180 at Vgs = - 4.5 V	22nC	- 10	





**Top View** 

### **FEATURES**

- TrenchFET® Power MOSFET
- 100 % R<sub>q</sub> Tested

### **APPLICATIONS**

- Load Switches Notebook PCs
  - Desktop PCs

<b>MAXIMUM RATINGS</b> (TA = 25 ° C unless otherwise noted)					
Symbol	Parameter	Ratings	Unit		
VDS	Drain to Source Voltage	-30	V		
Vgs	Gate to Source Voltage	±25	V		
ID	Drain Current - Continuous	- 11	Α		
	- Pulsed	-55			
PD	Power Dissipation for Single Operation	2.5	W		
	Y	1.2			
		1.0			
ТЈ, Tsтg	Operating and Storage Junction Temperature Range	-55 to +150	° C		

THERMAL CHARACTERISTICS					
Symbol	Parameter	Ratings	Unit		
Rojc	Thermal Resistance, Junction to Case	25	° C/W		
Roja	Thermal Resistance, Junction to Ambient	50			

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ELECTRICAL CHARACTERISTICS (TA= 25°C)							
Symbol	Parameter	Conditions	Min	Тур	Max	Unit	
OFF CHARACTERISTICS							
BVDSS	Drain to Source Breakdown Voltage	ID = −250 μ A, VGS = 0 V	-30			V	
ΔBVDSS	Breakdown Voltage Temperature	ID = −250 μ A,		-20		mV/°C	
ΔΤͿ	Coefficient	referenced to 25°C					
IDSS	Zero Gate Voltage Drain Current	VDS = −24 V, VGS = 0 V			- 1	μ <b>А</b>	
IGSS	Gate to Source Leakage Current	VGS = ±25 V, VDS = 0 V			± 10	μА	
	ON CHAR	ACTERISTICS					
VGS(th)	Gate to Source Threshold Voltage	VGS = VDS , ID = -250 μ A	- 1	-2	-3	V	
$\Delta V$ GS(th)	Gate to Source Threshold Voltage	ID = −250 μ A,		15.7		mV/°C	
$\Delta T_{\rm J}$	Temperature Coefficient	referenced to 25°C					
RDS(on)	Static Drain to Source On Resistance	VGS = -10 V, ID = - 11 A		10.8	13.0	mQ	
	7/X	VGS = -4.5 V, ID = -9 A		17.4	21.8		
		VGS = -10 V, ID = -11 A,		15.0	18.8		
		TJ = 125°C					
gFS	Forward Transconductance	VDS = −5 V, ID = − 11 A		34		S	
	DYNAMIC CH	ARACTERISTICS					
Ciss	Input Capacitance	VDS = -15 V, VGS = 0 V,		1855	2470	pF	
Coss	Output Capacitance	f = 1 MHz		335	450	pF	
Crss	Reverse Transfer Capacitance			330	500	pF	
	SWITCHING C	HARACTERISTICS					
td(on)	Turn-On Delay Time	VDD = - 15 V,		3.0	10	ns	
tr	Rise Time	ID = - 11 A,		7.8	16	ns	
td(off)	Turn-Off Delay Time	VGS = - 10 V,		120	200	ns	
tf	Fall Time	RGS = 6 Q		60	100	ns	
Qg	Total Gate Charge	VDS = - 15 V,		44	62	nC	
		VGS = - 10 V, ID = - 11 A					
Qg	Total Gate Charge	VDS = - 15 V,		25	35	nC	
Qgs	Gate to Source Charge	VGS = -5 V, ID = - 11 A	V	7.2		nC	
Qgd	Gate to Drain "Miller" Charge			11.4		nC	
DRAIN-SOURCE DIODE CHARACTERISTICS							
VSD	Source to Drain Diode Forward Voltage	VGS = 0V, IS = −2.1 A		-0.7	- 1.2	V	
trr	Reverse Recovery Time	IF = −11 A,			42	ns	
		di/dt = 100 A/ μ s					
Qrr	Reverse Recovery Charge	IF = −11 A,			30	nC	
		di/dt = 100 A/ μ s					

### Notes:

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a. R0JA is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins. R0JC is guaranteed by design while R0CA is determined by the user's board design. b. Pulse Test: Pulse Width < 300s, Duty cycle < 2.0%.

c. The diode connected between the gate and source serves only as protection against ESD. No gate overvoltage rating is implied.

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### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

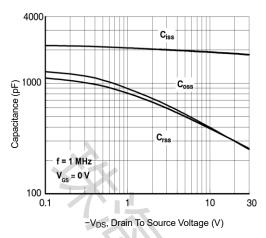


Fig 1. Capacitance vs Drain to Source Voltage

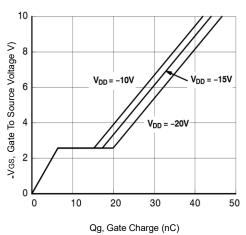


Fig 2. Gate Charge Characteristics

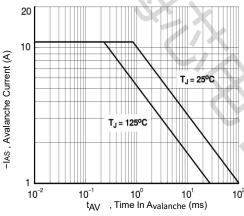


Fig 3. Unclamped Inductive Switching Capability

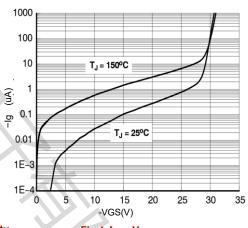


Fig 4. Igvs V<sub>GS</sub>

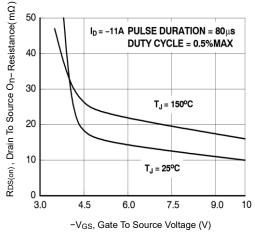


Fig 5. On-Resistance vs Gate to Source Voltage

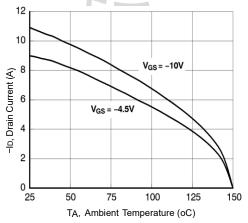


Fig 6. Maximum Continuous Drain Current
vs Ambient Temperature

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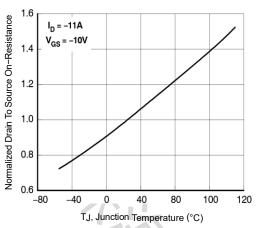


Fig 7. Normalized On – Resistance vs Junction Temperature

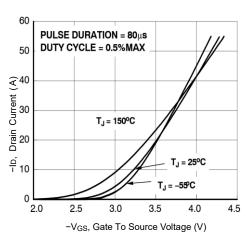


Fig 8. Transfer Characteristics

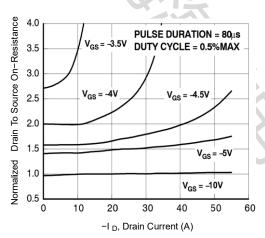
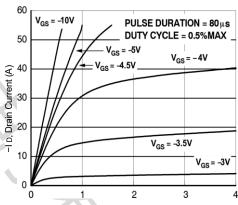


Fig 9. Normalized On-Resistance vs Drain Current and Gate Voltage



-VDS, Drain To Source Voltage (V)
Fig 10.On-Region Characteristics

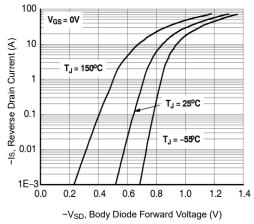


Fig 11. Source to Drain Diode Forward

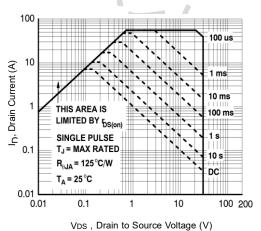


Fig 12. Forward Bias Safe Operating Area

Voltage vs Source Current

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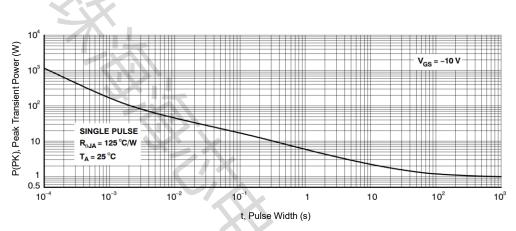


Fig 13. Single Pulse Maximum Power Dissipation

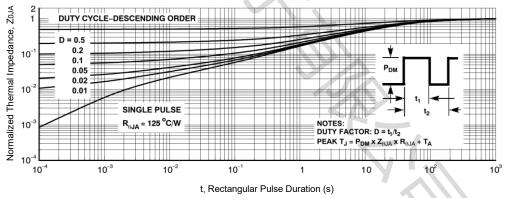
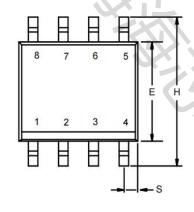


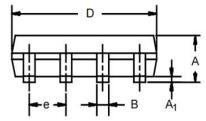
Fig 14. Junction To Ambient Transient Thermal Response Curve

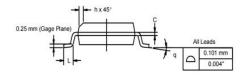
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# **SOP-8 Package Outline**

Dimensions are shown in millimeters (inches)



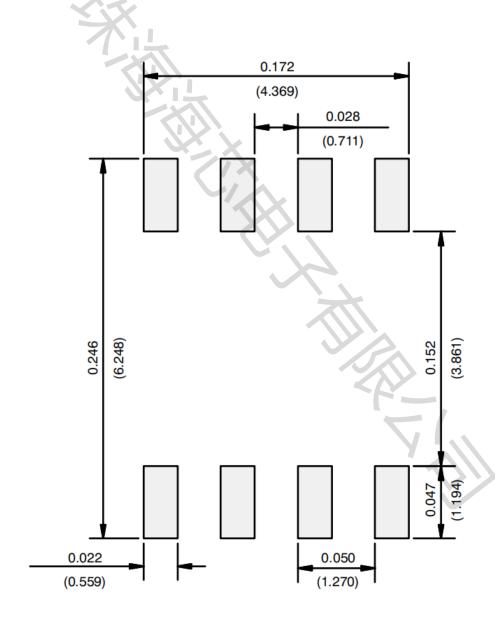




DIM	MILLIMETERS		INCHES		
	Min	Max	Min	Max	
Α	1.35	1.75	0.053	0.069	
A1	0.10	0.20	0.004	0.008	
В	0.35	0.51	0.014	0.020	
С	0.19	0.25	0.0075	0.010	
D	4.80	5.00	0.189	0.196	
E	3.80	4.00	0.150	0.157	
е	1.27 BSC		0.050 BSC		
Н	5.80	6.20	0.228	0.244	
h	0.25	0.50	0.010	0.020	
L	0.50	0.93	0.020	0.037	
q	0°	8°	0°	8°	
S	0.44	0.64	0.018	0.026	

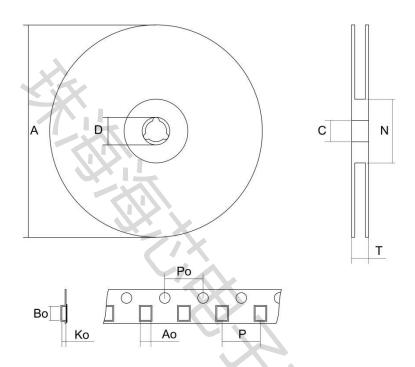
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### **RECOMMENDED MINIMUM PADS FOR SOP-8**



# **SOP-8** packing information

### **SOP-8** tape and reel



### **Tape orientation**

# User Direction of Feed

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