### SI9945BDY-T1-GE3-HX

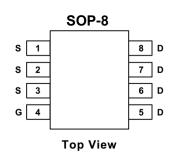
# **Dual N-Channel 30-V (D-S) MOSFET**

PRODUCT SUMMARY			
Vps (V)	60		
R <sub>D</sub> S(on) (Ω) at V <sub>G</sub> S = 10 V	0.040		
RDS(on) ( $\Omega$ ) at VGS = 4.5 V	0.055		
In (A) per leg	7		
Configuration	Dual		

#### **FEATURES**

- TrenchFET<sup>®</sup> Power MOSFET
- 100 % R<sub>a</sub> and UIS Tested





Absolute Maximum Ratings T <sub>A</sub> =25°C unless otherwise noted					
Parameter		Symbol	Maximum	Units	
Drain-Source Vol	Drain-Source Voltage		60	V	
Gate-Source Vol	Gate-Source Voltage		±20	V	
Continuous Drain	T <sub>A</sub> =25°C		4.5		
Current AF	T <sub>A</sub> =70°C	ΙD	3.6	А	
Pulsed Drain Current <sup>B</sup>		Ірм	20		
	T <sub>A</sub> =25°C		2	<u></u>	
Power Dissipation	T <sub>A</sub> =70°C	P <sub>D</sub>	1.28	W	
Avalanche Current <sup>B</sup>		lar, las	19	A	
Repetitive avalanche energy 0.1mH <sup>B</sup>		EAR, EAS	18	mJ	
Junction and Storage Temperature Range		TJ, TSTG	-55 to 150	°C	

Thermal Characteristics						
Parameter		Symbol	Тур	Max	Units	
Maximum Junction-to-Ambient <sup>A</sup>	t ≤ 10s	_	48	62.5	°C/W	
Maximum Junction-to-Ambient <sup>A</sup>	Steady-State	Reja	74	110	°C/W	
Maximum Junction-to-Lead <sup>c</sup>	Steady-State	Rθ <sub>JL</sub>	35	60	°C/W	

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Symbol	Parameter	Conditions	Min	Тур	Max	Units
-	C PARAMETERS			31		
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	I <sub>D</sub> =250μA, V <sub>GS</sub> =0V	60			V
		V <sub>DS</sub> =60V, V <sub>GS</sub> =0V			1	
IDSS	Zero Gate Voltage Drain Current	T <sub>J</sub> =	55°C		5	μΑ
Igss	Gate-Body leakage current	V <sub>DS</sub> =0V, V <sub>GS</sub> = ±20V			100	nA
V <sub>GS(th)</sub>	Gate Threshold Voltage	V <sub>DS</sub> =V <sub>GS</sub> I <sub>D</sub> =250μA	1	2.1	3	V
I <sub>D(ON)</sub>	On state drain current	V <sub>GS</sub> =10V, V <sub>DS</sub> =5V	20			Α
		V <sub>GS</sub> =10V, I <sub>D</sub> =4.5A		46	56	
RDS(ON)	Static Drain-Source On-Resistance	T <sub>J</sub> =1:	25°C	80	100	mΩ
	W11-	V <sub>GS</sub> =4.5V, I <sub>D</sub> =3A		64	77	mΩ
grs	Forward Transconductance	V <sub>DS</sub> =5V, I <sub>D</sub> =4.5A		11		S
V <sub>SD</sub>	Diode Forward Voltage	I <sub>S</sub> =1A,V <sub>GS</sub> =0V		0.74	1	V
ls	Maximum Body-Diode Continuous Curre	rimum Body-Diode Continuous Current			3	Α
lsм	Pulsed Body Diode Current <sup>B</sup>				20	Α
DYNA	MIC PARAMETERS					
Ciss	Input Capacitance			450	540	pF
Coss	Output Capacitance	$V_{GS}$ =0V, $V_{DS}$ =30V, f=1MHz		60		pF
Crss	Reverse Transfer Capacitance			25		pF
$R_g$	Gate resistance	V <sub>GS</sub> =0V, V <sub>DS</sub> =0V, f=1MHz	1.3	1.65	2	Ω
SWITC	HING PARAMETERS					
Q <sub>g</sub> (10V)	Total Gate Charge			8.5	10.5	nC
Qg(4.5V)	Total Gate Charge	.,		4.3	5.5	nC
Q <sub>gs</sub>	Gate Source Charge	V <sub>GS</sub> =10V, V <sub>DS</sub> =30V, I <sub>D</sub> =4.5A		1.6		nC
$Q_{gd}$	Gate Drain Charge		7	2.2		nC
tD(on)	Turn-On DelayTime	$V_{GS}$ =10V, $V_{DS}$ =30V, $R_L$ =6.7 $\Omega$ ,		4.7		ns
t <sub>r</sub>	Turn-On Rise Time	$R_{GEN}=3\Omega$		2.3		ns
tD(off)	Turn-Off DelayTime			15.7		ns
t <sub>f</sub>	Turn-Off Fall Time			1.9		ns
trr	Body Diode Reverse Recovery Time	I <sub>F</sub> =4.5A, dI/dt=100A/μs		27.5	35	ns
Qrr	Body Diode Reverse Recovery Charge	I <sub>F</sub> =4.5A, dI/dt=100A/μs		32		nC

#### **NOTES**

A.The value of Rejah is measured with the device mounted on 1in 2 FR-4 board with 2oz. Copper, in a still air environment with TA=25°C. The value in any given application depends on the user's specific board design.

- B. Repetitive rating, pulse width limited by junction temperature.
- C. The Reja is the sum of the thermal impedence from junction to lead Rejl and lead to ambient.
- D. The static characteristics in Figures 1 to 6 are obtained using <300 µs pulses, duty cycle 0.5% max.
- E. These tests are performed with the device mounted on 1 in 2 FR-4 board with 2oz. Copper, in a still air environment with TA=25°C. The SOA curve provides a single pulse rating.
- F. The current rating is based on the t≤ 10s junction to ambient thermal resistance rating.

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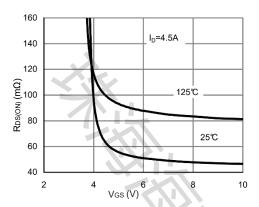


Fig 1. On-Resistance vs. Gate-Source Voltage

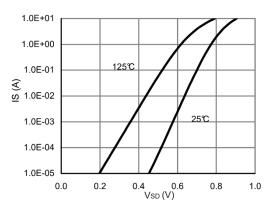


Fig 2. Body-Diode Characteristics

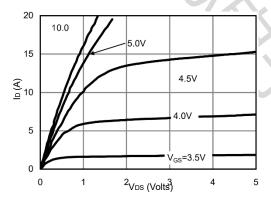


Fig 3. On-Region Characteristics

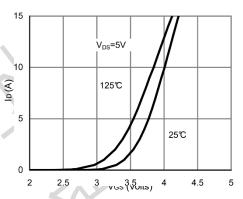


Fig 4. Transfer Characteristics

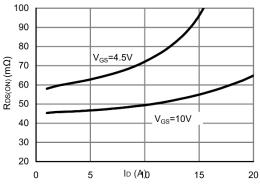


Fig 5. On-Resistance vs. Drain Current and Gate Voltage

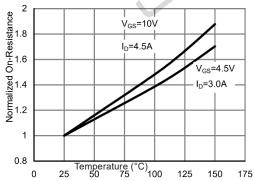
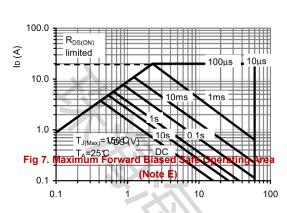


Fig 6. On-Resistance vs. Junction Temperature

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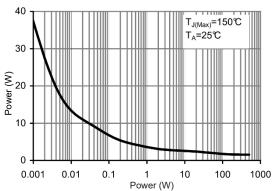


Fig 8. Single Pulse Power Rating Junction-to Ambient (Note E)

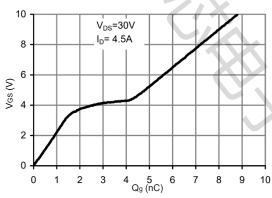


Fig 9. Gate-Charge Characteristics

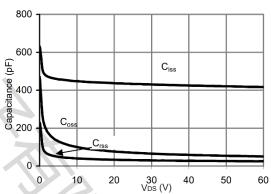


Fig 10. Capacitance Characteristics

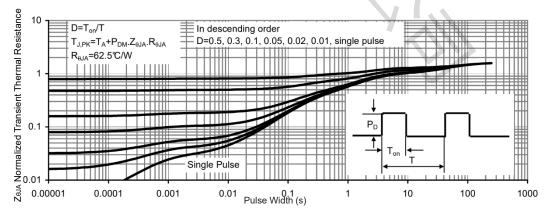
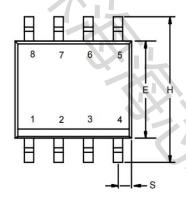


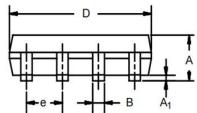
Fig 11. Normalized Maximum Transient Thermal Impedance

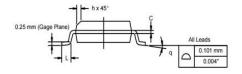
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# **SOP-8 Package Outline**

Dimensions are shown in millimeters (inches)



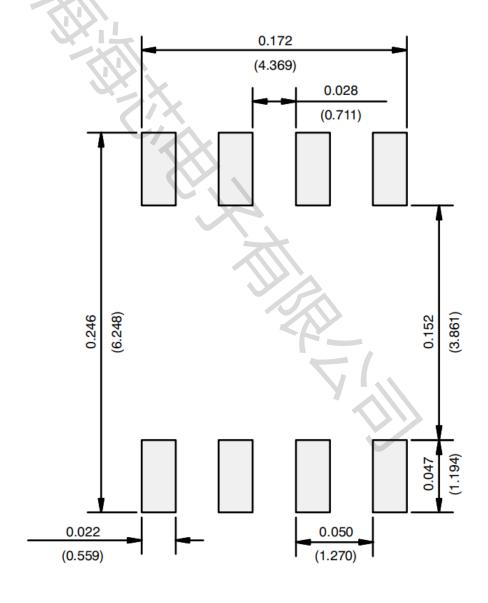




	MILLIMETERS			INCHES
DIM	Min	Max	Min	Max
Α	1.35	1.75	0.053	0.069
<b>A</b> 1	0.10	0.20	0.004	0.008
В	0.35	0.51	0.014	0.020
С	0.19	0.25	0.007 5	0.010
D	4.80	5.00	0.189	0.196
E	3.80	4.00	0.150	0.157
е	1.27 BSC			0.050 BSC
Н	5.80	6.20	0.228	0.244
h	0.25	0.50	0.010	0.020
L	0.50	0.93	0.020	0.037
q	0°	8°	0°	8°
S	0.44	0.64	0.018	0.026

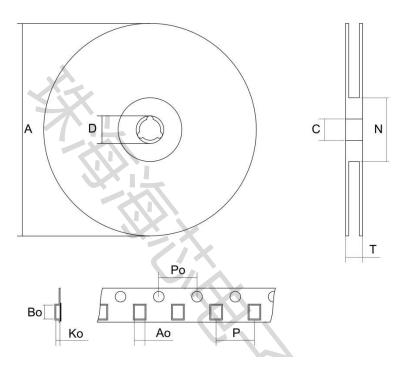
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## **RECOMMENDED MINIMUM PADS FOR SOP-8**

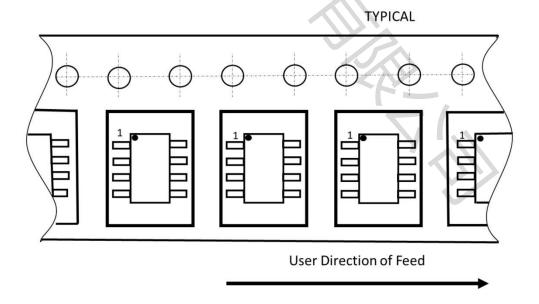


# **SOP-8** packing information

SOP-8 tape and reel



**Tape orientation** 



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