



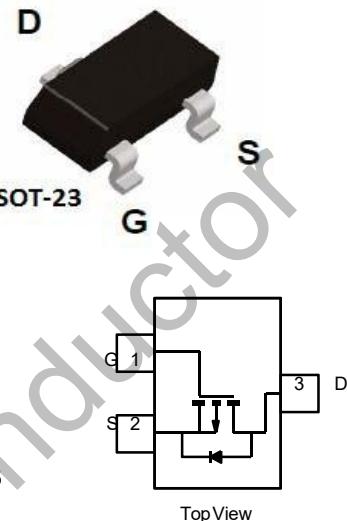
WG2301

20V P-Channel MOSFET

Features:

- Low Intrinsic Capacitances.
- Excellent Switching Characteristics.
- Extended Safe Operating Area.
- Unrivalled Gate Charge : $Q_g = 3.3\text{nC}$ (Typ.).
- $\text{BV}_DSS = -20\text{V}, \text{ID} = -3\text{A}$
- $\text{R}_{DS(on)} : 110\text{m}\Omega$ (Max) @ $\text{VG} = -4.5\text{V}$
- 100% Avalanche Tested

SOT-23



Marking:A1SHB

Absolute Maximum Ratings ($T_A = 25^\circ\text{C}$ unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V_{DS}	-20	V
Gate-Source Voltage	V_{GS}	± 12	V
Drain Current-Continuous	I_D	-3	A
Drain Current -Pulsed (Note 1)	I_{DM}	-10	A
Maximum Power Dissipation	P_D	1	W
Operating Junction and Storage Temperature Range	T_J, T_{STG}	-55 To 150	$^\circ\text{C}$

Thermal Characteristic

Thermal Resistance, Junction-to-Ambient (Note 2)	$R_{\theta JA}$	125	$^\circ\text{C}/\text{W}$
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Electrical Characteristics ($T_A=25^\circ\text{C}$ unless otherwise noted)

Parameter	Symbol	Condition	Min	Typ	Max	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	BV_{DSS}	$\text{V}_{\text{GS}}=0\text{V}, \text{I}_{\text{D}}=-250\mu\text{A}$	-20	-24	-	V
Zero Gate Voltage Drain Current	I_{DSS}	$\text{V}_{\text{DS}}=-20\text{V}, \text{V}_{\text{GS}}=0\text{V}$	-	-	-1	μA
Parameter	Symbol	Condition	Min	Typ	Max	Unit
Gate-Body Leakage Current	I_{GSS}	$\text{V}_{\text{GS}}=\pm 12\text{V}, \text{V}_{\text{DS}}=0\text{V}$	-	-	± 100	nA
On Characteristics (Note 3)						
Gate Threshold Voltage	$\text{V}_{\text{GS(th)}}$	$\text{V}_{\text{DS}}=\text{V}_{\text{GS}}, \text{I}_{\text{D}}=-250\mu\text{A}$	-0.4	-0.7	-1	V
Drain-Source On-State Resistance	$\text{R}_{\text{DS(ON)}}$	$\text{V}_{\text{GS}}=-4.5\text{V}, \text{I}_{\text{D}}=-3\text{A}$	-	64	110	$\text{m}\Omega$
		$\text{V}_{\text{GS}}=-2.5\text{V}, \text{I}_{\text{D}}=-2\text{A}$	-	89	140	$\text{m}\Omega$
Forward Transconductance	g_{FS}	$\text{V}_{\text{DS}}=-5\text{V}, \text{I}_{\text{D}}=-2\text{A}$	5	-	-	S
Dynamic Characteristics (Note 4)						
Input Capacitance	C_{iss}	$\text{V}_{\text{DS}}=-10\text{V}, \text{V}_{\text{GS}}=0\text{V}, \text{F}=1.0\text{MHz}$	-	405	-	PF
Output Capacitance	C_{oss}		-	75	-	PF
Reverse Transfer Capacitance	C_{rss}		-	55	-	PF
Switching Characteristics (Note 4)						
Turn-on Delay Time	$\text{t}_{\text{d(on)}}$	$\text{V}_{\text{DD}}=-10\text{V}, \text{I}_{\text{D}}=-1\text{A}, \text{V}_{\text{GS}}=-4.5\text{V}, \text{R}_{\text{GEN}}=10\Omega$	-	11	-	nS
Turn-on Rise Time	t_r		-	35	-	nS
Turn-Off Delay Time	$\text{t}_{\text{d(off)}}$		-	30	-	nS
Turn-Off Fall Time	t_f		-	10	-	nS
Total Gate Charge	Q_{g}	$\text{V}_{\text{DS}}=-10\text{V}, \text{I}_{\text{D}}=-3\text{A}, \text{V}_{\text{GS}}=2.5\text{V}$	-	3.3	12	nC
Gate-Source Charge	Q_{gs}		-	0.7	-	nC
Gate-Drain Charge	Q_{gd}		-	1.3	-	nC
Drain-Source Diode Characteristics						
Diode Forward Voltage (Note 3)	V_{SD}	$\text{V}_{\text{GS}}=0\text{V}, \text{I}_{\text{s}}=1.3\text{A}$	-	-	-1.2	V
Diode Forward Current (Note 2)	I_{s}		-	-	-3	A

Notes:

1. Repetitive Rating: Pulse width limited by maximum junction temperature.
2. Surface Mounted on FR4 Board, $t \leq 10$ sec.
3. Pulse Test: Pulse Width $\leq 300\mu\text{s}$, Duty Cycle $\leq 2\%$.
4. Guaranteed by design, not subject to production

Typical Characteristics

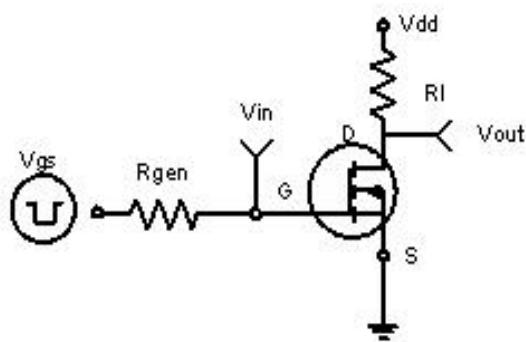


Figure 1:Switching Test Circuit

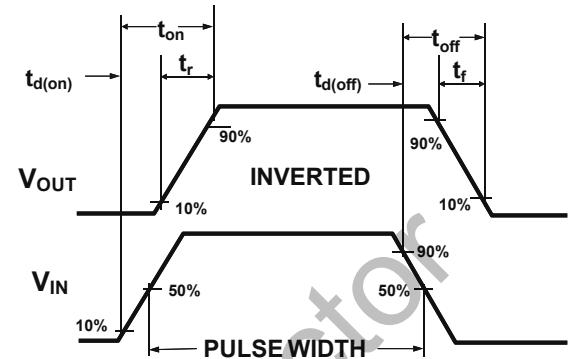


Figure 2:Switching Waveforms

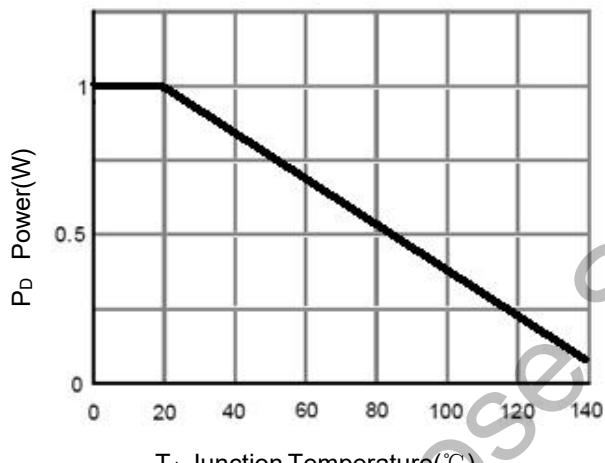


Figure 3 Power Dissipation

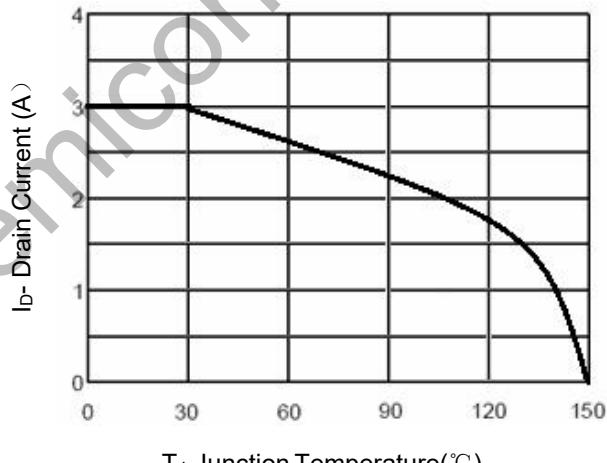


Figure 4 Drain Current

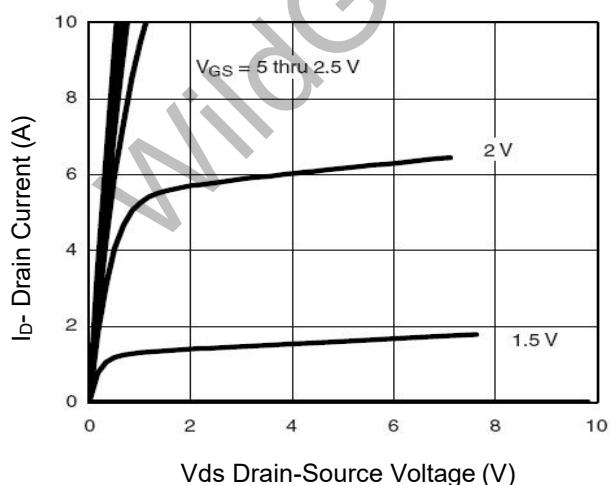


Figure 5 Output Characteristics

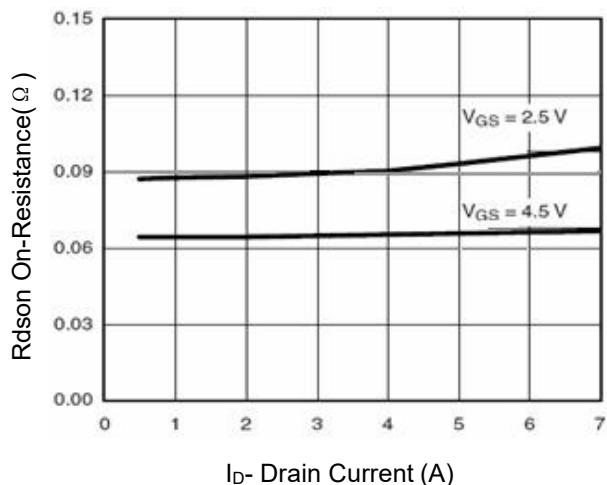
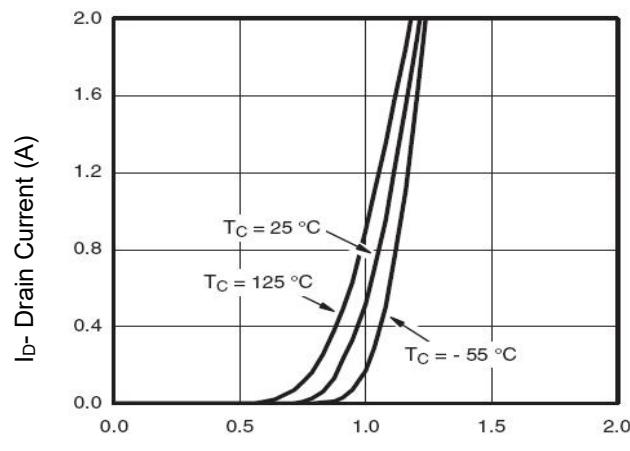
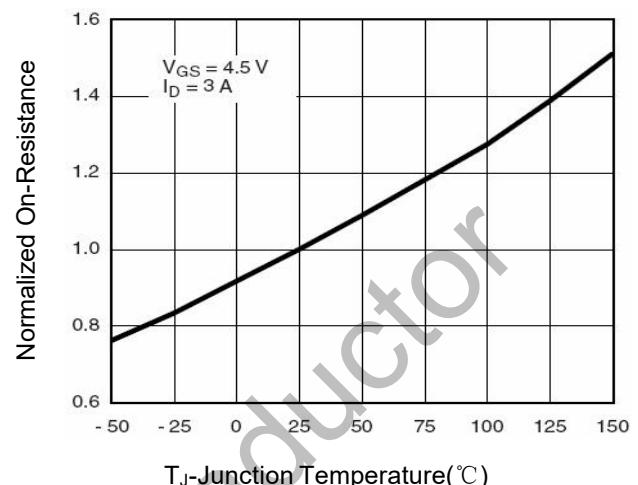
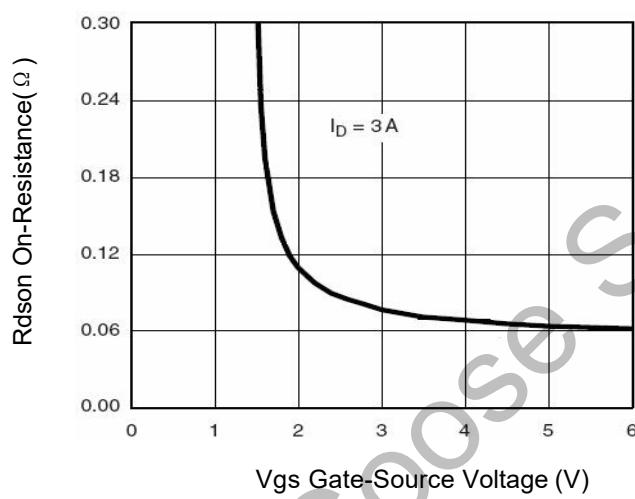
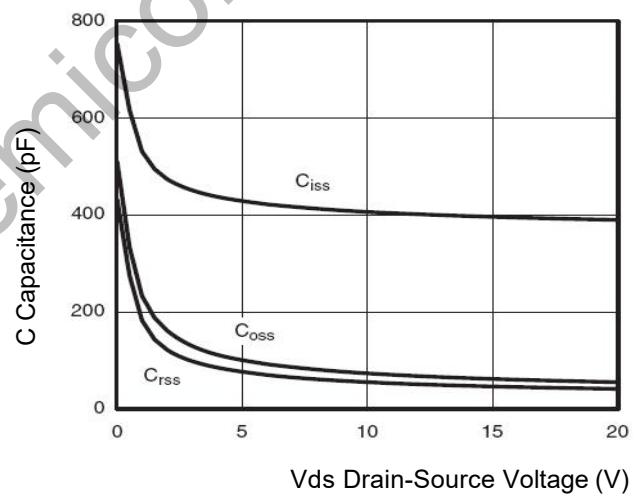
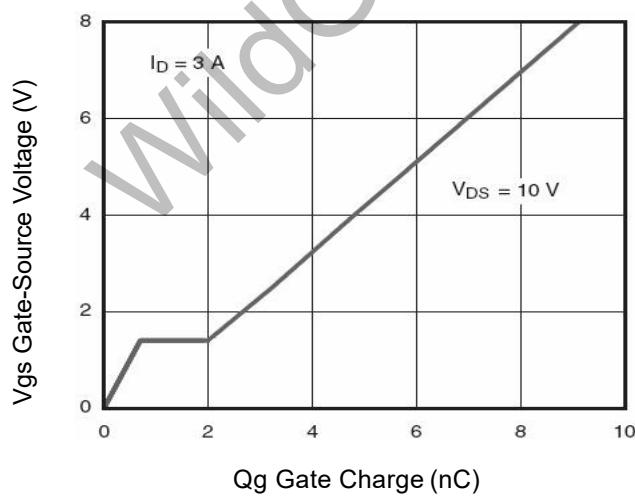
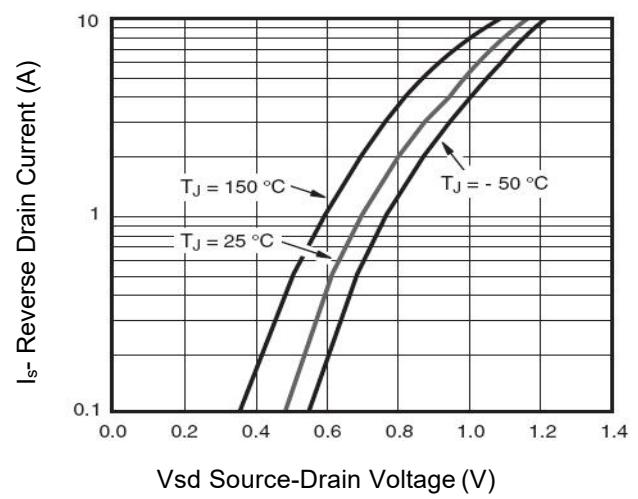
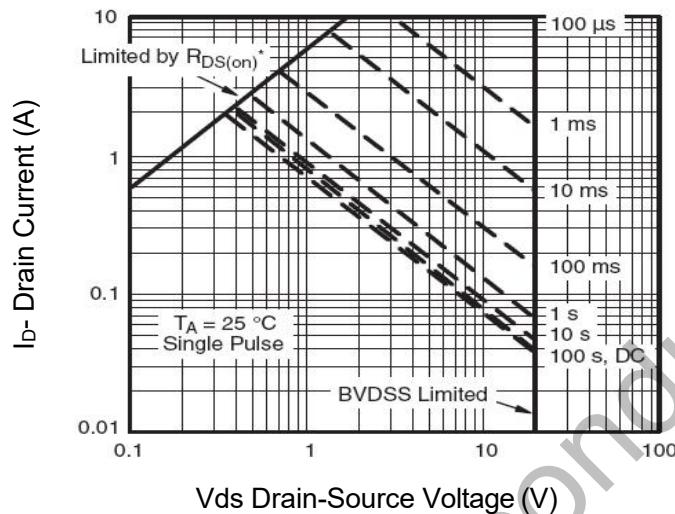
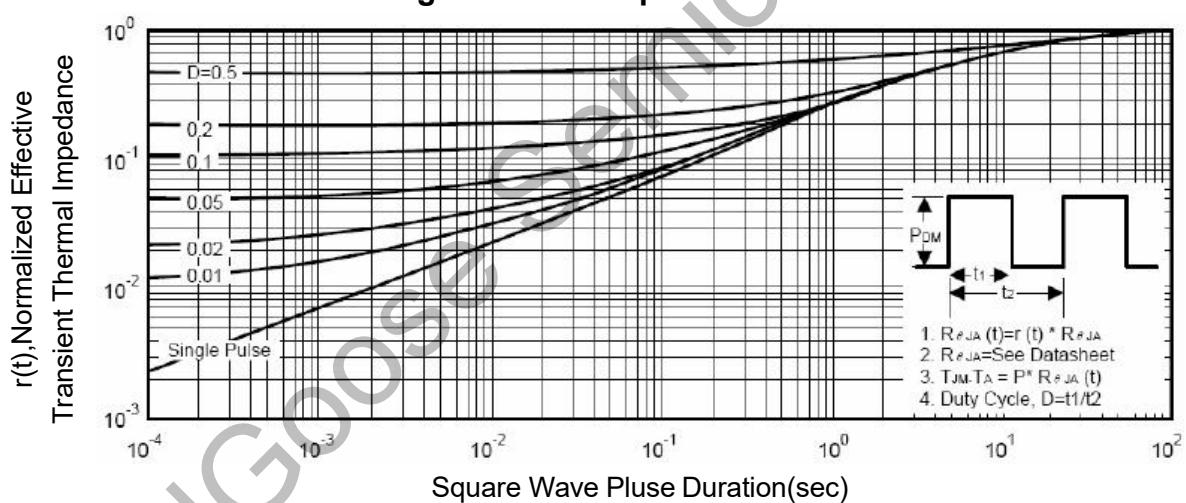


Figure 6 Drain-Source On-Resistance

Typical Characteristics (Continued)

**Figure 7 Transfer Characteristics****Figure 8 Drain-Source On-Resistance****Figure 9 $R_{DS(on)}$ vs V_{GS}** **Figure 10 Capacitance vs V_{DS}** **Figure 11 Gate Charge****Figure 12 Source-Drain Diode Forward**

Typical Characteristics (Continued)

**Figure 13 Safe Operation Area****Figure 14 Normalized Maximum Transient Thermal Impedance**