

Features

- Small body outline dimensions
- Low body height
- Stand off voltage: 2.5V, 3.3V, 5.0V, 6.0V, 7.0V, 12V
- Low leakage
- Response time is typically < 1ns
- Provide transient protection:
IEC 61000-4-2 (ESD) level 4
IEC 61000-4-4 (EFT) 40A (5/50ns)
IEC 61000-4-5 (Surge) (8/20us)
- Lead-free parts meet RoHS requirements.
- Compliant to Halogen-free.

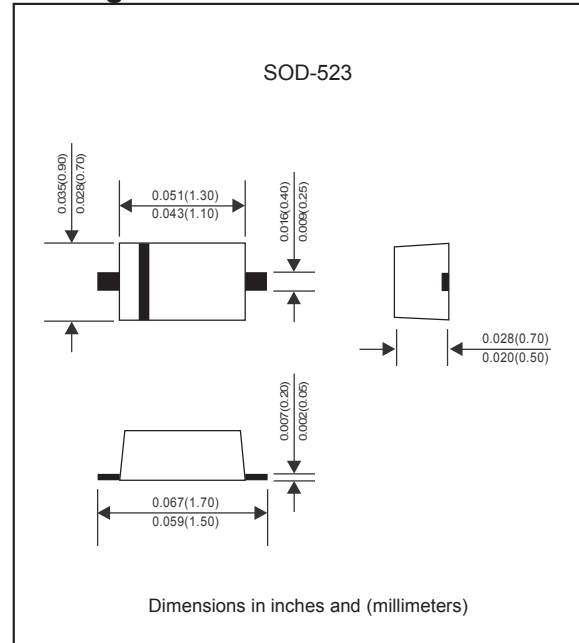
Applications

- Cellular phones
- Portable devices
- Digital cameras
- Power supplies

Mechanical data

- Epoxy: UL94-V0 rated flame retardant
- Case : Molded plastic, SOD-523
- Terminals : Solder plated, solderable per
MIL-STD-750, Method 2026
- Polarity : Indicated by cathode band
- Mounting Position : Any

Package outline



Maximum ratings (at T = 25°C unless otherwise noted)

PARAMETER	CONDITIONS	Symbol	Value	UNIT
Total power dissipation	Peak pulse power (tp = 8/20us)	P _{PP}	200	W
Electrostatic discharge	IEC61000-4-2 air discharge IEC61000-4-2 contact discharge	E _{SD}	±30 ±30	kV
ESD voltage	Per human body model	E _{SD}	16	kV
Electrostatic discharge	IEC61000-4-4	E _{FT}	40	A
Lead solder temperature-maximum	10 second duration	T _L	260	°C
Storage temperature range		T _{STG}	-55~+150	°C
Operating temperature range		T _{OP}	-55~+150	°C

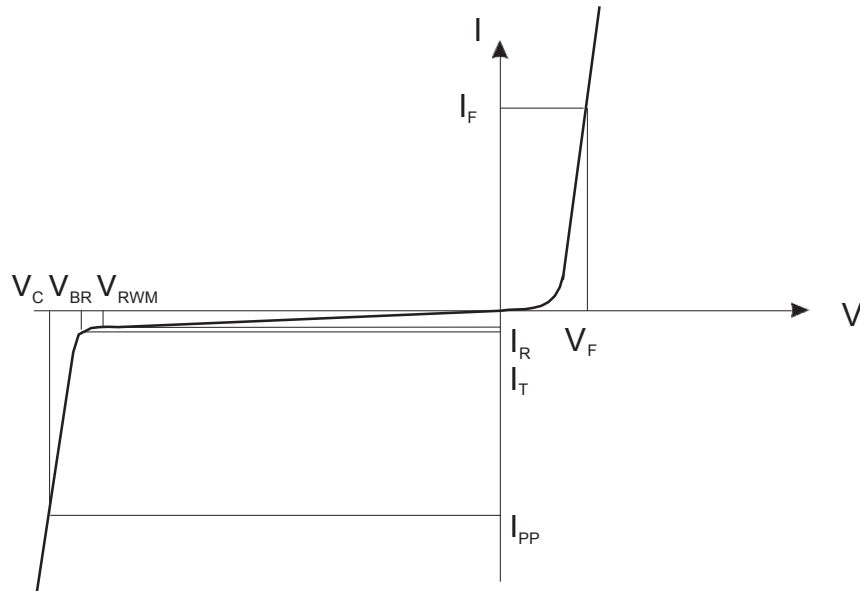
Electrical characteristics (at T = 25°C unless otherwise noted, V_F = 0.9V Max. @ I_F = 10mA for all types)

Part No.	V _{RWM} (V)	I _R (uA) @V _{RWM}	V _{BR} (V)@I _T (Note 2)	I _T (mA)	V _C (V)(Note 1) @ I _{PP} =5.0A	I _{PP} (A) (Note 1)	V _C (V)(Note 1) @Max I _{PP}	P _{PK} (W) (Note 1) Max.	C _J (pF) V _R =0V and f=1MHz Typ.
ESD5Z2V5	2.5	6.0	4.0	1.0	6.5	11.0	10.9	120	145
ESD5Z3V3	3.3	0.05	5.0	1.0	8.4	11.2	14.1	158	105
ESD5Z5V0	5.0	0.05	6.2	1.0	11.6	9.4	18.6	174	80
ESD5Z6V0	6.0	0.01	6.8	1.0	12.4	8.8	20.5	181	70
ESD5Z7V0	7.0	0.01	7.5	1.0	13.5	8.8	22.7	200	65
ESD5Z12V	12.0	0.01	13.5	1.0	17.0	9.6	25.0	240	55

Note 1. Surge current waveform per Fig.1

2. V_{BR} is measured with a pulse test current I_T at an ambient temperature of 25°C.

Typical characteristics (at $T = 25^{\circ}\text{C}$ unless otherwise noted)



Uni-Directional TVS

V_C : Clamping Voltage @ I_{PP}
 I_{PP} : Maximum Reverse Peak Pulse Current
 V_{RWM} : Maximum Working Peak Reverse voltage
 I_R : Maximum Reverse Leakage Current @ V_{RWM}
 V_{BR} : Breakdown voltage @ I_T
 I_T : Test Current
 I_F : Forward Current
 V_F : Forward Voltage @ I_F
 C_J : Capacitance @ $V_R = 0\text{V}$ and $f = 1\text{MHz}$

Rating and characteristic curves (ESD5Z SERIES)

FIG.1: Pulse waveform (8/20 μs)

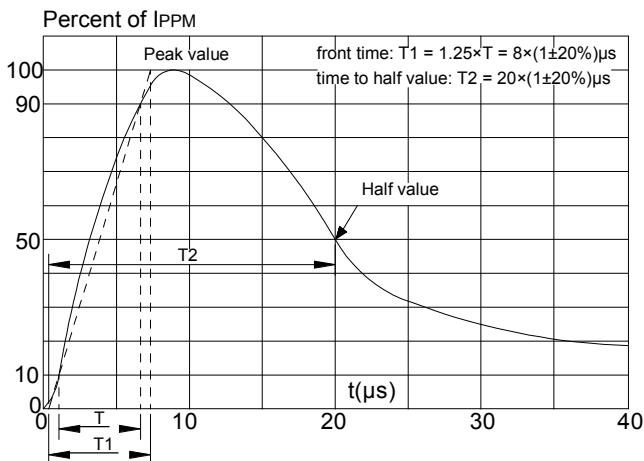
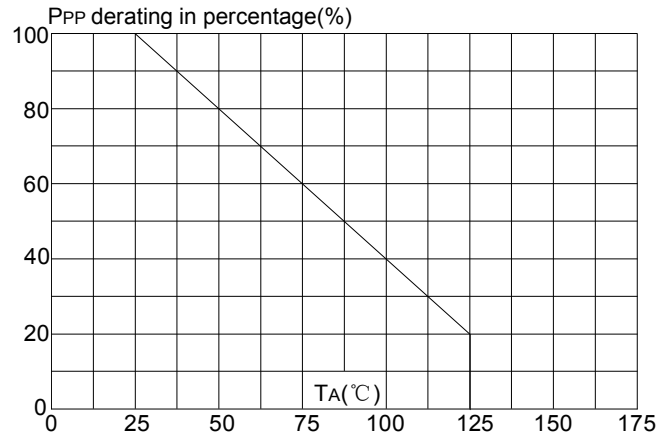
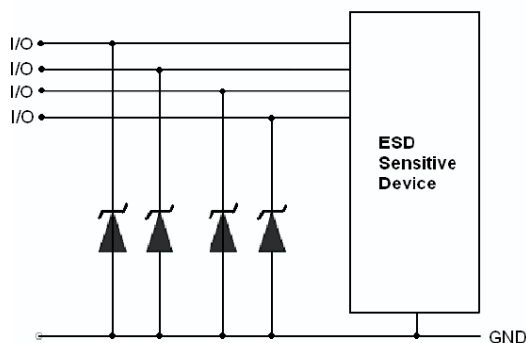


FIG.2: Pulse derating curve



Application Note

Electrostatic discharge (ESD) is a major cause of failure in electronic system. Transient Voltage Suppressors (TVS) are an ideal choice for ESD protection. They are capable of clamping the incoming transient to a low enough level such that damage to the protected semiconductor is prevented. Surface mount TVS offers the best choice for minimal lead inductance. They serve as parallel protection elements, connected between the signal lines to ground. As the transient rise above the operating voltage of the device, the TVS becomes a low impedance path diverting the transient current to ground. The ESD5Z Series is the ideal board level protection of ESD sensitive semiconductor components. The tiny SOD-523 package allows design flexibility in the design of high density boards where the space is at a premium. This enables to shorten the routing and contributes to hardening against ESD.



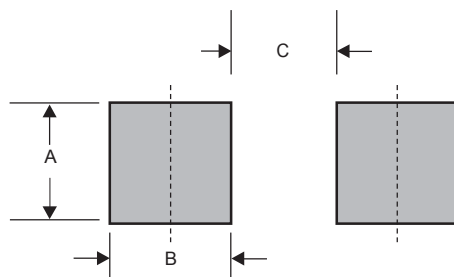
Pinning information

Pin	Simplified outline	Symbol
Pin1 cathode Pin2 anode	1  2	1  2

Marking

Type number	Marking code
ESD5Z2V5	ZD
ESD5Z3V3	ZE
ESD5Z5V0	ZF
ESD5Z6V0	ZG
ESD5Z7V0	ZH
ESD5Z12V	ZM

Suggested solder pad layout



Dimensions in inches and (millimeters)

PACKAGE	A	B	C
SOD-523	0.032 (0.80)	0.024 (0.60)	0.044 (1.10)

