

深圳市凯越翔电子有限公司

石英谐振器规格书

| 产品名称: | 石英晶振谐振器 |
|---------|-----------------|
| 产品型号: | 3215/32. 768KHZ |
| 产品参数: | 9PF/±20ppm |
| 原厂型号: | KFC327680920 |
| 凯越翔技术部: | 董宗全 |

| 客户确认印栏 | | | |
|---------|-------|--|--|
| 认 证 印 章 | 负责人印章 | | |
| 子当力。 | | | |
| 年 月 日 | 年 月 日 | | |

拟制 成望生 审核 董宗全 批准 谢为亮

本规格章程连同本页共9页

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INTRODUCTION

- 1. The contents is subject to change without notice.

 Please exchange the specification sheets regarding the product's warranty.
- 2. This sheet is not intended to guarantee or provide an approval of implementation of industrial patents.
- 3. We have prepared this sheet as carefully as possible. If you find it incomplete or unsatisfactory in any respect, We would welcome your comments.

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This product listed here is designed as components or parts for electronics equipment in general consumer use. We do not expect that any of these products would be incorporated or otherwise used as a component or part for the equipment, which requires an extra high reliability, such as satellite, rocket and other space systems, and medical equipment, the functional purpose of which is to keep life.

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[1] Absolute maximum ratings

| Item | Symbol | Rating value |
|---------------------|--------|-----------------|
| Storage temperature | Tstg | -55°C to +125°C |
| Maximum drive level | DL | 1.0 μW |

[2] Operating range

| | Symbol | Value | | |
|-----------------------------|--------|-------------|--------|-------|
| Item | | Min. | Тур. | Max. |
| Operating temperature range | Topr | -40°C | | +85°C |
| Drive level | DL | 0.01 μW | 0.1 μW | 0.5μW |
| Vibration mode | | Fundamental | | |

[3] Static characteristics

| Item | Symbol | Value | Note |
|-------------------------|--------|---|--|
| Frequency | fi | 32.768 kHz | |
| Frequency tolerance | Δ f/f | ±20 ×10 ⁻⁶ | CL = 9 pF , 12.5 pF Ta = $+25\pm3$ °C, Drive level : 0.1 μ W Not include aging |
| Series resistance | Rı | 70 k Ω Max. | |
| Motional capacitance | C1 | Typ. 3.4 fF | CI meter : Saunders 140B Drive level : 0.5 μW |
| Shunt capacitance | Co | Typ. 1.2 pF | |
| Turnover temperature | θТ | +25 ± 5 °C | Values are calculated by the frequencies at +10, +25, +40°C with C-MOS circuit. |
| Temperature coefficient | a | -4.0 × 10 ⁻⁸ /°C ² Max. | |
| Isolation resistance | IR | 500 MΩ Min. | DC 100V, 60 seconds Between terminal #1 and terminal #2 |
| Aging | fa | $\pm 3 \times 10^{-6}$ / year | Ta = +25 °C ± 3 °C Drive level : 0.1 μW |

[4] Environmental and Mechanical characteristics

| No. | Items | Value | Conditions | | |
|-----|-------------------------------|--|---|------------|--|
| 1 | Shock resistance | *3 Δ f/f : \pm 8 ×10 ⁻⁶ | 100g dummy(SEIKO EPSON Standard), Natural drop 500 mm height on to the concrete. | from | |
| | | | 3 directions × 10 times * | 2 | |
| 2 | Vibration resistance | *3 Δ f/f : \pm 3 ×10 ⁻⁶ | 10 Hz to 55 Hz amplitude 0.75 mm 55 Hz to 500 Hz acceleration 98 m/s ² 10 Hz \rightarrow 500 Hz \rightarrow 10 Hz 15 min./cycle 6 h (2 hours, 3 directions) | *2 | |
| 3 | Soldering heat resistance | $\Delta f/f$: $\pm 5 \times 10^{-6}$ | For convention reflow soldering furnace (2 time | s) | |
| 4 | High temperature storage | *3 Δ f/f : \pm 10 ×10 ⁻⁶ | +125 °C × 1000 h | *1 | |
| | | *3Δ f/f: ± 7 ×10 ⁻⁶ | +85°C× 1000 h | * 1 | |
| 5 | Low temperature storage | *3Δ f/f : ± 10 ×10 ⁻⁶ | -55 °C× 1000 h | *1 | |
| 6 | High temperature and humidity | *3 Δ f/f : $\pm 10 \times 10^{-6}$ | +85°C × 85%RH × 1000 h | #1 | |
| 7 | Temperature cycle | *3 Δ f/f : $\pm 10 \times 10^{-6}$ | -55 °C↔ +125°C 30 minutes at each temperature × 100 cycles | *1 | |
| 8 | Sealing | *3 1 × 10 ⁻⁸ hPa•1 / s Max. | For He leak detector | | |
| 9 | Shear | No peeling-off at a soldered part | 20 N press for 10 ± 1 s. Ref. IEC 60068-2-21 | | |
| 10 | Pull - off | No peeling-off at a soldered part | 20 N press for 10 ± 1 s. Ref. IEC 60068-2-21 | | |
| 11 | Substrate bending | No peeling-off at a soldered part | Bend width reaches 3 mm and hold for $5 \text{ s} \pm 1 \text{ s} \times 1$ time Ref. IEC 60068-2-21 | | |
| 12 | Solvent resistance | The marking shall be legible | Ref. JIS C 0052 or IEC 60068-2-45 | | |

< Notes >

- 1. *1 Each test done independently.
- 2. *2 Measuring 2 h to 24 h later leaving in room temperature after each test. Drive level : 0.5 μW
- 3. *3 Pre conditionings(Treat the Reflow 2 times with the following profile) Initial value shall be after 24 h at room temperature.

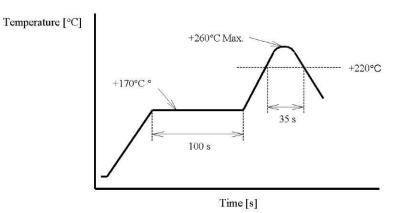
Shift of series resistance at before and after the test should be less than ± 20 % or less than $\pm 15 k\Omega$.

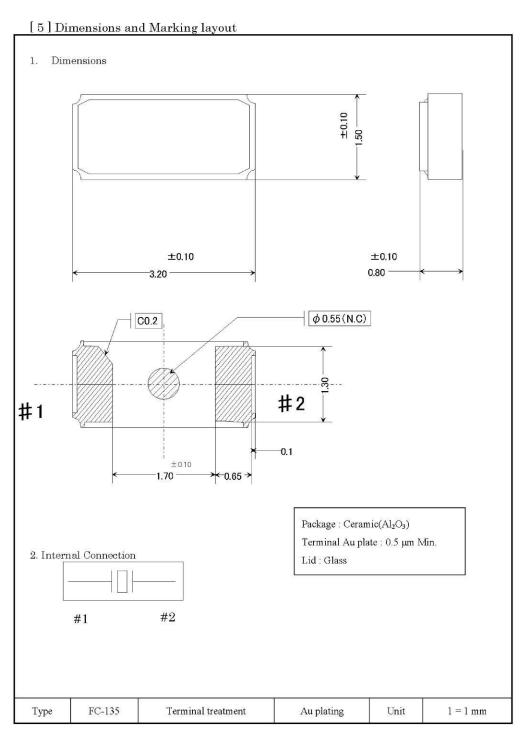
In case high temperature storage(+125°C \times 1 000 h), Soldering heat resistance, shift of series resistance at before and after the test should be less than ± 30 % or ± 20 k Ω .

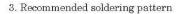
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♦ Air reflow

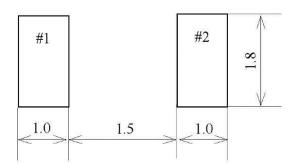
Pre heating temperature: +170 [°C] Heating temperature : +220 [°C] Pre heating time: 100 [s] Heating time : 30 [s]



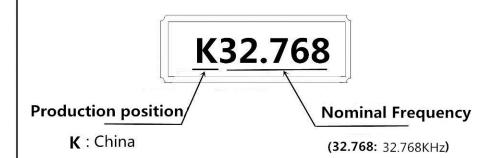




Unit: 1 = 1 mm



4. Marking layout



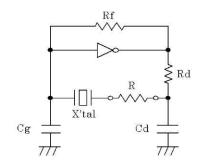
* The above marking layout shows only marking contents and their approximate position and it is not for font, size and exact position.

Type FC-135 Unit 1=1 mm

[6] Notes

- Max two (2) times reflow is allowed. Once miss soldering is happened, hand work soldering by soldering iron is recommended. (+350°C x within 5 s)
- 2. Patterning should be followed by our recommended one.
- 3. Applying excessive excitation force to the crystal resonator may cause deterioration damage.
- Unless adequate negative resistance is allocated in the oscillation circuit, start up time of oscillation may be increased, or no oscillation may occur.

How to check the negative resistance.



- (1) Connect the resistance (R) to the circuit in series with the crystal resonator.
- (2) Adjust R so that oscillation can start (or stop).
- (3) Measure R when oscillation just start (or stop) in above (2).
- (4) Get the negative resistance

$$-R = R + CI$$
 value.

(5) Recommended -R

$$|-R| > CI \times (5 \sim 10)$$

- The shortest patterning line on board is recommendable.
 Too long line on board may cause of abnormal oscillation.
- To avoid mull function, no pattern under or near the crystal is allowed. Solder paste should be more than 150 μm thickness.
- 7. This device must be stored at the normal temperature and humidity conditions before mounting on a board.
- Too much exciting shock or vibration may cause deterioration on damage.
 Depending on the condition such as a shock in assembly machinery, the products may be damaged.
 Please check your condition in advance to maintain shock level to be smallest.
- 9. Depending on the conditions, ultrasonic cleaning may cause resonant damage of the internal crystal resonator. Since we are unable to determine the conditions (type of cleaning unit, power, time, conditions inside the bath, etc.) to be used in your company, we cannot guarantee the safety of this unit when it is cleaned in an ultrasonic cleaner.
- 10. Ink marking may be damaged by some kind of solvent, please take precautions when choosing solvent by your selves.
- 11. Please refer to packing specification regarding how to storage the products in the pack.