

- ★ Green Device Available
- ★ Super Low Gate Charge
- ★ Excellent  $CdV/dt$  effect decline
- ★ Advanced high cell density Trench technology
- ★ 100% EAS Guaranteed

## Product Summary

RoHS

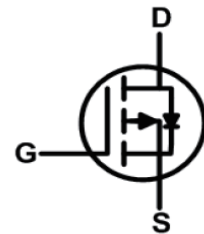
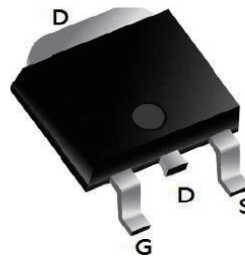
BVDSS	RDS(on)	ID
-60V	100mΩ	-10A

## Description

The 10P06 is the high cell density trench N-ch MOSFETs, which provide excellent  $R_{DS(on)}$  and gate charge for most of the synchronous buck converter applications.

The 10P06 meet the RoHS and Green Product, requirement 100% EAS guaranteed with full function reliability approved.

## TO252 Pin Configuration



## Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
$V_{DS}$	Drain-Source Voltage	-60	V
$V_{GS}$	Gate-Source Voltage	$\pm 20$	V
$I_D@T_C=25^\circ C$	Continuous Drain Current, $V_{GS} @ -10V^1$	-10	A
$I_D@T_C=100^\circ C$	Continuous Drain Current, $V_{GS} @ -10V^1$	-7.8	A
$I_D@T_A=25^\circ C$	Continuous Drain Current, $V_{GS} @ -10V^1$	-3.5	A
$I_D@T_A=70^\circ C$	Continuous Drain Current, $V_{GS} @ -10V^1$	-2.8	A
$I_{DM}$	Pulsed Drain Current <sup>2</sup>	-25	A
EAS	Single Pulse Avalanche Energy <sup>3</sup>	20	mJ
$I_{AS}$	Avalanche Current	-20	A
$P_D@T_C=25^\circ C$	Total Power Dissipation <sup>4</sup>	25	W
$P_D@T_A=25^\circ C$	Total Power Dissipation <sup>4</sup>	2	W
$T_{STG}$	Storage Temperature Range	-55 to 150	$^\circ C$
$T_J$	Operating Junction Temperature Range	-55 to 150	$^\circ C$

## Thermal Data

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction-Ambient <sup>1</sup>	---	62	$^\circ C/W$
$R_{\theta JC}$	Thermal Resistance Junction-Case <sup>1</sup>	---	5	$^\circ C/W$

Electrical Characteristics ( $T_J = 25^\circ\text{C}$  unless otherwise specified)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$BV_{DSS}$	Drain-Source Breakdown Voltage	$V_{GS}=0V$ , $I_D=-250\mu A$	-60	---	---	V
$\Delta BV_{DSS} / \Delta T_J$	$BV_{DSS}$ Temperature Coefficient	Reference to $25^\circ\text{C}$ , $I_D=-1mA$	---	-0.049	---	$V/^\circ\text{C}$
$R_{DS(ON)}$	Static Drain-Source On-Resistance <sup>2</sup>	$V_{GS}=-10V$ , $I_D=-8A$	---	100	140	$m\Omega$
		$V_{GS}=-4.5V$ , $I_D=-6A$	---	115	190	
$V_{GS(th)}$	Gate Threshold Voltage	$V_{GS}=V_{DS}$ , $I_D=-250\mu A$	-1	---	-2.5	V
$\Delta V_{GS(th)}$	$V_{GS(th)}$ Temperature Coefficient		---	5.42	---	$mV/^\circ\text{C}$
$I_{DSS}$	Drain-Source Leakage Current	$V_{DS}=-48V$ , $V_{GS}=0V$ , $T_J=25^\circ\text{C}$	---	---	1	$\mu A$
		$V_{DS}=-48V$ , $V_{GS}=0V$ , $T_J=150^\circ\text{C}$	---	---	5	
$I_{GSS}$	Gate-Source Leakage Current	$V_{GS}=\pm 20V$ , $V_{DS}=0V$	---	---	$\pm 100$	nA
$g_{fs}$	Forward Transconductance	$V_{DS}=-5V$ , $I_D=-5A$	---	5.8	---	S
$Q_g$	Total Gate Charge (-4.5V)	$V_{DS}=-20V$ , $V_{GS}=-4.5V$ , $I_D=-5A$	---	5.85	---	nC
$Q_{gs}$	Gate-Source Charge		---	2.9	---	
$Q_{gd}$	Gate-Drain Charge		---	1.8	---	
$T_{d(on)}$	Turn-On Delay Time	$V_{DD}=-12V$ , $V_{GS}=-10V$ , $R_G=3.3\Omega$ , $I_D=-5A$	---	10	---	ns
$T_r$	Rise Time		---	17	---	
$T_{d(off)}$	Turn-Off Delay Time		---	22	---	
$T_f$	Fall Time		---	21	---	
$C_{iss}$	Input Capacitance	$V_{DS}=-15V$ , $V_{GS}=0V$ , $F=1MHz$	---	715	---	pF
$C_{oss}$	Output Capacitance		---	51	---	
$C_{rss}$	Reverse Transfer Capacitance		---	34	---	

## Diode Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$I_S$	Continuous Source Current <sup>1,5</sup>	$V_G=V_D=0V$ , Force Current	---	---	-9.5	A
$I_{SM}$	Pulsed Source Current <sup>2,5</sup>		---	---	-24	A
$V_{SD}$	Diode Forward Voltage <sup>2</sup>	$V_{GS}=0V$ , $I_S=-1A$ , $T_J=25^\circ\text{C}$	---	---	-1.2	V
$t_{rr}$	Reverse Recovery Time	$I_F=-8A$ , $dI/dt=100A/\mu s$ , $T_J=25^\circ\text{C}$	---	10.2	---	nS
$Q_{rr}$	Reverse Recovery Charge		---	5.4	---	nC

## Note :

- The data tested by surface mounted on a 1 inch<sup>2</sup> FR-4 board with 20Z copper.
- The data tested by pulsed, pulse width  $\leq 300\mu s$ , duty cycle  $\leq 2\%$
- The EAS data shows Max. rating. The test condition is  $V_{DD}=-25V$ ,  $V_{GS}=-10V$ ,  $L=0.1mH$ ,  $I_{AS}=-15A$
- The power dissipation is limited by  $150^\circ\text{C}$  junction temperature
- The data is theoretically the same as  $I_D$  and  $I_{DM}$ , in real applications, should be limited by total power dissipation.

## P-Channel Typical Characteristics

Figure1: Output Characteristics

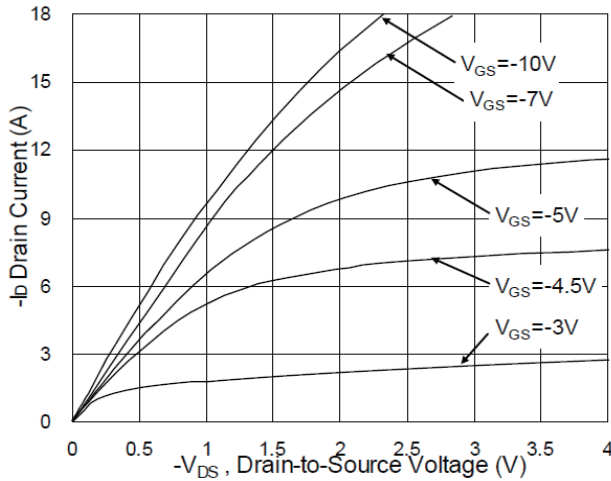


Figure 2: On-Resistance vs. G-S Voltage

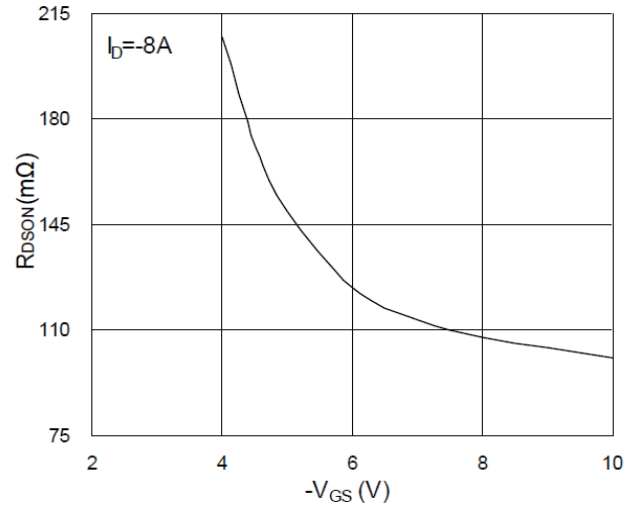


Figure 3: Forward Characteristics Of Reverse

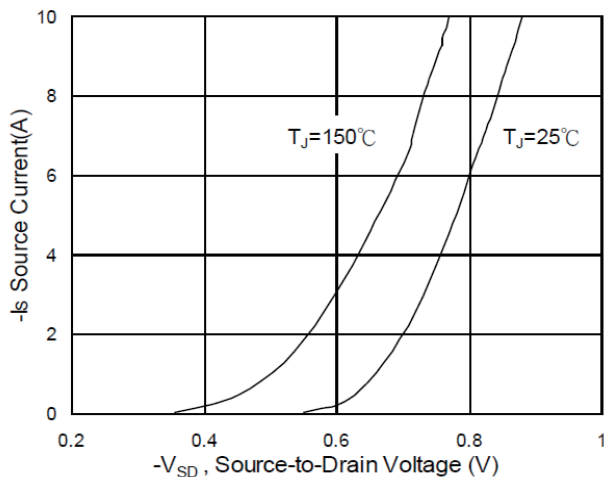


Figure 4: Gate-Charge Characteristics

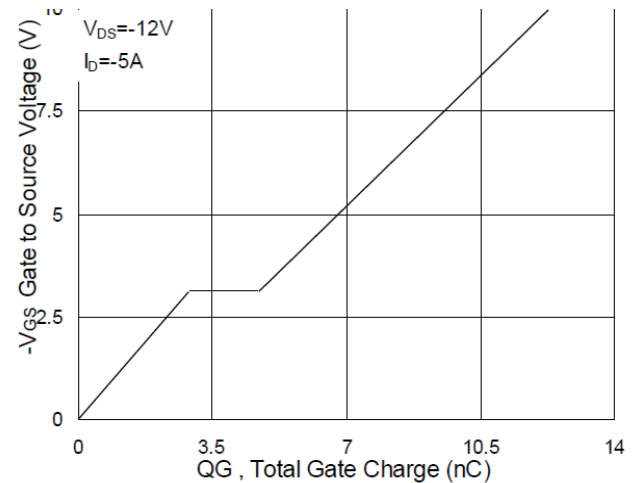


Figure 5: Normalized VGS(th) vs. TJ

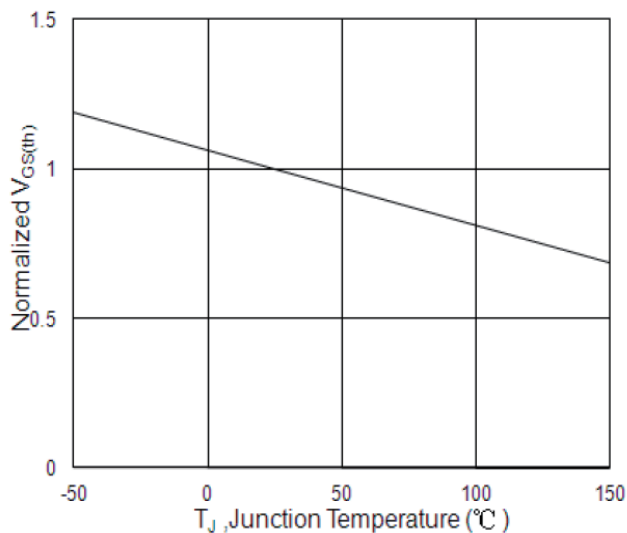
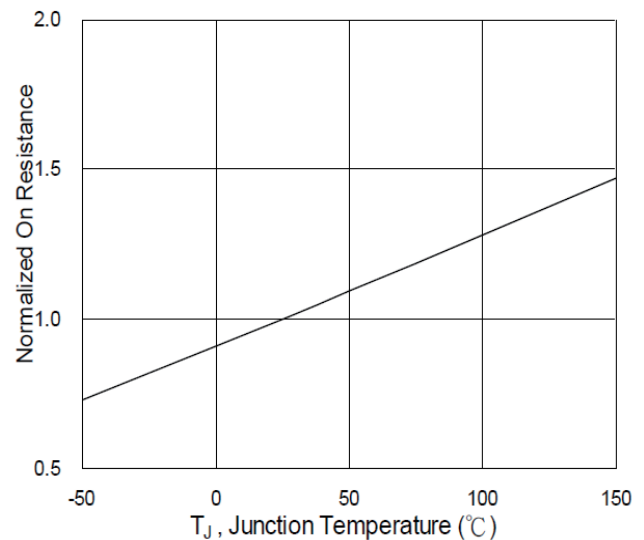


Figure 6: Normalized RDS(on) vs. TJ



## Typical Performance Characteristics

Figure 7: Capacitance

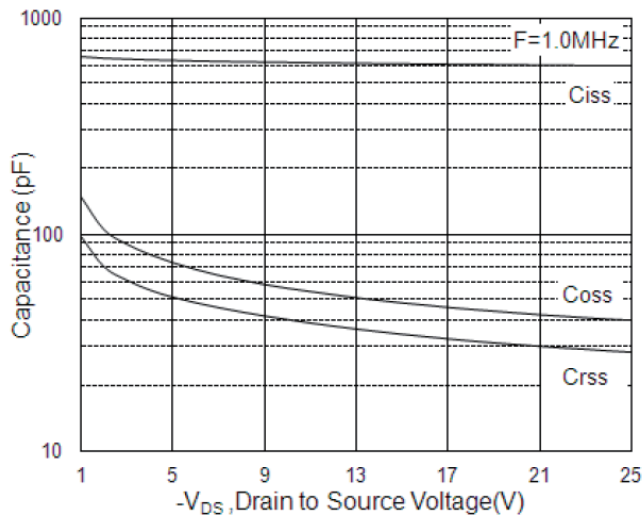


Figure 8: Safe Operating Area

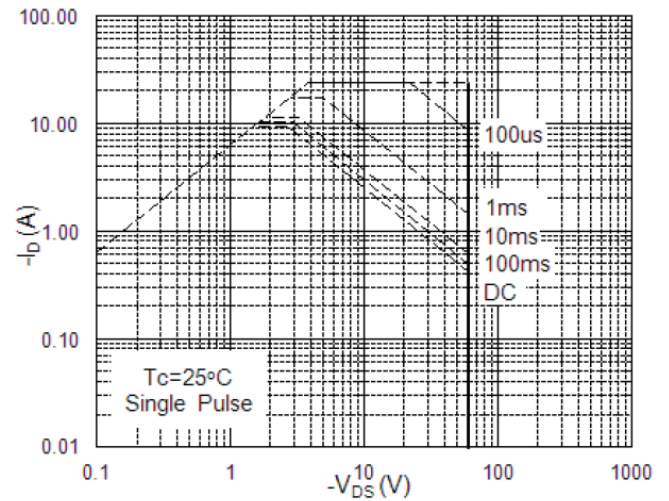


Figure 9: Normalized Maximum Transient

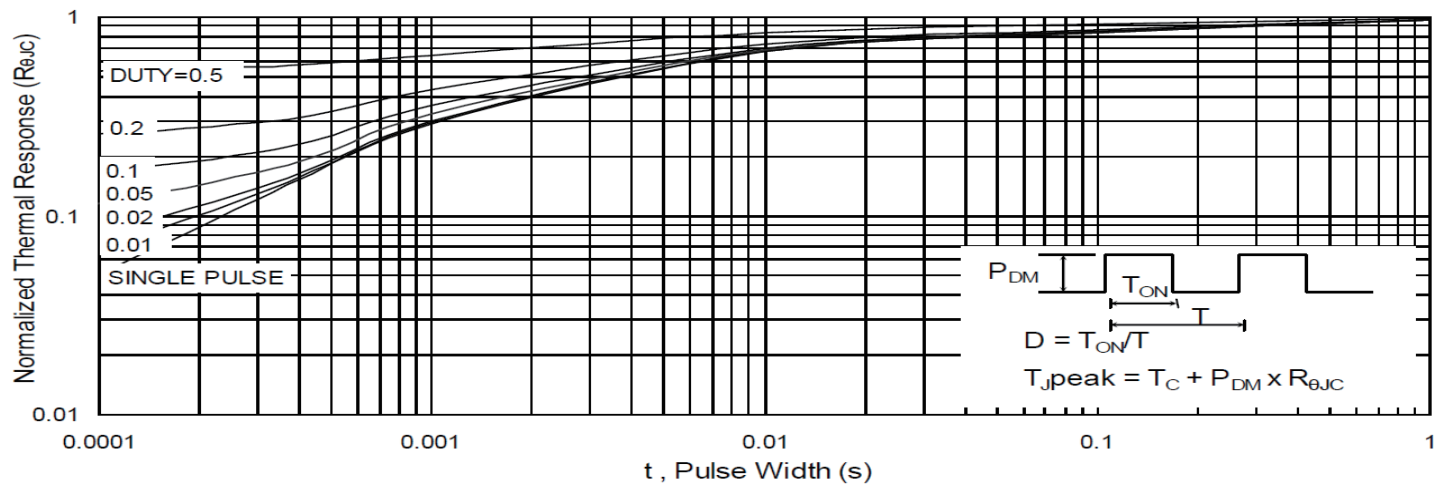


Figure 11: Switching Time Waveform

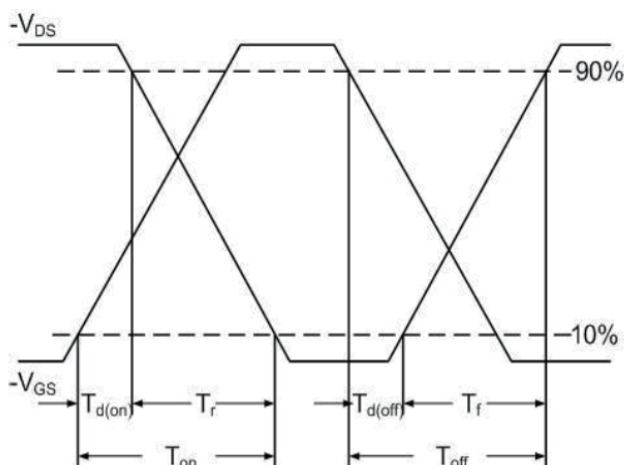
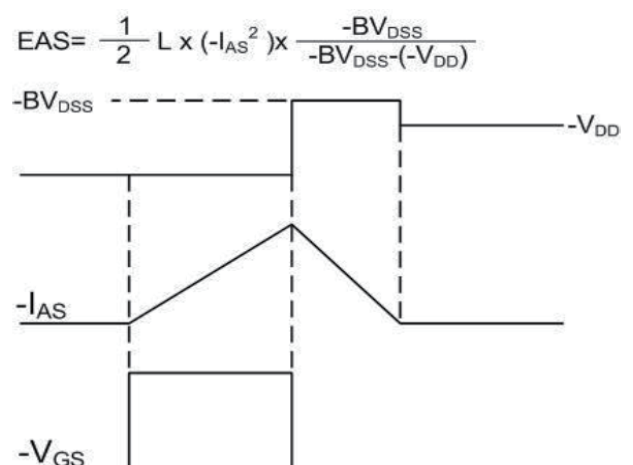
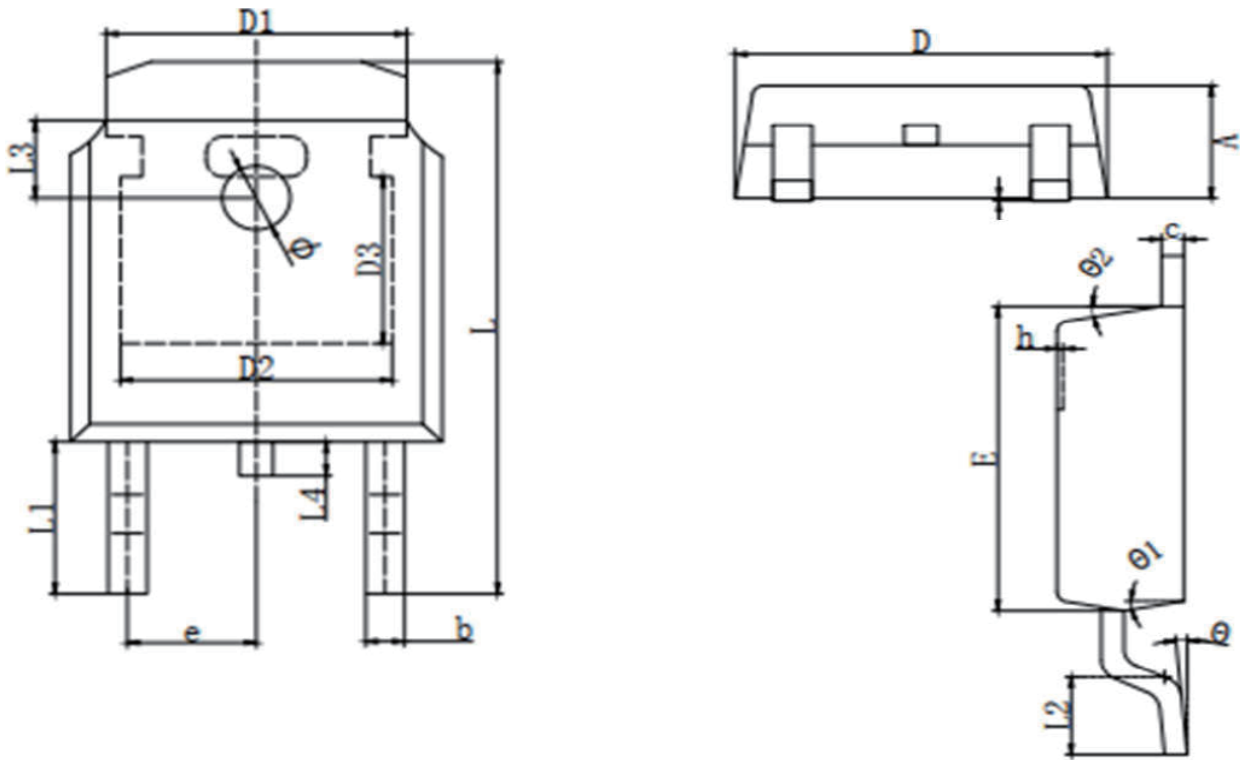


Figure 10: Unclamped Inductive Switching



## TO-252 Package outline



Symbol	MILLMETER		Symbol	MILLMETER	
	MIN	MAX		MIN	MAX
A	2.200	2.400	h	0.000	0.200
A1	0.000	0.127	L	9.900	10.30
b	0.640	0.740	L1	2.888REF	
c	0.460	0.580	L2	1.400	1.700
D	6.500	6.700	L3	1.600REF	
D1	5.334REF		L4	0.600	1.000
D2	4.826REF		Ø	1.100	1.300
D3	3.166REF		θ	0°	8°
E	6.00	6.200	θ <sub>1</sub>	9° TYP2	
e	2.286TYP		θ <sub>2</sub>	9° TYP	